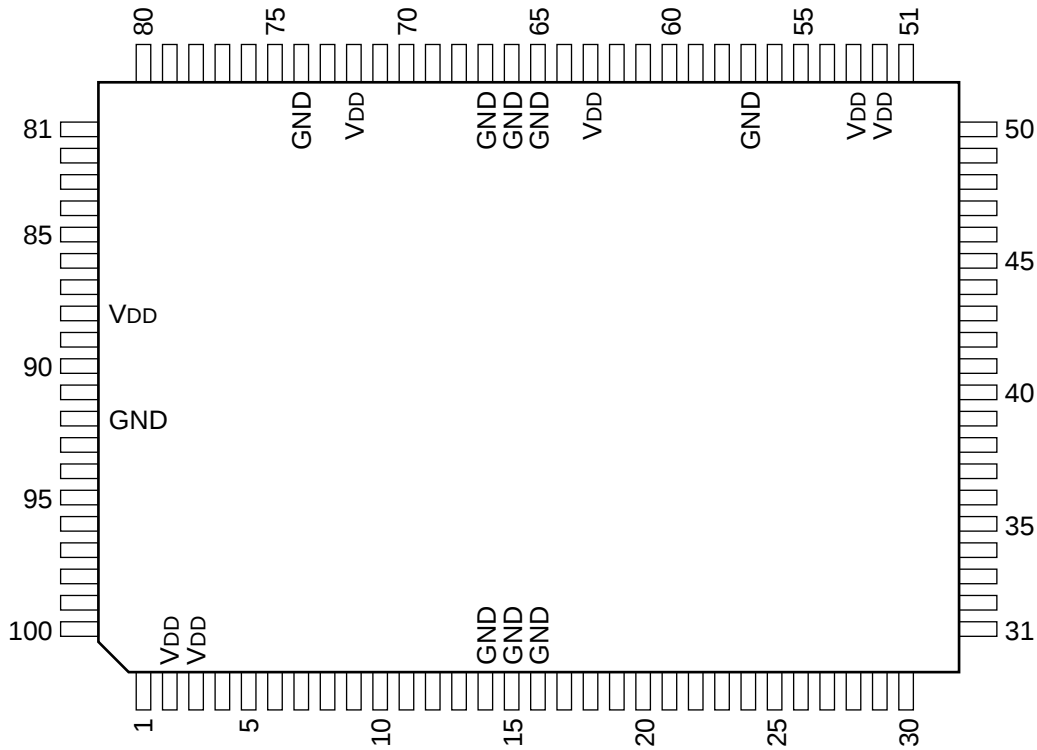


C-MOS ERROR DETECTION AND HANDLING

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	TEST0	21	I	IDATA8	41	I	TEST6	61	O	ODATA8	81	O	APERR
2	—	VDD	22	I	IDATA9	42	I	A3	62	O	ODATA7	82	O	FFERR
3	—	VDD	23	I	FORM0	43	I	A2	63	—	VDD	83	O	ANCER
4	O	TEST1	24	I	FORM1	44	I	A1	64	O	ODATA6	84	I/O	ANCEDH
5	O	TEST2	25	I	AUTO	45	I	A0	65	—	GND	85	I/O	ANCEDA
6	I	TEST3	26	I	MODE0	46	I	WRITE	66	—	GND	86	I/O	ANCIDH
7	I	TEST4	27	I	MODE1	47	I	READ	67	—	GND	87	I/O	ANCIDA
8	O	TEST5	28	I	MODE2	48	I	CS	68	O	ODATA5	88	—	VDD
9	I	ICLOCK	29	O	OTHANC	49	O	TEST7	69	O	ODATA4	89	I/O	ANCUES
10	I	IDATA0	30	O	EDHVLD	50	I	TEST8	70	O	ODATA3	90	I/O	APEDH
11	I	IDATA1	31	O	DPR	51	I	RESET	71	O	ODATA2	91	I/O	APEDA
12	I	IDATA2	32	O	INTR	52	—	VDD	72	—	VDD	92	—	GND
13	I	IDATA3	33	I/O	D7	53	—	VDD	73	O	ODATA1	93	I/O	APIDH
14	—	GND	34	I/O	D6	54	O	TEST9	74	—	GND	94	I/O	APIDA
15	—	GND	35	I/O	D5	55	O	TEST10	75	O	ODATA0	95	I/O	APUES
16	—	GND	36	I/O	D4	56	O	TEST11	76	O	TEST13	96	I/O	FFEDH
17	I	IDATA4	37	I/O	D3	57	—	GND	77	O	OD1	97	I/O	FFEDA
18	I	IDATA5	38	I/O	D2	58	O	TEST12	78	O	OL525	98	I/O	FFIDH
19	I	IDATA6	39	I/O	D1	59	O	OCLOCK	79	I	TEST14	99	I/O	FFIDA
20	I	IDATA7	40	I/O	D0	60	O	ODATA9	80	I	TEST15	100	I/O	FFUES

INPUT

A0 - A3	: ADDRESS
AUTO	: AUTO DETECT
$\overline{\text{CS}}$	: CHIP SELECT
FORM0, FORM1	: VIDEO FORMAT
ICLOCK	: CLOCK
IDATA0 - IDATA9	: VIDEO DATA
MODE0	: 1 = Detect each FLAG and calculate CRC. Add the result and output. 0 = Input each FLAG from outside and calculate CRC. Add the result and output.
MODE1	: 1 = If other ANCILLARY DATA existe EDH, such as AUDIO, no EDH is added. 0 = EDH is added unconditionally.
MODE2	: MODE SELECT
$\overline{\text{READ}}$	: READ
$\overline{\text{RESET}}$	: RESET
TEST3, TEST4, TEST6, TEST8, TEST14, TEST15	: TEST
$\overline{\text{WRITE}}$	: WRITE

OUTPUT

ANCER	: ANCILLARY DATA ERROR
APERR	: ACTIVE PICTURE ERROR
DPR	: INPUT SIGNAL DETECT
EDHVLB	: ERROR DETECTION & HANDLING CORRESPOND DETECT
FFERR	: FULL FIELD ERROR
INTR	: INTERRUPT
OCLOCK	: OUTPUT CLOCK
OD1	: D1/D2 FORMAT SELECT
ODATA0 - ODATA9	: OUTPUT DATA
OL525	: 525/625, 422/4FC FORMAT SELECT
OTHANC	: OTHER ANC DATA DETECT
TEST0 - TEST2, TEST5, TEST7, TEST9 - TEST13	: TEST

INPUT/OUTPUT

ANCEDA	: ANCILLARY EDA FLAG
ANCEDH	: ANCILLARY EDH FLAG
ANCIDA	: ANCILLARY IDA FLAG
ANCIDH	: ANCILLARY IDH FLAG
ANCUES	: ANCILLARY UES FLAG
APEDA	: ACTIVE PICTURE EDA FLAG
APEDH	: ACTIVE PICTURE EDH FLAG
APIDA	: ACTIVE PICTURE IDA FLAG
APIDH	: ACTIVE PICTURE IDH FLAG
APUES	: ACTIVE PICTURE UES FLAG
D0 - D7	: CPU DATA
FFEDA	: FULL FIELD EDA FLAG
FFEDH	: FULL FIELD EDH FLAG
FFIDA	: FULL FIELD IDA FLAG
FFIDH	: FULL FIELD IDH FLAG
FFUES	: FULL FIELD UES FLAG

