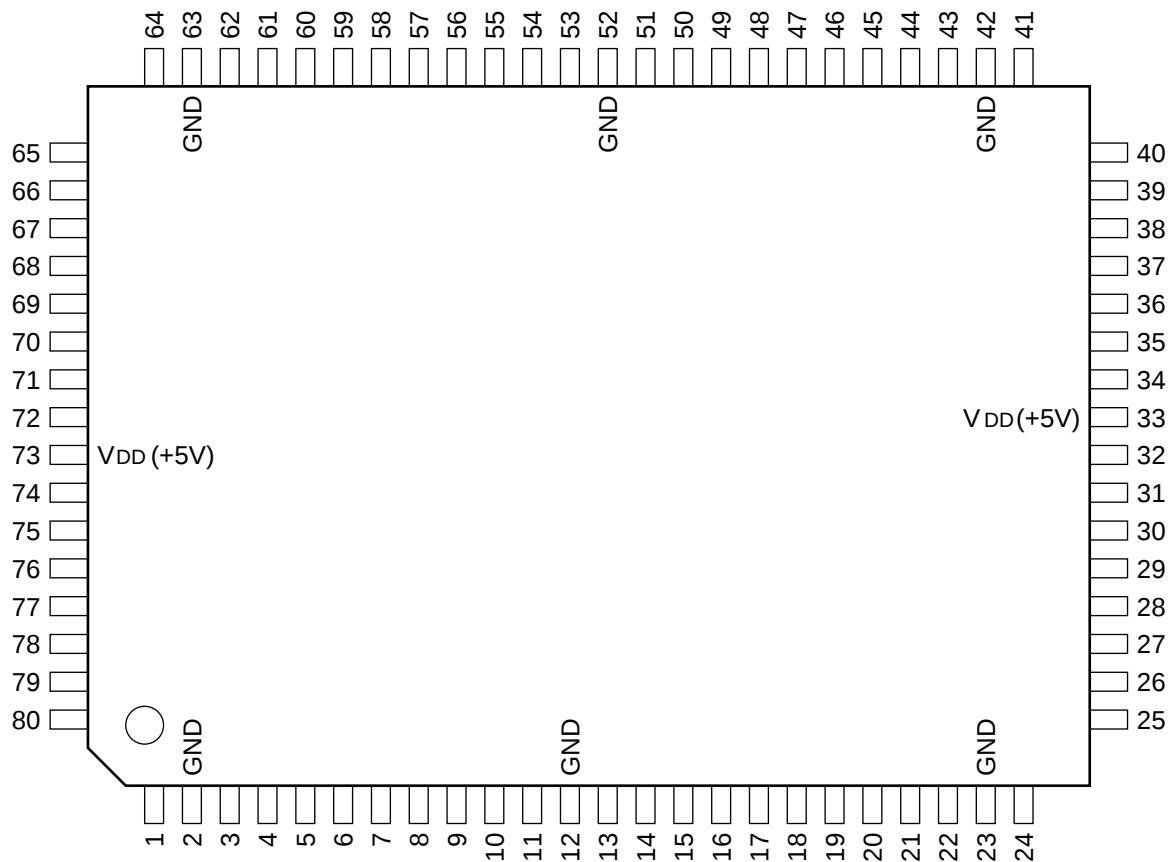

C-MOS COLOR CORRECT, CHROMA KEY AND LUMINANCE KEY GENERATOR
- TOP VIEW -



(VDD =+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	FMOD	21	O	YQ3	41	O	UVQ1	61	I	D7
2	-	GND	22	O	YQ4	42	-	GND	62	O	LST
3	O	KQ0	23	-	GND	43	O	UVQ2	63	-	GND
4	I	YSL0	24	O	YQ5	44	O	UVQ3	64	I	A0
5	I	YSL1	25	O	YQ6	45	I	LY0	65	I	A1
6	I	UVD0	26	O	YQ7	46	I	LY1	66	I	SC0
7	I	UVD1	27	I	YD0	47	I	LUV0	67	I	SC1
8	I	UVD2	28	I	YD1	48	I	LUV1	68	I	WE0
9	I	UVD3	29	I	YD2	49	I	D0	69	I	WE1
10	O	KQ1	30	I	YD3	50	O	UVQ4	70	I	WE2
11	O	KQ2	31	I	YD4	51	O	UVQ5	71	I	WE3
12	-	GND	32	I	YD5	52	-	GND	72	I	WE4
13	O	KQ3	33	-	VDD	53	O	UVQ6	73	-	VDD
14	O	YQ0	34	I	YD6	54	O	UVQ7	74	I	TSL
15	O	YQ1	35	I	YD7	55	I	D1	75	I	CK
16	I	UVD4	36	I	KD	56	I	D2	76	I	LK0
17	I	UVD5	37	I	KM	57	I	D3	77	I	LK1
18	I	UVD6	38	I	CCON	58	I	D4	78	I	LK2
19	I	UVD7	39	I	KINV	59	I	D5	79	I	LK3
20	O	YQ2	40	O	UVQ0	60	I	D6	80	I	TST

1	FMOD	KQ0	3	
4	YSL0	KQ1	10	
5	YSL1	KQ2	11	
6	UVD0	KQ3	13	
7	UVD1			
8	UVD2	YQ0	14	
9	UVD3	YQ1	15	
16	UVD4	YQ2	20	
17	UVD5	YQ3	21	
18	UVD6	YQ4	22	
19	UVD7	YQ5	24	
27	YD0	YQ6	25	
28	YD1	YQ7	26	
29	YD2			
30	YD3	UVQ0	40	
31	YD4	UVQ1	41	
32	YD5	UVQ2	43	
34	YD6	UVQ3	44	
35	YD7	UVQ4	50	
36	KD	UVQ5	51	
37	KM	UVQ6	53	
38	CCON	UVQ7	54	
39	KINV			
45	LY0	LST	62	
46	LY1			
47	LUV0			
48	LUV1			
55	D1			
56	D2			
57	D3			
58	D4			
59	D5			
60	D6			
61	D7			
64	A0			
65	A1			
66	SC0			
67	SC1			
68	WE0			
69	WE1			
70	WE2			
71	WE3			
72	WE4			
74	TSL			
75	CK			
76	LK0			
77	LK1			
78	LK2			
79	LK3			
80	TST			

INPUT

A0, A1	; ADDRESS
CCON	; COLOR CORRECTOR ON
CK	; CLOCK
D0-D7	; DATA
FMOD	; FREQUENCY MODE SELECT (L ; 1f, H ; 2f)
KD	; KEY
KINV	; KEY INVERTER
KM	; KEY MASK
LK0-LK3	; KEY DELAY
LUV0, LUV1	; UV DELAY
LY0, LY1	; Y (LUMINANCE SIGNAL) DELAY
SC0, SC1	; UV DATA CYCLE
TST	; ERROR DETECT DATA SELECT
UVD0-UVD7	; UV DATA
WE0-WE4	; WRITE ENABLE
YD0-YD7	; Y (LUMINANCE SIGNAL)
YSL0, YSL1	; Y SELECT

OUTPUT

KQ0-KQ3	; KEY
LST	; LAST OF UV DATA
UVQ0-UVQ7	; UV DATA
YQ0-YQ7	; Y (LUMINANCE SIGNAL)