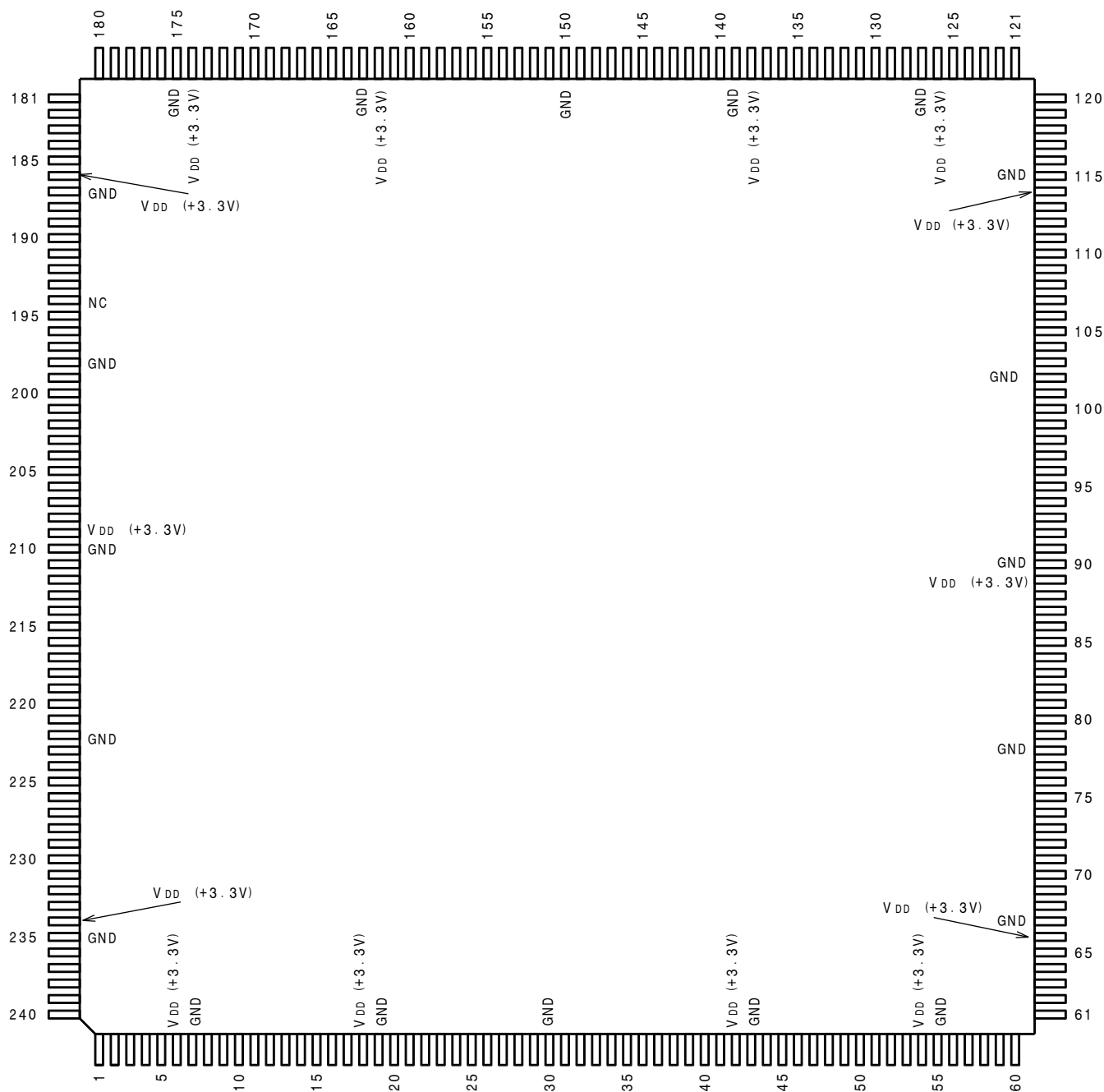

C-MOS PROGRAM PLAY PROCESSOR

-TOP VIEW-



(V_{DD} = +3.3V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	0	AA12	49	I	CF11	97	I/O	SYSIO5	145	I	PPDEDT7	193	0	PPDATA0
2	0	AA11	50	I	CF10	98	I/O	SYSIO4	146	I	PPDEDT6	194	-	NC
3	0	AA10	51	I	CF9	99	I	STRB	147	I	PPDEDT5	195	0	WP
4	0	AA9	52	I	CF8	100	I	SCS	148	I	PPDEDT4	196	0	GIVEUP
5	0	AA8	53	I	CF7	101	I	DEFS256	149	I	PPDEDT3	197	I	PPDEAU1
6	-	V _{DD}	54	-	V _{DD}	102	-	GND	150	-	GND	198	-	GND
7	-	GND	55	-	GND	103	0	FS256P	151	I	PPDEDT2	199	I	PPDEAU0
8	0	AA7	56	I	CF6	104	I	DEFS	152	I	PPDEDT1	200	I/O	AMAI015
9	0	AA6	57	I	CF5	105	I	IBUSEN	153	I	PPDEDT0	201	I/O	AMAI014
10	0	AA5	58	I	CF4	106	0	OBUSEN	154	I	PPDEPRY	202	I/O	AMAI013
11	0	AA4	59	I	CF3	107	I/O	SYSIO3	155	I	PPDESY	203	I/O	AMAI012
12	0	AA3	60	I	CF2	108	I/O	SYSIO2	156	I	PPDEEF	204	I/O	AMAI011
13	0	AA2	61	I	CF1	109	I/O	SYSIO1	157	I	FSP1	205	I/O	AMAI010
14	0	AA1	62	I	CF0	110	I/O	SYSIO0	158	0	MRST	206	I/O	AMAI09
15	0	AA0	63	0	CA14	111	I	STAT1	159	I	MI7	207	I/O	AMAI08
16	0	AB15	64	0	CA13	112	I	STAT0	160	I	MI6	208	I/O	AMAI07
17	0	AB14	65	0	CA12	113	I	MCK	161	I	MI5	209	-	V _{DD}
18	-	V _{DD}	66	-	V _{DD}	114	-	V _{DD}	162	-	V _{DD}	210	-	GND
19	-	GND	67	-	GND	115	-	GND	163	-	GND	211	I/O	AMAI06
20	0	AB13	68	0	CA11	116	I	PPCK	164	I	MI4	212	I/O	AMAI05
21	0	AB12	69	0	CA10	117	I	CHIPID	165	I	MI3	213	I/O	AMAI04
22	0	AB11	70	0	CA9	118	0	SYSHL0	166	I	MI2	214	I/O	AMAI03
23	0	AB10	71	0	CA8	119	0	SYSHL1	167	I	MI1	215	I/O	AMAI02
24	0	AB9	72	0	CA7	120	0	VPHASE	168	I	MI0	216	I/O	AMAI01
25	0	AB8	73	0	CA6	121	0	APHASE	169	I	MI EF	217	I/O	AMAI00
26	0	AB7	74	0	CA5	122	0	CPHASE	170	0	MWEN0	218	I/O	AMBI015
27	0	AB6	75	0	CA4	123	0	RPHASE	171	0	MWEN1	219	I/O	AMBI014
28	0	AB5	76	0	CA3	124	0	PTWEN	172	0	MWEN2	220	I/O	AMBI013
29	0	AB4	77	0	CA2	125	0	PPREFV	173	0	MWEN3	221	I/O	AMBI012
30	-	GND	78	-	GND	126	-	V _{DD}	174	-	V _{DD}	222	-	GND
31	0	AB3	79	0	CA1	127	-	GND	175	-	GND	223	I/O	AMBI011
32	0	AB2	80	0	CA0	128	I	RESET	176	0	MREN0	224	I/O	AMBI010
33	0	AB1	81	I	MDTES4	129	I	REFV	177	0	MREN1	225	I/O	AMBI09
34	0	AB0	82	I	MDTES3	130	0	PITY	178	0	MREN2	226	I/O	AMBI08
35	I	CF23	83	I	MDTES2	131	0	PITX	179	0	MREN3	227	I/O	AMBI07
36	I	CF22	84	I	MDTES1	132	I	MCDT7	180	0	ASUB	228	I/O	AMBI06
37	I	CF21	85	I	MDTES0	133	I	MCDT6	181	0	PPPRY	229	I/O	AMBI05
38	I	CF20	86	0	SELECT	134	I	MCDT5	182	0	PPSY	230	I/O	AMBI04
39	I	CF19	87	0	PPAU1	135	I	MCDT4	183	0	PPEF	231	I/O	AMBI03
40	I	CF18	88	0	PPAU0	136	I	MCDT3	184	0	PPDATA7	232	I/O	AMBI02
41	I	CF17	89	-	V _{DD}	137	I	MCDT2	185	0	PPDATA6	233	I/O	AMBI01
42	-	V _{DD}	90	-	GND	138	-	V _{DD}	186	-	V _{DD}	234	-	V _{DD}
43	-	GND	91	I	FS256	139	-	GND	187	-	GND	235	-	GND
44	I	CF16	92	I	FS	140	I	MCDT1	188	0	PPDATA5	236	I/O	AMBI00
45	I	CF15	93	0	MUTE	141	I	MCDT0	189	0	PPDATA4	237	0	AMWEN
46	I	CF14	94	0	PPCK0	142	0	PTOE	190	0	PPDATA3	238	0	AA15
47	I	CF13	95	I/O	SYSIO7	143	0	FOWRST	191	0	PPDATA2	239	0	AA14
48	I	CF12	96	I/O	SYSIO6	144	0	FORRST	192	0	PPDATA1	240	0	AA13

INPUT	: AUDIO RATE CONVERTER COEFFICIENTS
CF0-23	: CHIP IDENTIFICATION
CHPTID	: DECODER AUDIO FREQUENCY
DEFS	: (48kHz OR VARIABLE)
DEF5256	: DECODER AUDIO FREQUENCY
	: (48kHzX256 OR VARIABLE)
FS	: AUDIO REFERENCE FREQUENCY (48kHz)
FS256	: AUDIO REFERENCE FREQUENCY (48kHzX256)
FSP1	: AUDIO PROGRAM PLAY FREQUENCY
	: (48kHz $\pm 1\%$)
IBUSEN	: SYSTEM CONTROL INTERFACE
	: SYS100-7 BUS SWITCH
MCDDT0-7	: MOVING COMPENSATE 2-FIELD DELAY DATA
MCK	: MASTER CLOCK (13.5MHz)
MDTES0-4	: TEST PINS (NORMALLY LOW)
M10-7	: FIFO MEMORY DATA
M1EF	: FIFO MEMORY ERROR DATA
PPCK	: PROGRAM PLAY CLOCK (13.5MHz $\pm 1\%$)
PPDEAU0-1	: AUDIO SERIAL DATA CH1/CH2, CH3/CH4
PPDDT0-7	: VIDEO 8-BIT DATA
PPDEF	: VIDEO ERROR FLAG
PPDEPRY	: VIDEO PARITY FLAG
PPDESY	: VIDEO SYNCHRONOUS PULSE
REFV	: REFERENCE VD
RESET	: CHIP RESET PULSE
SCS	: SYSTEM CONTROL INTERFACE CHIP SELECT
STAT0, 1	: SYSTEM CONTROL INTERFACE STATES
STRB	: SYSTEM CONTROL INTERFACE CLOCK

OUTPUT	
AA0-15	:AUDIO A MEMORY CONTROL ADDRESSES
AB0-15	:AUDIO B MEMORY CONTROL ADDRESSES
AMWEN	:AUDIO MEMORY WRITE ENABLE
APHASE	:AUDIO CLOCK PHASE DETECTION
ASUB	:PITCH SHIFT CONTROL SIGNAL
CA0-14	:AUDIO RATE CONVERTER COEFFICIENTS ADDRESSES
CPHASE	:AUDIO VIDEO CLOCK PHASE DETECTION
FORRST	:FIFO READ RESET PULSE
FORWRST	:FIFO WRITE RESET PULSE
FS256P	:AUDIO PROGRAM PLAY FREQUENCY (48kHz $\pm$ 256 $\pm$ 1%)
GIVEUP	:AUDIO PITCH DETECTION FAILUER MONITOR
MWEN0-3	:FRAME SYNCHRONIZER CONTROL READ ENABLE PULSES
MRST	:FIFO READ RESET
MUTE	:AUDIO MUTE SERIAL SIGNAL
MWEN0-3	:FRAME SYNCHRONIZER CONTROL WRITE ENABLE PULSES
OBUSEN	:SYSTEM CONTROL INTERFACE I/O BUS CONTROL
PITX	:CHANNEL A AUDIO PITCH WIDTH MONITOR
PITY	:CHANNEL B AUDIO PITCH WIDTH MONITOR
PPAU0.1	:AUDIO SERIAL DATA CH1/CH2, CH3/CH4
PPCKO	:SELECTED VIDEO CLOCK
PPDATA0-7	:VIDEO 8-BIT DATA
PPEF	:VIDEO ERROR FLAG
PPPRY	:VIDEO PARITY FLAG
PPREFV	:PROGRAM PLAY REFERENCE VD
PPSY	:VIDEO SYNCHRONOUS PULSE
PTOE	:AUDIO SUB MEMORY OUTPUT ENABLE
PTWEN	:AUDIO SUB MEMORY WRITE ENABLE
RPHASE	:AUDIO CLOCK REFERENCE PHASE DETECTION
SELECT	:AUDIO SWITCH PULSE
SYSHL0.1	:SYSTEM CONTROL PORTS
VPHASE	:VIDEO CLOCK PHASE DETECTION
WP	:AUDIO WINDOW PULSE

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INPUT/OUTPUT
SYS100-7  :SYSTEM CONTROL INTERFACE DATA
AMA100-15 :AUDIO A MEMORY DATA I/O
AMB100-16 :AUDIO B MEMORY DATA I/O

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