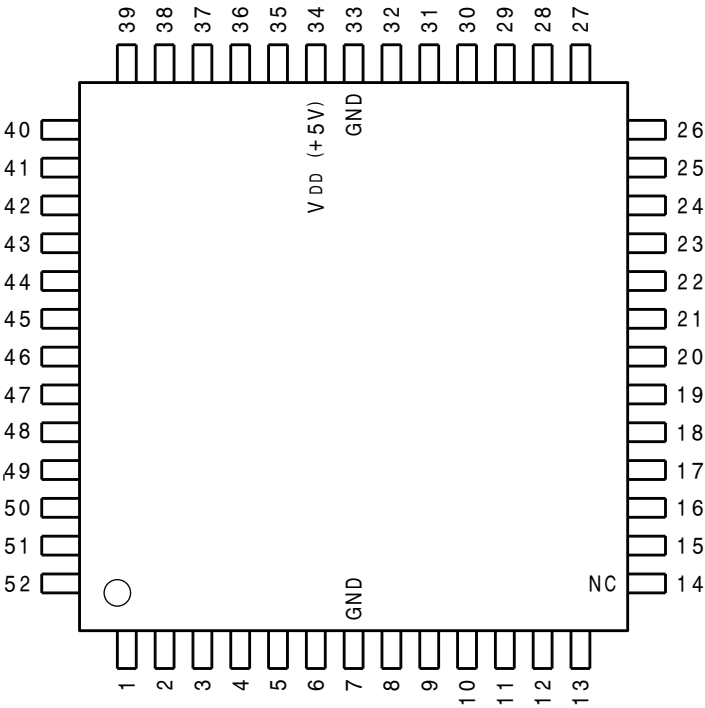

C-MOS VIDEO TIME BASE CORRECTOR
-TOP VIEW-



(V_{DD}=+5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	TST	14	-	NC	27	I	CKX	40	I	DIN8
2	O	OUT9	15	I	REFV	28	I	RST	41	I	DIN7
3	O	OUT8	16	I	RFLD	29	I	CS	42	I	DIN6
4	O	OUT7	17	I	RCFP	30	I/O	SDAT	43	I	DIN5
5	O	OUT6	18	I	RCK	31	I	SADD	44	I	DIN4
6	O	OUT5	19	O	TRS	32	I	CKD	45	I	DIN3
7	-	GND	20	I	SMPL	33	-	GND	46	I	DIN2
8	O	OUT4	21	O	WPT1	34	-	V _{DD}	47	I	DIN1
9	O	OUT3	22	O	WPT0	35	I	WCK	48	I	DIN0
10	O	OUT2	23	O	TS0	36	I	TS4	49	I	EXH
11	O	OUT1	24	I	RPT0	37	I	TS5	50	O	TS3
12	O	OUT0	25	O	TS1	38	I	TS6	51	I	EXFL
13	I	REFH	26	O	TS2	39	I	DIN9	52	I	EXCF

39	DIN9	OUT9	2
40	DIN8	OUT8	3
41	DIN7	OUT7	4
42	DIN6	OUT6	5
43	DIN5	OUT5	6
44	DIN4	OUT4	8
45	DIN3	OUT3	9
46	DIN2	OUT2	10
47	DIN1	OUT1	11
48	DIN0	OUT0	12
35	WCK	TRS	19
17	RCFP	RPT0	24
16	RFLD	WPT0	22
15	REFV	WPT1	21
13	REFH		
18	RCK	EXH	49
28	RST	EXFL	51
20	SMPL	EXCF	52
27	CKX	TS6	38
32	CKD	TS5	37
30	SDAT	TS4	36
31	SADD	TS3	50
29	CS	TS2	26
		TS1	25
		TS0	23
		TST	1

INPUT

CKD	; SERIAL INTERFACE CLOCK
CKX	; SERIAL INTERFACE ENABLE TIMING
$\overline{\text{CS}}$	; CHIP SELECT
DIN0-9	; VIDEO DATA BUS (4:2:2 FORMAT)
EXCF	; EXTERNAL COLOR FRAME REFERENCE (ONLY D-2)
EXFL	; EXTERNAL FIELD ODD/EVEN REFERENCE
EXH	; EXTERNAL H REFERENCE
RCFP	; COLOR FRAME PULSE FOR PHASE REFERENCE
RCK	; CLOCK FOR PHASE REFERENCE
REFH	; H SYNC FOR PHASE REFERENCE
REFV	; V SYNC FOR PHASE REFERENCE
RFLD	; FIELD O/E FOR PHASE REFERENCE
RPT0	; READ PORT 0
$\overline{\text{RST}}$	; SYSTEM RESET
SADD	; SERIAL ADDRESS
SMPL	; OUTPUT SAMPLE PULSE
WCK	; SYSTEM CLOCK

OUTPUT

OUT0-9	; OUTPUT BUS
WPT0, 1	; WRITE PORT 0, 1

INPUT/OUTPUT

SDAT	; SERIAL DATA
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