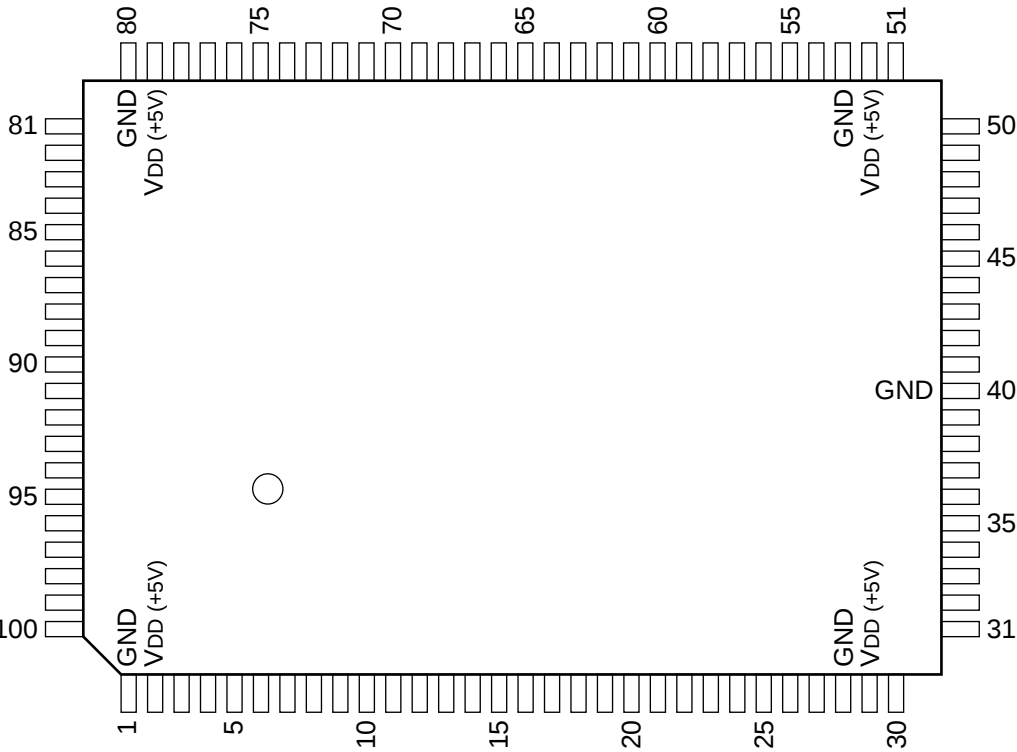

C-MOS ADDRESS-KEY SIGNAL GENERATOR
-TOP VIEW-



(VDD = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	21	O	K1	41	O	MY6	61	I	Y5	81	I	CKD
2	—	VDD	22	O	K2	42	O	MY7	62	I	Y6	82	I	TGL
3	I	OEM	23	O	K3	43	O	MY8	63	I	Y7	83	I	INH
4	O	MX0	24	O	K4	44	O	MY9	64	I	Y8	84	I	X15
5	O	MX1	25	O	K5	45	O	MY10	65	I	Y9	85	I	X14
6	O	MX2	26	O	K6	46	O	MY11	66	I	Y10	86	I	X13
7	O	MX3	27	I	OEK	47	O	MY12	67	I	Y11	87	I	X12
8	O	MX4	28	—	GND	48	O	MY13	68	I	Y12	88	I	X11
9	O	MX5	29	—	VDD	49	O	MY14	69	I	Y13	89	I	X10
10	O	MX6	30	O	K7	50	O	MY15	70	I	Y14	90	I	X9
11	O	MX7	31	O	K8	51	O	OVF	71	I	Y15	91	I	X8
12	O	MX8	32	O	K9	52	—	VDD	72	I	TST0	92	I	X7
13	O	MX9	33	O	CLIP	53.	—	GND	73	I	TST1	93	I	X6
14	O	MX10	34	O	MY0	54	I	FLD	74	I	PRL	94	I	X5
15	O	MX11	35	O	MY1	55	I	FL0	75	I	CS	95	I	X4
16	O	MX12	36	O	MY2	56	I	Y0	76	I	SDAT	96	I	X3
17	O	MX13	37	O	MY3	57	I	Y1	77	I	SADD	97	I	X2
18	O	MX14	38	O	MY4	58	I	Y2	78	I	CKX	98	I	X1
19	O	MX15	39	O	MY5	59	I	Y3	79	—	VDD	99	I	X0
20	O	K0	40	—	GND	60	I	Y4	80	—	GND	100	I	CK

84	X15	MX15	19	INPUT	
85	X14	MX14	18	CK	; SYSTEM CLOCK
86	X13	MX13	17	CKD	; SERIAL INTERFACE CLOCK
87	X12	MX12	16	CKX	; SWITCHING TIMING PLUS
88	X11	MX11	15	$\overline{CS}$	; CHIP SELECT (LOW ACTIVE)
89	X10	MX10	14	FLD, FL0	; FIELD-OFFSET CONTROL FOR MY OUTPUT
90	X9	MX9	13	INH	; SET ADDRESS-KEY TO 0 (HIGH TO 0)
91	X8	MX8	12	$\overline{OEK}$	; OUTPUT ENABLE OF ADDRESS-KEY (LOW ENABLE)
92	X7	MX7	11	$\overline{OEM}$	; OUTPUT ENABLE OF MX, MY OUTPUT (LOW ENABLE)
93	X6	MX6	10	PRL	; SET PARALLEL MODE FOR REGISTER
94	X5	MX5	9	SADD	; SERIAL ADDRESS
95	X4	MX4	8	SDAT	; SERIAL DATA
96	X3	MX3	7	TGL	; SWITCH INTERNAL REGISTER
97	X2	MX2	6		REG (0, 4) <=> REG (1, 5)
98	X1	MX1	5	TST1, TST0	; SET TEST MODE ((0, 0) FOR NORMAL)
99	X0	MX0	4	X15 - X0	; X ADDRESS
				Y15 - Y0	; Y ADDRESS
71	Y15	MY15	50	OUTPUT	
70	Y14	MY14	49	CLIP	; ADDRESS-KEY = 0 AREA FLAG
69	Y13	MY13	48	K9 - K0	; ADDRESS-KEY SIGNAL
68	Y12	MY12	47	OVF	; OVERFLOW FLAG
67	Y11	MY11	46	MX15 - MX0	; X ADDRESS
66	Y10	MY10	45	MY15 - MY0	; Y ADDRESS
65	Y9	MY9	44		
64	Y8	MY8	43		
63	Y7	MY7	42		
62	Y6	MY6	41		
61	Y5	MY5	39		
60	Y4	MY4	38		
59	Y3	MY3	37		
58	Y2	MY2	36		
57	Y1	MY1	35		
56	Y0	MY0	34		
82	TGL	OEM	3		
55	FL0	OVF	51		
54	FLD	CLIP	33		
83	INH				
		K9	32		
75	CS	K8	31		
		K7	30		
76	SDAT	K6	26		
77	SADD	K5	25		
81	>CKD	K4	24		
78	>CKX	K3	23		
		K2	22		
100	>CK	K1	21		
		K0	20		
		OEK	27		
72	TST0				
73	TST1				
74	PRL				

