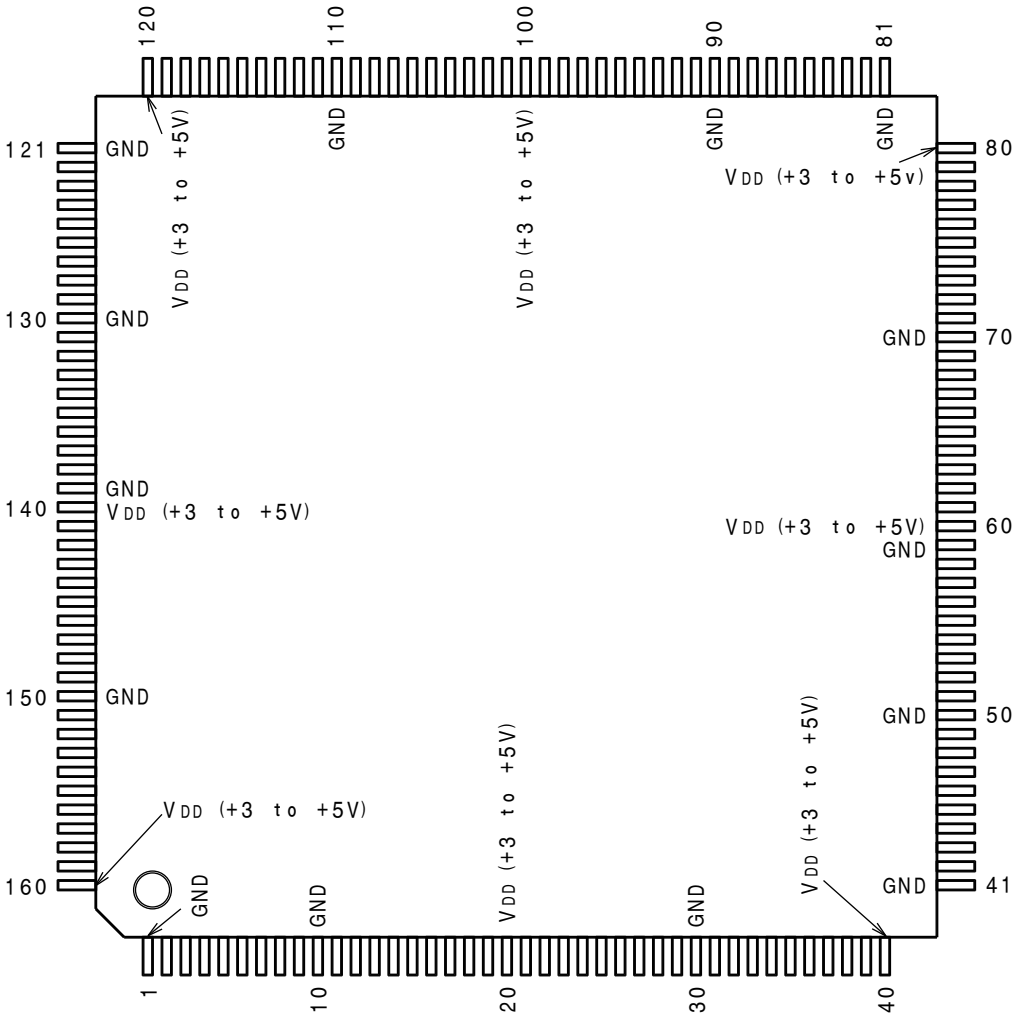
****

C-MOS CROSS POINT

-TOP VIEW-



(V_{DD} =+3 to +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	–	GND	41	–	GND	81	–	GND	121	–	GND
2	O	DOA0	42	O	DOD0	82	O	DOG0	122	I	SADD
3	O	DOA1	43	O	DOD1	83	O	DOG1	123	I	DI33
4	O	DOA2	44	O	OEA	84	O	DOG2	124	I	DI34
5	I	DI83	45	I	A0	85	I	SMPL	125	I	DI35
6	I	DI84	46	I	A1	86	I	$\overline{CS}$	126	I	DI40
7	O	DOA3	47	O	OEB	87	O	DOG3	127	I	DI41
8	O	DOA4	48	O	DOD2	88	O	DOG4	128	I	DI42
9	O	DOA5	49	O	DOD3	89	O	DOG5	129	I	CK
10	–	GND	50	–	GND	90	–	GND	130	–	GND
11	O	DOB0	51	O	DOD4	91	O	DOH0	131	I/O	SDAT
12	O	DOB1	52	O	DOD5	92	O	DOH1	132	I	DI43
13	O	DOB2	53	O	OEC	93	O	DOH2	133	I	DI44
14	I	DI85	54	O	OED	94	I	DI10	134	I	DI45
15	I	DI90	55	I	A2	95	I	DI11	135	I	DI50
16	I	DI91	56	O	OEE	96	I	DI12	136	I	DI51
17	I	DI92	57	O	DOE0	97	I	DI13	137	I	DI52
18	I	DI93	58	O	DOE1	98	I	DI14	138	I	CKD
19	I	DI94	59	–	GND	99	I	DI15	139	–	GND
20	–	V_{DD}	60	–	V_{DD}	100	–	V_{DD}	140	–	V_{DD}
21	I	DI95	61	O	DOE2	101	I	DI20	141	I	DI53
22	I	DI100	62	O	DOE3	102	I	DI21	142	I	DI54
23	I	DI101	63	O	OEF	103	I	DI22	143	I	DI55
24	I	DI102	64	O	OEG	104	I	DI23	144	I	DI60
25	I	DI103	65	I	A3	105	I	DI24	145	I	DI61
26	I	DI104	66	O	OEH	106	I	DI25	146	I	DI62
27	O	DOB3	67	O	OEI	107	O	DOH3	147	I	DI63
28	O	DOB4	68	O	DOE4	108	O	DOH4	148	I	DI64
29	O	DOB5	69	O	DOE5	109	O	DOH5	149	I	DI65
30	–	GND	70	–	GND	110	–	GND	150	–	GND
31	O	DOC0	71	O	DOF0	111	O	DOI0	151	I	DI70
32	O	DOC1	72	O	DOF1	112	O	DOI1	152	I	DI71
33	O	DOC2	73	O	DOF2	113	O	DOI2	153	I	DI72
34	I	DI105	74	I	A4	114	I	DI30	154	I	DI73
35	I	MOD1	75	I	CKX	115	I	DI31	155	I	DI74
36	I	MOD2	76	I	$\overline{RST}$	116	I	DI32	156	I	DI75
37	O	DOC3	77	O	DOF3	117	O	DOI3	157	I	DI80
38	O	DOC4	78	O	DOF4	118	O	DOI4	158	I	DI81
39	O	DOC5	79	O	DOF5	119	O	DOI5	159	I	DI82
40	–	V_{DD}	80	–	V_{DD}	120	–	V_{DD}	160	–	V_{DD}

INPUT

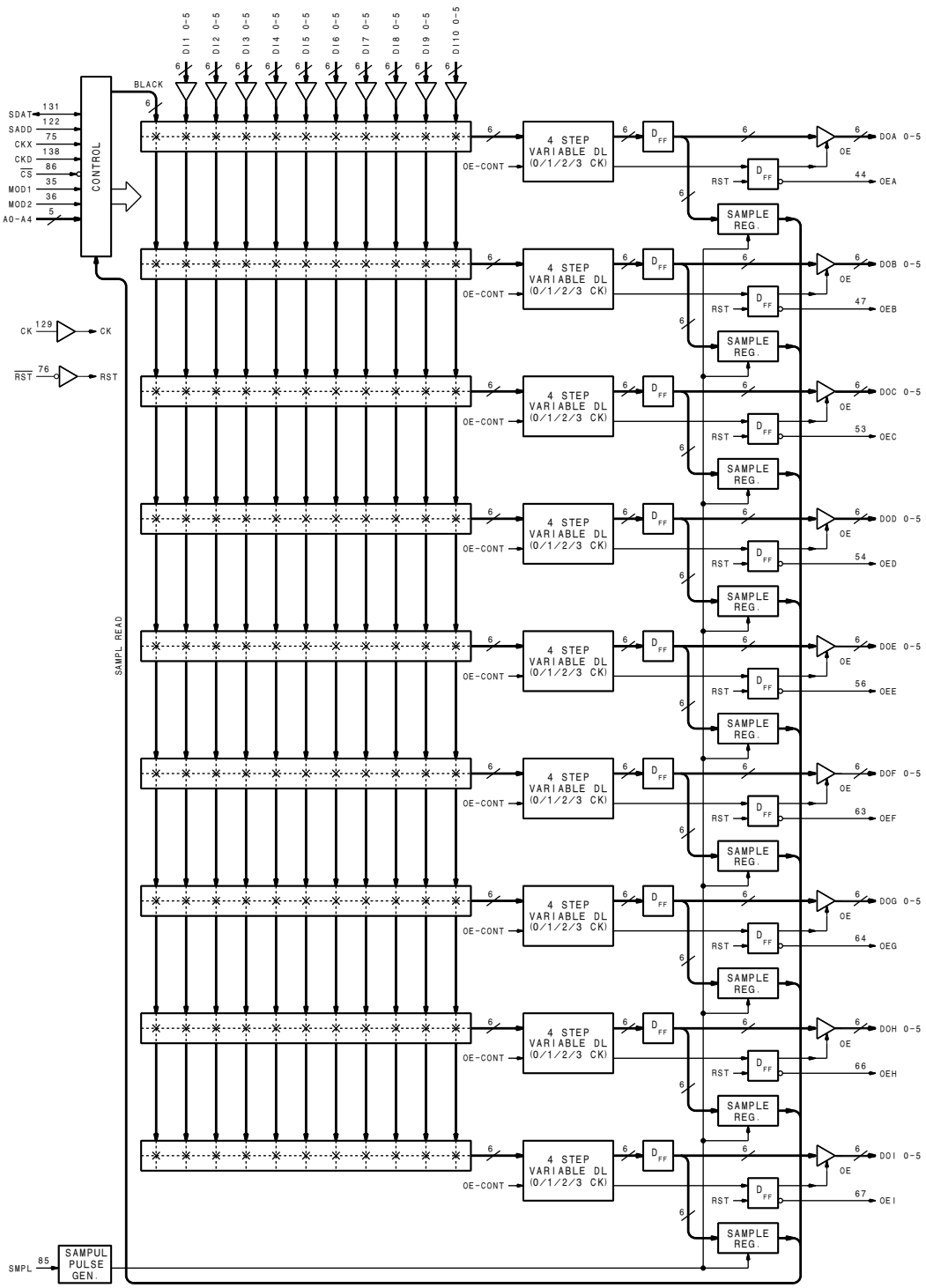
A4-A0	;ADDRESS BUS
CK	;SYSTEM CLOCK
CKD	;SERIAL CONTROL CLOCK
CKX	;SWITCHING TIMING PULSE
$\overline{CS}$	;CHIP SELECT
DI15-DI10	;INPUT DATA BUS 1
DI25-DI20	;INPUT DATA BUS 2
DI35-DI30	;INPUT DATA BUS 3
DI45-DI40	;INPUT DATA BUS 4
DI55-DI50	;INPUT DATA BUS 5
DI65-DI60	;INPUT DATA BUS 6
DI75-DI70	;INPUT DATA BUS 7
DI85-DI80	;INPUT DATA BUS 8
DI95-DI90	;INPUT DATA BUS 9
DI105-DI100	;INPUT DATA BUS 10
MOD1	; (LOW=L, HIGH=M)
MOD2	; (LOW=C, HIGH=Y)
$\overline{RST}$	;RESET PULSE
SADD	;SERIAL ADDRESS
SMPL	;SAMPLE PULSE

OUTPUT

DOA5-DOA0	;OUTPUT DATA BUS A
DOB5-DOB0	;OUTPUT DATA BUS B
DOC5-DOC0	;OUTPUT DATA BUS C
DOD5-DOD0	;OUTPUT DATA BUS D
DOE5-DOE0	;OUTPUT DATA BUS E
DOF5-DOF0	;OUTPUT DATA BUS F
DOG5-DOG0	;OUTPUT DATA BUS G
DOH5-DOH0	;OUTPUT DATA BUS H
DOI5-DOI0	;OUTPUT DATA BUS I
OEA-OEI	;OUTPUT ENABLE

INPUT/OUTPUT

SDAT	;SERIAL DATA


DI1 0-5
DI2 0-5
DI3 0-5
DI4 0-5
DI5 0-5
DI6 0-5
DI7 0-5
DI8 0-5
DI9 0-5
DI10 0-5
BLACK
CONTROL
SDAT 131
SADD 122
CKX 75
CKD 138
CS 86
MOD1 35
MOD2 36
A0-A4 5
CK 129
RST 76
SAMPL READ
SMPL 85
SAMPUL PULSE GEN.
4 STEP VARIABLE DL (0/1/2/3 CK)
OE-CONT
D FF
RST
OE
SAMPLE REG.
DOA 0-5
OEA 44
DOB 0-5
OEB 47
DOC 0-5
OEC 53
DOD 0-5
OED 54
DOE 0-5
OEE 56
DOF 0-5
OEF 63
DOG 0-5
OEG 64
DOH 0-5
OEH 66
DOI 0-5
OEI 67

SCL=1.15