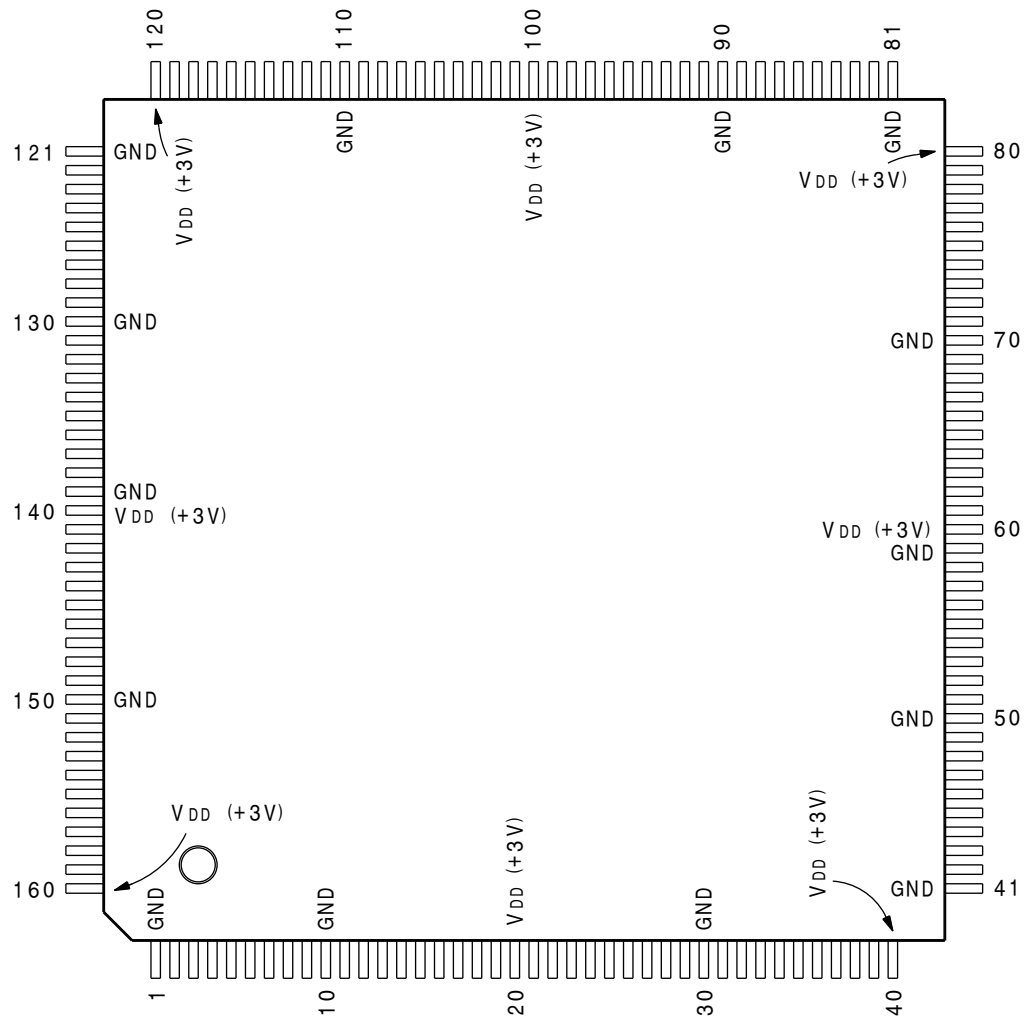

CXD8849Q(1/4)
IL08

C-MOS COMBINER PROCESSOR

-TOP VIEW-



(V_{DD} = +3V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	–	GND	33	0	VIDEO6	65	I	HD	97	I	TRNS18	129	I	BLANK1
2	I	BKGDB1	34	0	VIDEO5	66	I	TRNS20	98	I	TRNS19	130	–	GND
3	I	BKGDB2	35	0	VIDEO4	67	I	TRNS21	99	I	TRNS110	131	0	BLANK0
4	I	BKGDB3	36	0	VIDEO3	68	I	TRNS22	100	–	V _{DD}	132	I	KYFL20
5	I	BKBDB4	37	0	VIDEO2	69	I	RSTL	101	I	TRNS111	133	I	KYFL21
6	I	BKGDB5	38	0	VIDEO1	70	–	GND	102	I	KYSR20	134	I	KYFL22
7	I	BKGDB6	39	0	VIDEO0	71	I	XMM	103	I	KYSR21	135	I	KYFL23
8	I	BKGDB7	40	–	V _{DD}	72	I/O	XTS0	104	I	KYSR22	136	I	KYFL24
9	I	BKGDB8	41	–	GND	73	I/O	XTS1	105	I	KYSR23	137	I	KYFL25
10	–	GND	42	0	KEY/Z11	74	I/O	XTS2	106	I	KYSR24	138	I	KYFL26
11	I	BKGDB9	43	0	KEY/Z10	75	I/O	XTS3	107	I	KYSR25	139	–	GND
12	I	BKGDB10	44	0	KEY/Z9	76	I/O	XTS4	108	I	KYSR26	140	–	V _{DD}
13	I	BKGDB11	45	0	KEY/Z8	77	I	TRNS23	109	I	KYSR27	141	I	KYFL27
14	I	BKGDA0	46	0	KEY/Z7	78	I	TRNS24	110	–	GND	142	I	KYFL28
15	I	BKGDA1	47	0	KEY/Z6	79	I	TRNS25	111	I	KYSR28	143	I	KYFL29
16	I	BKGDA2	48	0	KEY/Z5	80	–	V _{DD}	112	I	KYSR29	144	I	KYFL210
17	I	BKGDA3	49	0	KEY/Z4	81	–	GND	113	I	KYSR210	145	I	KYFL211
18	I	BKGDA4	50	–	GND	82	I	TRNS26	114	I	KYSR211	146	I	KYFL10
19	I	BKGDA5	51	0	KEY/Z3	83	I	TRNS27	115	I	KYSR10	147	I	KYFL11
20	–	V _{DD}	52	0	KEY/Z2	84	I	TRNS28	116	I	KYSR11	148	I	KYFL12
21	I	BKGDA6	53	0	KEY/Z1	85	I	TRNS29	117	I	KYSR12	149	I	KYFL13
22	I	BKGDA7	54	0	KEY/Z0	86	I	TRNS210	118	I	KYSR13	150	–	GND
23	I	BKGDA8	55	I/O	SMPL	87	I	TRNS211	119	I	KYSR14	151	I	KYFL14
24	I	BKGDA9	56	I	CS2	88	I	TRNS10	120	–	V _{DD}	152	I	KYFL15
25	I	BKGDA10	57	I	CS1	89	I	TRNS11	121	–	GND	153	I	KYFL16
26	I	BKGDA11	58	I	CK	90	–	GND	122	I	KYSR15	154	I	KYFL17
27	0	VIDEO11	59	–	GND	91	I	TRNS12	123	I	KYSR16	155	I	KYFL18
28	0	VIDEO10	60	–	V _{DD}	92	I	TRNS13	124	I	KYSR17	156	I	KYFL19
29	0	VIDEO9	61	I	CKD	93	I	TRNS14	125	I	KYSR18	157	I	KYFL110
30	–	GND	62	I	SADD	94	I	TRNS15	126	I	KYSR19	158	I	KYFL111
31	0	VIDEO8	63	I/O	SDAT	95	I	TRNS16	127	I	KYSR110	159	I	BKGDB0
32	0	VIDEO7	64	I	CKX	96	I	TRNS17	128	I	KYSR111	160	–	V _{DD}

INPUT

BKGDA (0-11) ; BKGDA VIDEO INPUT
 BKGDB (0-11) ; BKGDB VIDEO INPUT
 BLANKI ; BLANKING PULSE INPUT
 CK ; 27MHz CLOCK INPUT
 CKD ; CLOCK INPUT FOR SERIAL CONTROL
 CKX ; SWITCHING TIMING PULSE INPUT
 $\overline{CS}$ (1-2) ; CHIP SELECT
 HD ; H DRIVE INPUT
 KYFL1 (0-11) ; KEY1 FILL VIDEO INPUT
 KYFL2 (0-11) ; KEY2 FILL VIDEO INPUT
 KYSR1 (0-11) ; KEY1 SOURCE INPUT
 KYSR2 (0-11) ; KEY2 SOURCE INPUT
 $\overline{RSTL}$; RESET INPUT FOR CHIP (L: RESET)
 SADD ; ADDRESS INPUT FOR SERIAL CONTROL
 TRANS1 (0-11) ; TRANSITION1 INPUT
 TRANS2 (0-11) ; TRANSITION2 INPUT
 XMM ; MULTIPLIER CHECK MODE (H: CHECK) (TEST PIN FOR IC CHECK)

OUTPUT

BLANKO ; BLANKING PULSE OUTPUT
 KEY/Z (0-11) ; KEY & Z OUTPUT
 VIDEO (0-11) ; VIDEO OUTPUT

INPUT/OUTPUT

SDAT ; DATA INPUT/OUTPUT FOR SERIAL CONTROL
 SMPL ; SAMPLE PULSE INPUT/OUTPUT FOR TEST
 (XMM=L: SAMPLE PULSE INPUT)
 XTS (0-4) ; INPUT/OUTPUT FOR TEST (TEST PIN FOR IC CHECK)
 (XMM=L: INPUT FOR TEST)

