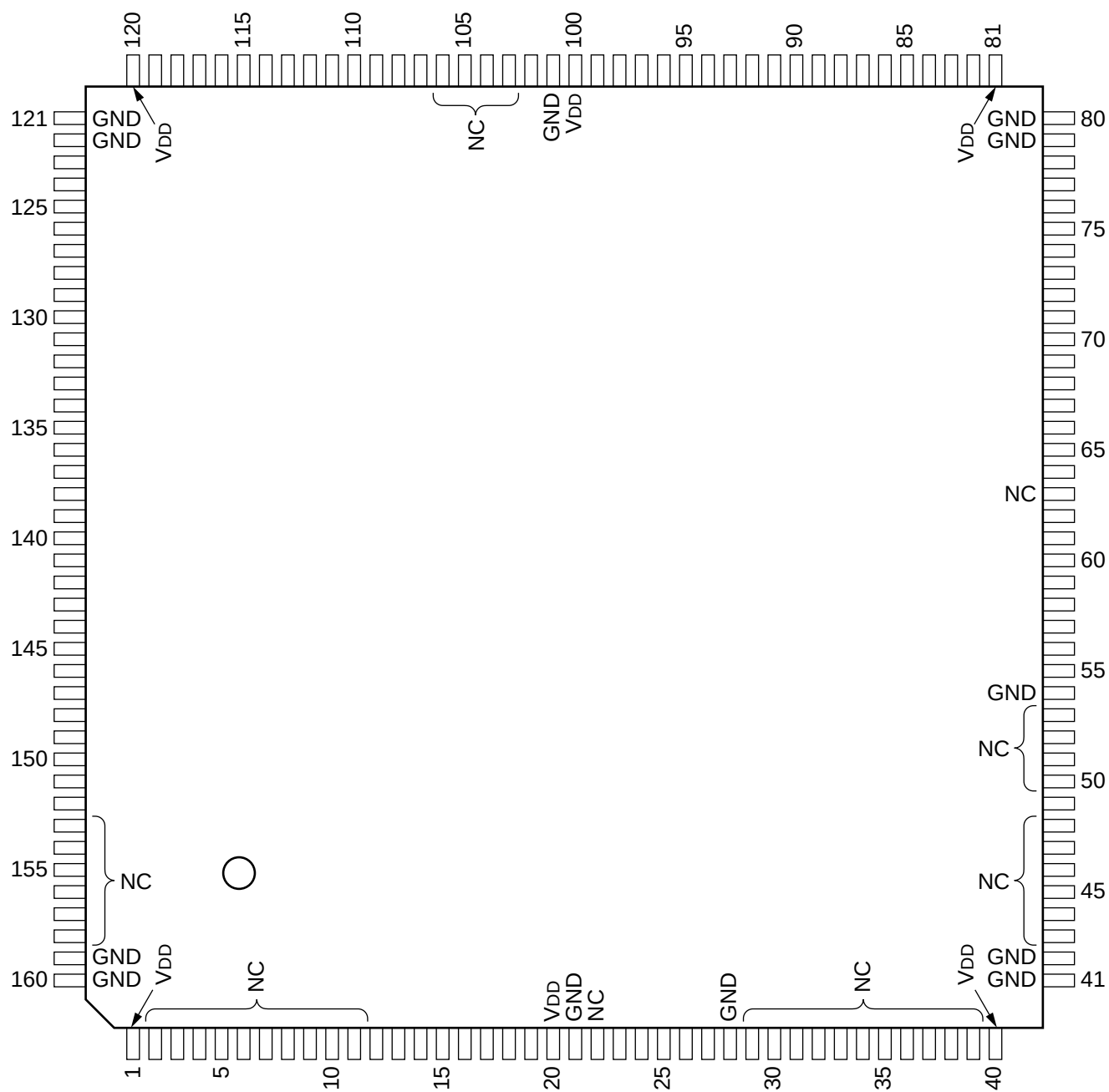


C-MOS 23-BITS INTERPOLATOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	33	—	NC	65	I/O	D28	97	O	POT1	129	I	OE1
2	—	NC	34	—	NC	66	I/O	D27	98	O	POT0	130	O	O22
3	—	NC	35	—	NC	67	I/O	D26	99	I	CKW	131	O	O21
4	—	NC	36	—	NC	68	I/O	D25	100	—	VDD	132	O	O20
5	—	NC	37	—	NC	69	I/O	D24	101	—	GND	133	O	O19
6	—	NC	38	—	NC	70	I/O	D23	102	I	RST	134	O	O18
7	—	NC	39	—	NC	71	I/O	D22	103	—	NC	135	O	O17
8	—	NC	40	—	VDD	72	I/O	D21	104	—	NC	136	O	O16
9	—	NC	41	—	GND	73	I/O	D20	105	—	NC	137	O	O15
10	—	NC	42	—	GND	74	I/O	D19	106	—	NC	138	O	O14
11	—	NC	43	—	NC	75	I/O	D18	107	I	CKX	139	O	O13
12	I	I12	44	—	NC	76	I/O	D17	108	O	FLGO	140	O	O12
13	I	I11	45	—	NC	77	I/O	D16	109	O	POT2	141	O	O11
14	I	I10	46	—	NC	78	I/O	D15	110	O	RM1	142	O	O10
15	I	I09	47	—	NC	79	—	GND	111	O	RM0	143	O	O09
16	I	I08	48	—	NC	80	—	GND	112	I	OE3	144	O	O08
17	I	I07	49	I	MODE	81	—	VDD	113	O	W12	145	O	O07
18	I	I06	50	—	NC	82	I/O	D14	114	O	W11	146	O	O06
19	I	I05	51	—	NC	83	I/O	D13	115	O	W10	147	O	O05
20	—	VDD	52	—	NC	84	I/O	D12	116	O	W09	148	O	O04
21	—	GND	53	—	NC	85	I/O	D11	117	O	W08	149	O	O03
22	—	NC	54	—	GND	86	I/O	D10	118	O	W07	150	O	O02
23	I	I04	55	I	ADD3	87	I/O	D09	119	O	W06	151	O	O01
24	I	I03	56	I	ADD2	88	I/O	D08	120	—	VDD	152	O	O00
25	I	I02	57	I	ADD1	89	I/O	D07	121	—	GND	153	—	NC
26	I	I01	58	I	ADD0	90	I/O	D06	122	—	GND	154	—	NC
27	I	I00	59	I	CS	91	I/O	D05	123	O	W05	155	—	NC
28	—	GND	60	I	RW	92	I/O	D04	124	O	W04	156	—	NC
29	—	NC	61	I	STRB	93	I/O	D03	125	O	W03	157	—	NC
30	—	NC	62	I	STRT	94	I/O	D02	126	O	W02	158	—	NC
31	—	NC	63	—	NC	95	I/O	D01	127	O	W01	159	—	GND
32	—	NC	64	I/O	D29	96	I/O	D00	128	O	W00	160	—	GND

INPUT

ADD0 - ADD3 ; PARALLEL ADDRESS
CS ; CHIP SELECT
CKW ; SYSTEM CLOCK
CKX ; COMAND EXECUTION CLOCK
I00 - I12 ; INTERPOLATION RATIO INPUTS
MODE ; LOW : NORMAL
OE1 ; O00-O22 OUTPUT ENABLE
OE3 ; W00-W12 OUTPUT ENABLE
RST ; RESET SIGNAL
RW ; READ / WRITE SIGNAL
STRB ; STROBE SIGNAL
STRT ; INTERPOLATION TIMING SIGNAL

OUTPUT

FLGO ; INTERNAL TIMING FLAG
O00 - O22 ; INTERPOLATED OUTPUT DATA
POT0 - POT2 ; PARALLEL OUTPUT PORTS
RM0, RM1 ; INTERNAL TIMING FLAGS
W00 - W12 ; EXTERNAL RAM WRITE ADDRESS

INPUT/OUTPUT

D00 - D29 ; PARALLEL DATA

