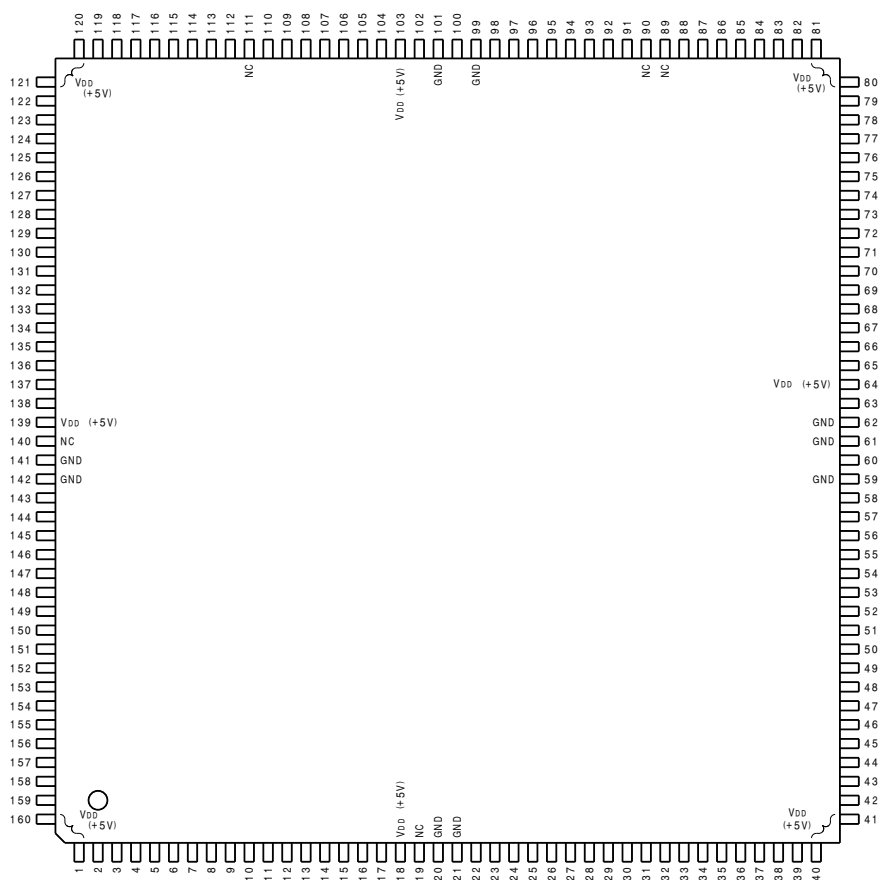


CXD8827Q(1/3)
IL08

-TOP VIEW-



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	–	VDD	41	–	VDD	81	–	VDD	121	–	VDD
2	I	IN11	42	I	IN33	82	I	DT11	122	I	IN0B
3	I	IN10	43	I	IN32	83	I	DT21	123	I	IN0A
4	I	IN2B	44	I	IN31	84	I	LD1	124	I	IN09
5	I	IN2A	45	I	IN30	85	I	EN1	125	I	IN08
6	I	IN29	46	I	IN4B	86	I	LD2	126	I	IN07
7	I	IN28	47	I	IN4A	87	I	EN2	127	I	IN06
8	I	IN27	48	I	IN49	88	I	TN_IN	128	I	IN05
9	I	IN26	49	I	IN48	89	–	NC	129	I	IN04
10	I	IN25	50	I	IN47	90	–	NC	130	I	IN03
11	I	IN24	51	I	IN46	91	O	DT1C0	131	I	IN02
12	I	IN23	52	I	IN45	92	O	DT1Y0	132	I	IN01
13	O	OUT1B	53	O	OUT2B	93	O	DT20	133	I	IN00
14	O	OUT1A	54	O	OUT2A	94	O	OUT3B	134	O	OUT0B
15	O	OUT19	55	O	OUT29	95	O	OUT3A	135	O	OUT0A
16	O	OUT18	56	O	OUT28	96	O	OUT39	136	O	OUT09
17	O	OUT17	57	O	OUT27	97	O	OUT38	137	O	OUT08
18	–	VDD	58	O	OUT26	98	O	OUT37	138	O	OUT07
19	–	NC	59	–	GND	99	–	GND	139	–	VDD
20	–	GND	60	I	CK	100	I	CKD	140	–	NC
21	–	GND	61	–	GND	101	–	GND	141	–	GND
22	O	OUT16	62	–	GND	102	I/O	SDAT	142	–	GND
23	O	OUT15	63	I	CKS	103	–	VDD	143	O	OUT06
24	O	OUT14	64	–	VDD	104	O	OUT36	144	O	OUT05
25	O	OUT13	65	O	OUT25	105	O	OUT35	145	O	OUT04
26	O	OUT12	66	O	OUT24	106	O	OUT34	146	O	OUT03
27	O	OUT11	67	O	OUT23	107	O	OUT33	147	O	OUT02
28	O	OUT10	68	O	OUT22	108	O	OUT32	148	O	OUT01
29	I	IN22	69	O	OUT21	109	O	OUT31	149	O	OUT00
30	I	IN21	70	O	OUT20	110	O	OUT30	150	I	IN1B
31	I	IN20	71	I	IN44	111	–	NC	151	I	IN1A
32	I	IN3B	72	I	IN43	112	I	SADD	152	I	IN19
33	I	IN3A	73	I	IN42	113	I	CKX	153	I	IN18
34	I	IN39	74	I	IN41	114	I	CS2	154	I	IN17
35	I	IN38	75	I	IN40	115	I	CS1	155	I	IN16
36	I	IN37	76	I	OE0	116	I	CS0	156	I	IN15
37	I	IN36	77	I	OE1	117	I	CTIM	157	I	IN14
38	I	IN35	78	I	OE2	118	O	TSTO	158	I	IN13
39	I	IN34	79	I	OE3	119	I	RST	159	I	IN12
40	–	VDD	80	–	VDD	120	–	VDD	160	–	VDD

133	IN00	OUT00	149	INPUT
132	IN01	OUT01	148	CK :SYSTEM CLOCK
131	IN02	OUT02	147	CKD :CLOCK FOR CS0, CS1, CS2, SADD, SDAT
130	IN03	OUT03	146	CKS :CLOCK FOR SAMPLE AND HOLD CIRCUIT IN OUT1
129	IN04	OUT04	145	CKX :TIMING FOR SERIAL CONTROL EXECUTION
128	IN05	OUT05	144	CS0, CS1, CS2
127	IN06	OUT06	143	:CHIP SELECT
126	IN07	OUT07	138	CTIM :COLOR TIMING SPECIFIED
125	IN08	OUT08	137	DT11, DT21
124	IN09	OUT09	136	:SAMPLE PULSE FOR SAMPLE AND HOLD CIRCUIT IN OUT1, OUT2
123	IN0A	OUT0A	135	EN1, EN2 :NAMADD1, NAMADD2 CIRCUIT ENABLE (IN COUNTER MODE)
122	IN0B	OUT0B	134	IN00-IN0B, IN10-IN1B, IN20-IN2B, IN30-IN3B, IN40-IN4B
3	IN10	OUT10	28	:DATA INPUTS
2	IN11	OUT11	27	LD1, LD2 :NAMADD1, NAMADD2 CIRCUIT LOAD PIN (IN COUNTER MODE)
159	IN12	OUT12	26	OE0, OE1, OE2, OE3
158	IN13	OUT13	25	:OUTPUT ENABLE FOR OUT0, OUT1, OUT2, OUT3
157	IN14	OUT14	24	SADD :SERIAL ADDRESS
156	IN15	OUT15	23	RST :POWER RESET
155	IN16	OUT16	22	TN IN :TEST TERMINAL
154	IN17	OUT17	17	OUTPUT
153	IN18	OUT18	16	DT1Y0, DT1C0, DT20
152	IN19	OUT19	15	:SAMPLE PULSE OUTPUT
151	IN1A	OUT1A	14	OUT00-OUT0B, OUT10-OUT1B, OUT20-OUT2B, OUT30-OUT3B
150	IN1B	OUT1B	13	:DATA OUTPUTS
31	IN20	OUT20	70	TST0 :TEST TERMINAL
30	IN21	OUT21	69	INPUT/OUTPUT
29	IN22	OUT22	68	SDAT :SERIAL DATA
12	IN23	OUT23	67	
11	IN24	OUT24	66	
10	IN25	OUT25	65	
9	IN26	OUT26	58	
8	IN27	OUT27	57	
7	IN28	OUT28	56	
6	IN29	OUT29	55	
5	IN2A	OUT2A	54	
4	IN2B	OUT2B	53	
45	IN30	OUT30	110	
44	IN31	OUT31	109	
43	IN32	OUT32	108	
42	IN33	OUT33	107	
39	IN34	OUT34	106	
38	IN35	OUT35	105	
37	IN36	OUT36	104	
36	IN37	OUT37	98	
35	IN38	OUT38	97	
34	IN39	OUT39	96	
33	IN3A	OUT3A	95	
32	IN3B	OUT3B	94	
75	IN40	DT1Y0	92	
74	IN41	DT1C0	91	
73	IN42	DT20	93	
72	IN43	OE0	76	
71	IN44	OE1	77	
52	IN45	OE2	78	
51	IN46	OE3	79	
50	IN47	DT11	82	
49	IN48	DT21	83	
48	IN49	TN IN	88	
47	IN4A	TST0	118	
46	IN4B			
117	CTIM			
102	SDAT			
112	SADD			
	CK			
	CKS			
	CKD			
	CKX			
	RST			
	CS0			
	CS1			
	CS2			
	LD1			
	LD2			
	EN1			
	EN2			

CXD8827Q(2/3)
1L08

