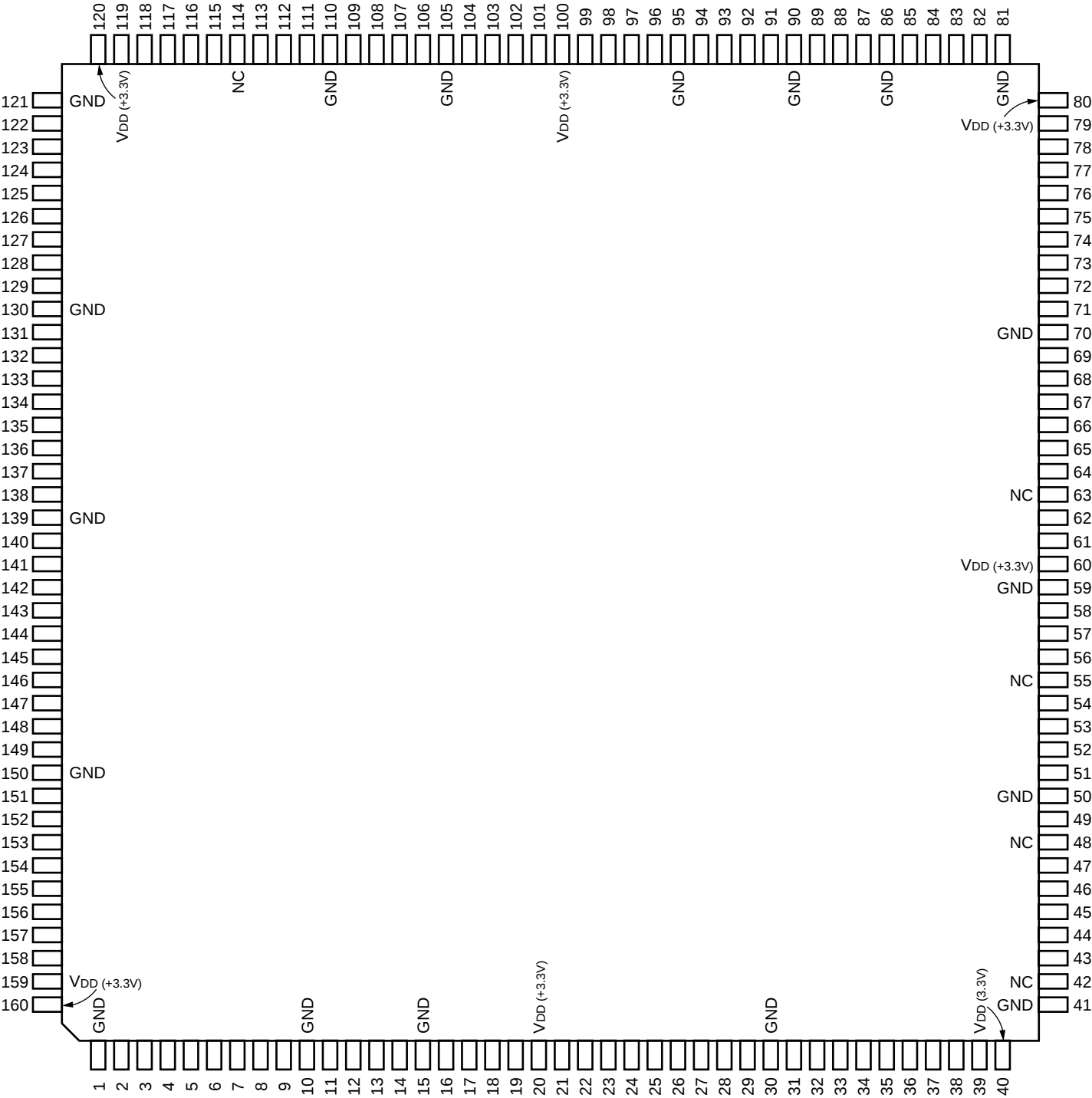

C-MOS JOG AUDIO SOUND PROCESSOR
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	41	—	GND	81	—	GND	121	—	GND
2	I	RESET	42	—	NC	82	O	A16	122	I/O	IO6
3	I	JMPSL	43	I	TAI4	83	O	A15	123	I/O	IO5
4	I	LIMON	44	I	TAI3	84	O	A14	124	I/O	IO4
5	I	ENMUTE	45	I	TAI2	85	O	TESO8	125	I	DE2X1
6	O	TDO7	46	I	TAI1	86	—	GND	126	I	DE2X2
7	O	TDO6	47	I	TAI0	87	O	A13	127	I	DE2XV
8	O	TDO5	48	I	TDI7	88	O	A12	128	I/O	IO3
9	O	OBUSEN	49	I	TDI6	89	O	A11	129	I/O	IO2
10	—	GND	50	—	GND	90	—	GND	130	—	GND
11	I	IBUSEN	51	I	TDI5	91	O	A10	131	I/O	IO1
12	O	TDO4	52	O	MNDN	92	O	A9	132	I/O	IO0
13	O	TDO3	53	O	MNUP	93	O	A8	133	I	DE2XJP
14	O	TDO2	54	O	MNMUTE	94	O	A7	134	I	TESI8
15	—	GND	55	—	NC	95	—	GND	135	I	TESI7
16	O	TDO1	56	I	TDI4	96	O	TESO7	136	I	TESI6
17	O	TDO0	57	I	TDI3	97	O	TESO6	137	I	TESI5
18	I	STAT1	58	I	TDI2	98	O	TESO5	138	I	FS
19	I	STAT0	59	—	GND	99	O	TESO4	139	—	GND
20	I	VDD	60	—	VDD	100	—	VDD	140	—	VDD
21	I	STRB	61	I	TDI1	101	O	TESO3	141	I	DE2Y1
22	I	SCS	62	I	TDI0	102	O	TESO2	142	I	DE2Y2
23	I	TAI10	63	—	NC	103	O	TESO1	143	I	DE2YV
24	I	TAI9	64	I	TWE	104	O	TESO0	144	I	TESI4
25	I	TAI8	65	I	TRE	105	—	GND	145	I	TESI3
26	I/O	SYSIO7	66	I	XMM	106	O	A6	146	I	TESI2
27	I/O	SYSIO6	67	O	CNFVD	107	O	A5	147	I	TESI1
28	I/O	SYSIO5	68	O	CNF2	108	O	A4	148	I	TESI0
29	I/O	SYSIO4	69	O	CNF1	109	O	A3	149	I	FS256
30	—	GND	70	—	GND	110	—	GND	150	—	GND
31	I/O	SYSIO3	71	O	ADV1	111	O	A2	151	I	DTJMP
32	I/O	SYSIO2	72	O	ADV2	112	O	A1	152	I	TRJMP
33	I/O	SYSIO1	73	O	ADVVD	113	O	A0	153	I	DIR
34	I/O	SYSIO0	74	I	MDTES3	114	—	NC	154	I	CAPFG
35	I	CHIPID1	75	I	MDTES2	115	O	SPD3	155	I	FRNORM
36	I	CHIPID0	76	I	MDTES1	116	O	SPD2	156	O	SPD7
37	I	TAI7	77	I	MDTES0	117	O	SPD1	157	O	SPD6
38	I	TAI6	78	O	OE	118	O	SPD0	158	O	SPD5
39	I	TAI5	79	O	WE	119	I/O	IO7	159	O	SPD4
40	—	VDD	80	—	VDD	120	—	VDD	160	—	VDD

INPUT

CAPFG	; CAPSTAN FG
CHIPID1, CHIPID0	; ID NUMBER
DE2X1, DE2X2	; AUDIO SERIAL DATA (X1 = CH1/CH2, X2 = CH3/CH4)
DE2XJP	; DISCONTINUITY OF FIELD
DE2XV, DE2YV	; VD AT FIELD START OF AUDIO DATA
DE2Y1, DE2Y2	; AUDIO SERIAL DATA (Y1 = CH1/CH2, Y2 = CH3/CH4)
DIR	; TAPE DRIVE DIRECTION
DTJMP	; DT HEAD JUMP SIGNAL
ENMUTE	; MUTE FLAG (H : NORMAL)
FRNORM	; DT HEAD JUMP AT FRAME OR FEILD
FS	; AUDIO SAMPLING FREQUENCY
FS256	; REFERENCE CLOCK (FSx256)
IBUSEN	; I/O CONTROL FOR SYSTEM CONTROL TERMINAL
JMPSL	; CONTROL LINE SELECT FOR EXIT MEMORY
LIMON	; CONTROL WIDTH OF SELECT SPEED-DATA (H : NORMAL)
MDTES3-MDTES0	; TEST MODE SELECT
RESET	; SYSTEM RESET
SCS	; CLOCK SELECT FOR SYSTEM CONTROL
STAT1, STAT0	; ATTRIBUTE DISCRIMINATE OF SYSTEM CONTROL DATA
STRB	; CLOCK FOR SYSTEM CONTROL
TAI10-TAI0, TDI7-TDI0, TESI8-TESI0	; TEST DATA
TRJMP	; JUMP SIGNAL AT FIELD DATA DISCONTINUITY
TWE, TRE	; TEST DATA FOR INTERNAL MEMORY
XMM	; SELECT INTERNAL MEMORY TEST

OUTPUT

A16-A0	; ADDRESS LINE FOR EXTERNAL MEMORY
ADV1, ADV2	; PROCESS SIGNAL (CH1/CH2, CH3/CH4)
ADVVD	; PROCESS SIGNAL (VD)
CNF1, CNF2	; INPUT SIGNAL SELECT (CH1/CH2, CH3/CH4)
CNFVD	; INPUT SIGNAL SELECT (VD)
MNDN, MNUP	; MONITOR OF SPEED DATA WINDOW POSITION
MNMUTE	; MONITOR OF MUTE
OBUSEN	; I/O CONTROL FOR SYSTEM CONTROL TERMINAL
OE	; I/O CONTROL FOR EXTERNAL MEMORY
SPD7-SPD0	; SPEED DATA MONITOR
TDO7-TDO0	; TEST DATA
TESO8-TESO0	; TEST DATA AND DIFFERENCE ADDRESS WRITE AND READ
WE	; WRITE ENABLE FOR EXTERNAL MEMORY

INPUT/OUTPUT

I07-IO0	; DATA FOR EXTERNAL MEMORY
SYSIO7-SYSIO0	; DATA FOR SYSTEM CONTROL