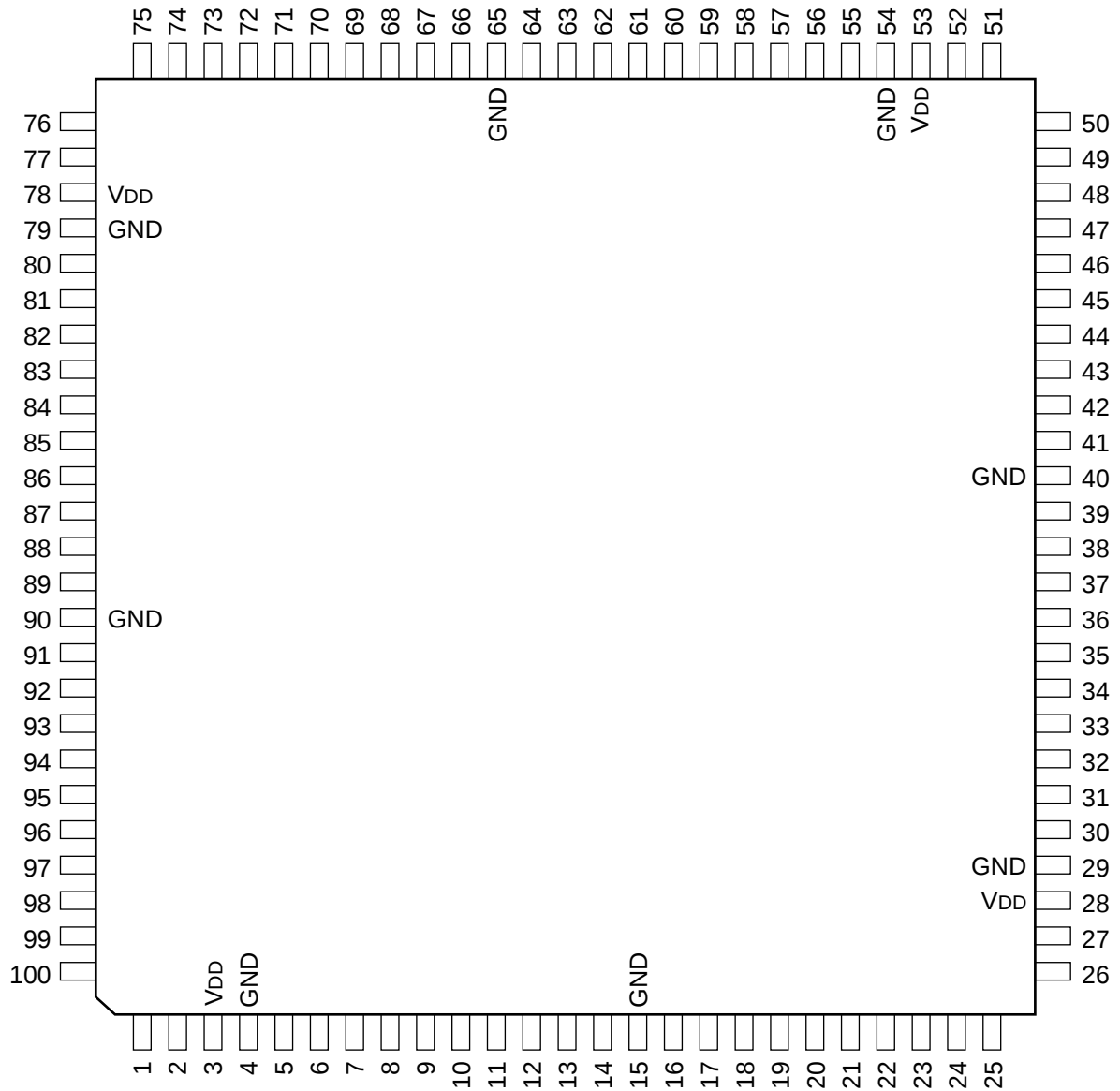


C-MOS VITC READER GENERATOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	OVD9	26	I	HRTH	51	I/O	DATA0	76	O	RVITC
2	O	OVD8	27	I	RESET	52	I/O	DATA1	77	O	RLTCH
3	—	VDD	28	—	VDD	53	—	VDD	78	—	VDD
4	—	GND	29	—	GND	54	—	GND	79	—	GND
5	O	OVD7	30	I	27CK	55	I/O	DATA2	80	O	GVITC
6	O	OVD6	31	I	ALLTH	56	I/O	DATA3	81	O	VGATE
7	O	OVD5	32	I	ICAD0	57	I/O	DATA4	82	O	BGATE
8	I	TST0	33	I	ICAD1	58	I/O	DATA5	83	O	XCFI
9	I	TST1	34	I	ICAD2	59	I/O	DATA6	84	O	NT4F
10	I	TST2	35	I	ICAD3	60	I/O	DATA7	85	O	PL4F
11	I	TST3	36	I	IPA	61	I	STS0	86	O	PL8F
12	O	OVD4	37	I	ICF	62	I	STS1	87	O	PM8F
13	O	OVD3	38	I	IVD	63	I	STRB	88	O	HMSK
14	O	OVD2	39	I	IHD	64	I	RD	89	O	VMASK
15	—	GND	40	—	GND	65	—	GND	90	—	GND
16	O	OVD1	41	I	IVD0	66	I	CS	91	O	CK135
17	O	OVD0	42	I	IVD1	67	I	LPARA	92	O	IDEN
18	O	OHD	43	I	IVD2	68	I	SCK	93	O	PERR
19	O	OVD	44	I	IVD3	69	I	START	94	O	IFBUSY
20	O	OCF	45	I	IVD4	70	I	SIN	95	O	RCF
21	O	OPA	46	I	IVD5	71	O	SOUT	96	O	RDF
22	I	TST4	47	I	IVD6	72	O	RINT	97	O	RFM
23	I	TST5	48	I	IVD7	73	O	REND	98	O	RERR
24	I	TST6	49	I	IVD8	74	O	OSVI	99	O	G SAV
25	I	TST7	50	I	IVD9	75	I	ISVI	100	O	EXP0

41	IVD0	OVD0	17
42	IVD1	OVD1	16
43	IVD2	OVD2	14
44	IVD3	OVD3	13
45	IVD4	OVD4	12
46	IVD5	OVD5	7
47	IVD6	OVD6	6
48	IVD7	OVD7	5
49	IVD8	OVD8	2
50	IVD9	OVD9	1
39	IHD	OHD	18
38	IVD	OVD	19
37	ICF	OCF	20
36	IPA	OPA	21
31	ALLTH	PERR	93
51	DATA0	GSAV	99
52	DATA1		
55	DATA2	VGATE	81
56	DATA3	BGATE	82
57	DATA4	GVITC	80
58	DATA5	XCFI	83
59	DATA6	IDEN	92
60	DATA7	NT4F	84
		PL4F	85
32	ICAD0	PL8F	86
33	ICAD1	PM8F	87
34	ICAD2		
35	ICAD3	EXP0	100
		IFBUSY	94
70	SIN	SOUT	71
68	SCK		
67	LPARA	RVITC	76
61	STS0	RLTCH	77
62	STS1	RINT	72
63	STRB	REND	73
69	START	RCF	95
64	RD	RDF	96
66	CS	RFM	97
		RERR	98
26	HRTH	HMSK	88
75	ISVI	VMASK	89
		OSVI	74
30	27CK		
27	RESET	CK135	91
	TST0		
	TST1		
	TST2		
	TST3		
	TST4		
	TST5		
	TST6		
	TST7		
8			
9			
10			
11			
22			
23			
24			
25			

INPUT

27CK	: 27 MHz CLOCK
ALLTH	: ALL THOUGH MODE
CS	: CHIP SELECT (FOR PARALLEL INTERFACE)
HRTH	: READER THOUGH MODE
ICAD0 - ICAD3	: IC ADDRESS
ICF	: COLOR FRAMING
IHD	: HD
IPA	: PARITY
ISVI	: SLICED VITC
IVD0 - IVD9	: VIDEO DATA
IVD	: VD
LPARA	: PARARELL INTERFACE SELECT
RD	: READ (FOR PARALLEL INTERFACE)
RESET	: POWER ON RESET
SCK	: SERIAL INTERFACE CLOCK
SIN	: SERIAL DATA
START	: SERIAL INTERFACE START
STRB	: STROBE (FOR PARALLEL INTERFACE)
STS0, STS1	: STATUS 0, 1 (FOR PARALLEL INTERFACE)
TST0 - TST7	: FOR TEST 0-7

OUTPUT

BGATE	: BLANKING LINE PULSE
CK135	: 13.5 MHz CLOCK
EXP0	: EXPANDED OUTPUT PORT
GSAV	: (G) SAV PULSE
GVITC	: GNENRATED VITC
HMSK	: (R) VITC SEARCH LINE
IDEN	: IC INTERFACE ENABLE
IFBUSY	: INTERFACE BUSY
NT4F	: NTSC 4FIELD
OCF	: COLOR FRAMING
OHD	: HD
OPA	: PARITY
OSVI	: SLICED VITC
OVD0 - OVD9	: VIDEO DATA
OVD	: VD
PERR	: PARITY ERROR
PL4F	: PAL 4FIELD
PL8F	: PAL 8FIELD
PM8M	: PALM 8FIELD
RCF	: READ COLOR FRAME BIT
RDF	: READ DROP FRAME BIT
REND	: READ END
RERR	: READ ERROR SIGNAL
RFM	: READ FEILD MARK BIT
RINT	: READ END INTERRUPT
RLTCH	: READ VITC LATCH PULSE
RVITC	: READ VITC
SOUT	: SERIAL DATA
VGATE	: INSERT LINE PULSE
VMASK	: (R) EAV_SAV MASK PULSE
XCFI	: COLOR FRAMING INFORMATION

INPUT/OUTPUT

DATA0 - DATA7 : PARARELL INTERFACE DATA BUS