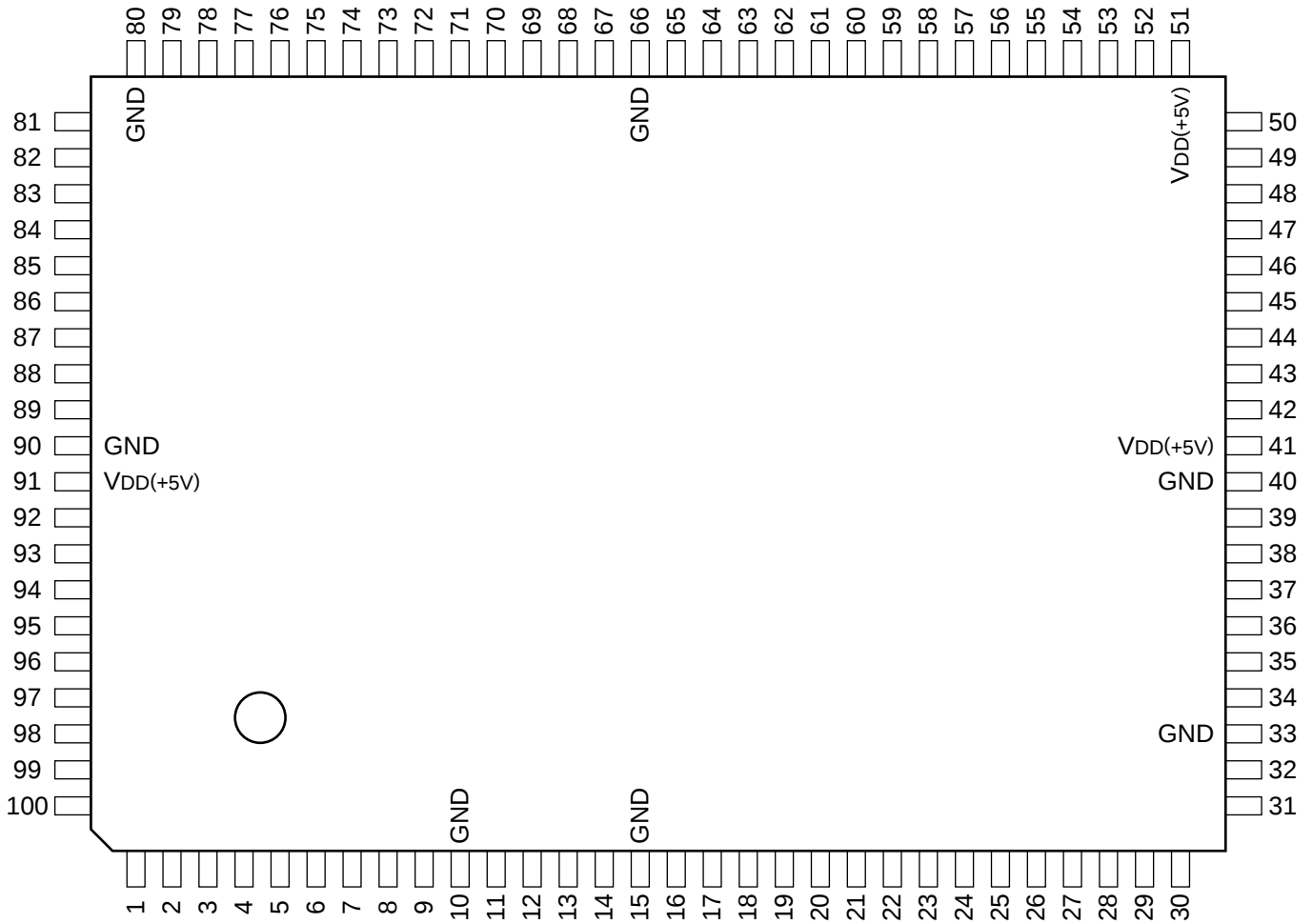

C-MOS RF BLOCK DIGITAL CONTROL INTERFACE

-TOP VIEW-



(V_{DD} = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	SACN	26	O	$\overline{\text{ICS1}}$	51	—	V _{DD}	76	I/O	D3L
2	I	SAAD	27	O	$\overline{\text{ICS0}}$	52	I	CP14	77	I/O	D2L
3	I	SBCN	28	O	D GND	53	I	CP13	78	I/O	D1L
4	I	SBAD	29	O	XIN1	54	I	CP12	79	I/O	D0L
5	I	TES1	30	I	XIN0	55	I	CP11	80	—	GND
6	I	MACN	31	O	XOU2	56	I	CP10	81	O	$\overline{\text{WEM}}$
7	I	MAAD	32	O	XOUT	57	I	CP9	82	O	$\overline{\text{OEM}}$
8	I	MBCN	33	—	GND	58	I	CP8	83	O	$\overline{\text{CEM}}$
9	I	MBAD	34	O	$\overline{\text{MPS1}}$	59	I	A7L	84	I/O	D7M
10	—	GND	35	O	$\overline{\text{MPS0}}$	60	I	A6L	85	I/O	D6M
11	O	REAC	36	O	RFIF	61	I	A5L	86	I/O	D5M
12	O	DACE	37	O	SRAM	62	I	A4L	87	I/O	D4M
13	O	SCK	38	O	$\overline{\text{CRAM}}$	63	I	A3L	88	I/O	D3M
14	O	SOUT	39	O	RACS	64	I	A2L	89	I/O	D2M
15	—	GND	40	—	GND	65	I	A1L	90	—	GND
16	O	STB3	41	—	V _{DD}	66	—	GND	91	—	V _{DD}
17	O	STB2	42	O	$\overline{\text{ROSK}}$	67	I	A0L	92	I/O	D1M
18	O	STB1	43	O	$\overline{\text{ROSJ}}$	68	O	$\overline{\text{WAIL}}$	93	I/O	D0M
19	O	NVRM	44	O	$\overline{\text{ROCS}}$	69	I	$\overline{\text{WRL}}$	94	O	A6M
20	O	$\overline{\text{ICS7}}$	45	I	CLK	70	I	$\overline{\text{WRD}}$	95	O	A5M
21	O	$\overline{\text{ICS6}}$	46	O	C OUT	71	I	$\overline{\text{MREQ}}$	96	O	A4M
22	O	$\overline{\text{ICS5}}$	47	I	SYCK	72	I/O	D7L	97	O	A3M
23	O	$\overline{\text{ICS4}}$	48	I	PRST	73	I/O	D6L	98	O	A2M
24	O	$\overline{\text{ICS3}}$	49	I	CP19	74	I/O	D5L	99	O	A1M
25	O	$\overline{\text{ICS2}}$	50	I	CP17	75	I/O	D4L	100	O	A0M

68	WAIL	SCK	13
69	WRL	SOUT	14
70	WRD	STB1	18
71	MREQ	STB2	17
		STB3	16
79	D0L		
78	D1L	WEM	81
77	D2L	OEM	82
76	D3L	CEM	83
75	D4L	A0M	100
74	D5L	A1M	99
73	D6L	A2M	98
72	D7L	A3M	97
		A4M	96
67	A0L	A5M	95
65	A1L	A6M	94
64	A2L		
63	A3L	D0M	93
62	A4L	D1M	92
61	A5L	D2M	89
60	A6L	D3M	88
59	A7L	D4M	87
		D5M	86
58	CP8	D6M	85
57	CP9	D7M	84
56	CP10		
55	CP11	NVRM	19
54	CP12	RFIF	36
53	CP13	SRAM	37
52	CP14	CRAM	38
50	CP17	RACS	39
49	CP19	ROSK	42
		ROSJ	43
48	PRST	ROCS	44
47	SYCK	MPS0	35
46	COUT	MPS1	34
45	CLK	ICS0	27
5	TES1	ICS1	26
		ICS2	25
		ICS3	24
		ICS4	23
		ICS5	22
		ICS6	21
		ICS7	20
1	SACN	REAC	11
2	SAAD	DACE	12
3	SBCN		
4	SBAD		
6	MACN		
7	MAAD		
8	MBCN		
9	MBAD		
30	XIN0	XOUT	32
29	XIN1	XOU2	31
28	DGND		

INPUT

A0L - A7L ; CPU ADDRESS BUS(LOW ORDER)
 CLK ; CPU CLOCK
 CP8 - CP14, CP17, CP19 ; CPU ADDRESS BUS(UPPER ORDER)
 D GND ; CRYSTAL OSCILLATOR CIRCUIT GND
 MAAD ; METRIC PULSE INPUT ADV - AC
 MACN ; METRIC PULSE INPUT CONF1 - AC
 MBAD ; METRIC PULSE INPUT ADV - BD
 MBCN ; METRIC PULSE INPUT CONF1 - BD
 MREQ ; CPU MEMORY REQUEST
 PRST ; POWER ON RESET
 SAAD ; HEAD SELECT INPUT ADV - AC
 SACN ; HEAD SELECT INPUT CONF1 - AC
 SBAD ; HEAD SELECT INPUT ADV - BD
 SBCN ; HEAD SELECT INPUT CONF1 - BD
 SYCK ; SYSTEM CLOCK
 WRD ; CPU READ STROBE
 WRL ; CPU WRITE STROBE
 XIN0 ; CRYSTAL INPUT 0
 XIN1 ; CRYSTAL INPUT 1

OUTPUT

A0M - A6M ; EXTERNAL RAM ADDRESS BUS
 CEM ; EXTERNAL RAM CHIP SELECT
 COUT ; CKL INPUT 2FREQUENCY
 CRAM ; ADDRESS DECODER
 DACE ; METRIC DATA COUNT END
 ICS0 - ICS7 ; ADDRESS DECODER
 MPS0, MPS1 ; ADDRESS DECODER
 NVRM ; ADDRESS DECODER
 OEM ; EXTERNAL OUTPUT ENABLE
 RACS ; ADDRESS DECODER
 REAC ; METRIC DATA A / C DETECTOR
 RFIF ; ADDRESS DECODER
 ROCS ; ADDRESS DECODER
 ROSJ ; ADDRESS DECODER
 ROSK ; ADDRESS DECODER
 SCK ; SERIAL TRANSMISSION CLOCK
 SOUT ; SERIAL TRANSMISSION DATA
 STB1 - STB3 ; SERIAL TRANSMISSION STROBE1 - 3
 SRAM ; ADDRESS DECODER
 WAIL ; CPU WAIT
 WEM ; EXTERNAL RAM WRITE STROBE
 XOUT ; CRYSTAL OSCILLATOR OUTPUT
 XOU2 ; CRYSTAL OSCILLATOR OUTPUT 2

INPUT/OUTPUT

D0L - D7L ; CPU DATA BUS
 D0M - D7M ; EXTERNAL RAM DATA BUS

