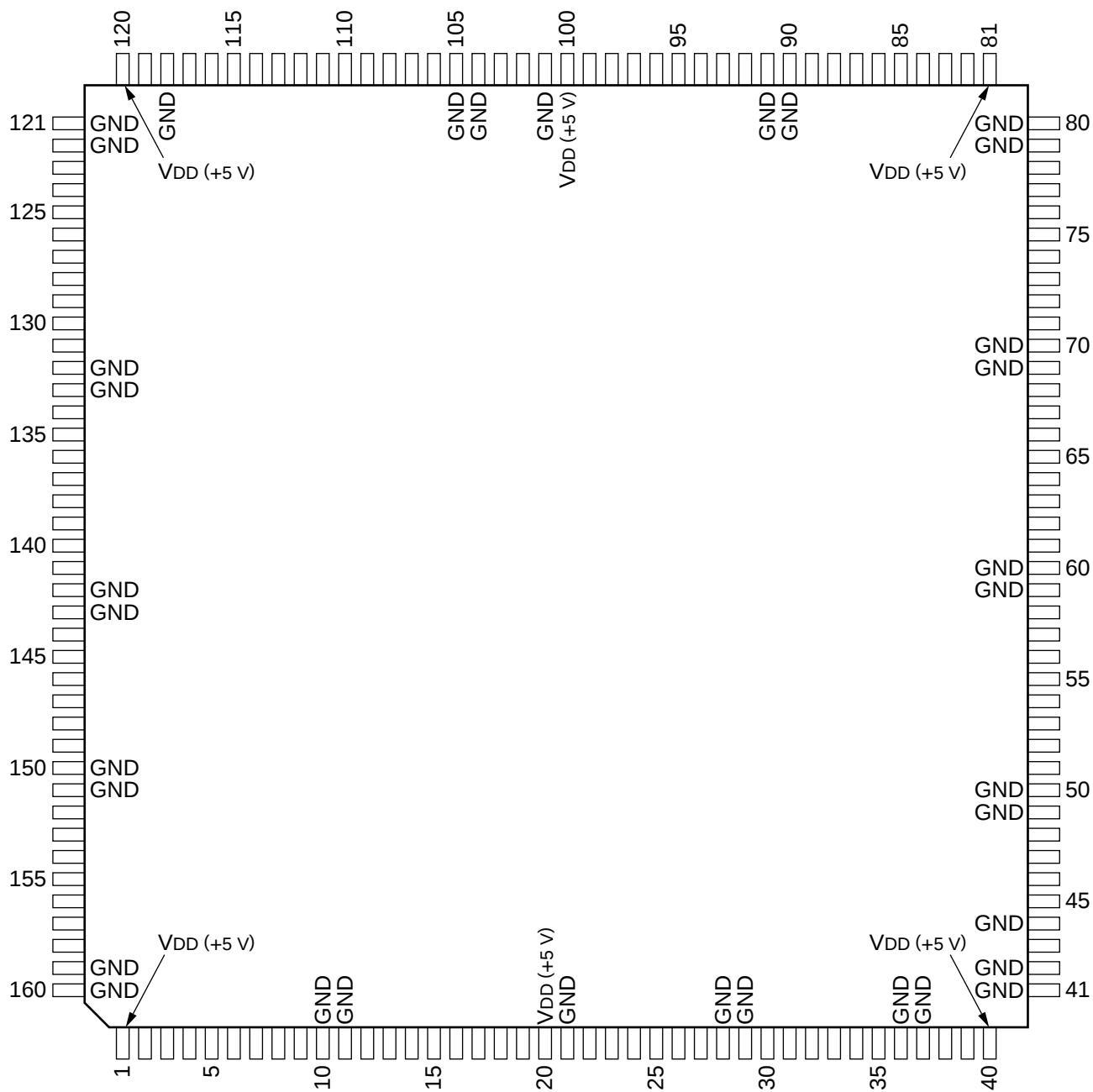


C-MOS GATE ARRAY

—TOP VIEW—



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	VDD	33	O	DA2	65	O	PDO12	97	I	CPUA5	129	O	NIFCSO
2	I	SD7	34	O	DA1	66	O	PDO13	98	I	CPUA6	130	O	NIFWR
3	I	SD6	35	O	DA0	67	O	PDO14	99	I	CPUA7	131	O	NIFRD
4	I	SD5	36	—	GND	68	O	PDO15	100	—	VDD	132	—	GND
5	I	SD4	37	—	GND	69	—	GND	101	—	GND	133	—	GND
6	I	SD3	38	O	NCAS	70	—	GND	102	I	CPUA8	134	I/O	IFD0
7	I	SD2	39	O	NRAS	71	O	PDO16	103	I	CPUA9	135	I/O	IFD1
8	I	SD1	40	—	VDD	72	O	PDO17	104	—	GND	136	I/O	IFD2
9	I	SD0	41	—	GND	73	O	PDO18	105	—	GND	137	I/O	IFD3
10	—	GND	42	—	GND	74	O	PDO19	106	O	NSTOP	138	I/O	IFD4
11	—	GND	43	O	NDWE	75	O	PDO20	107	O	CPUREF	139	I/O	IFD5
12	I/O	DD7	44	—	GND	76	O	PDO21	108	O	NCPUINT2	140	I/O	IFD6
13	I/O	DD6	45	O	DLYCLK0	77	O	PDO22	109	O	NCPUINT1	141	I/O	IFD7
14	I/O	DD5	46	O	DLYCLK1	78	O	PDO23	110	O	NCPUINT0	142	—	GND
15	I/O	DD4	47	O	DLYCLK2	79	—	GND	111	I	NDMAEN	143	—	GND
16	I/O	DD3	48	O	DLYCLK3	80	—	GND	112	I	NAREALD	144	O	IFA0
17	I/O	DD2	49	—	GND	81	—	VDD	113	I	CPUMOD	145	O	IFA1
18	I/O	DD1	50	—	GND	82	I/O	CPUD7	114	I	CUP	146	O	IFA2
19	I/O	DD0	51	O	PDO0	83	I/O	CPUD6	115	O	DMABSY	147	O	IFA3
20	—	VDD	52	O	PDO1	84	I/O	CPUD5	116	I	NCPURD	148	O	IFA4
21	—	GND	53	O	PDO2	85	I/O	CPUD4	117	I	NCPUWR	149	O	IFA5
22	O	DA11	54	O	PDO3	86	I/O	CPUD3	118	—	GND	150	—	GND
23	O	DA10	55	O	PDO4	87	I/O	CPUD2	119	I	CLK	151	—	GND
24	O	DA9	56	O	PDO5	88	I/O	CPUD1	120	—	VDD	152	I	NINT0
25	O	DA8	57	O	PDO6	89	I/O	CPUD0	121	—	GND	153	I	NINT1
26	O	DA7	58	O	PDO7	90	—	GND	122	—	GND	154	I	NINT2
27	O	DA6	59	—	GND	91	—	GND	123	I	NRES	155	I	NSTB
28	—	GND	60	—	GND	92	I	CPUA0	124	I	NIFCSI	156	O	NSRED
29	—	GND	61	O	PDO8	93	I	CPUA1	125	I	NCPUCS2	157	O	NSDACK
30	O	DA5	62	O	PDO9	94	I	CPUA2	126	I	NCPUCS1	158	I	SDRQ
31	O	DA4	63	O	PDO10	95	I	CPUA3	127	I	NCPUCS0	159	—	GND
32	O	DA3	64	O	PDO11	96	I	CPUA4	128	I	TEST	160	—	GND