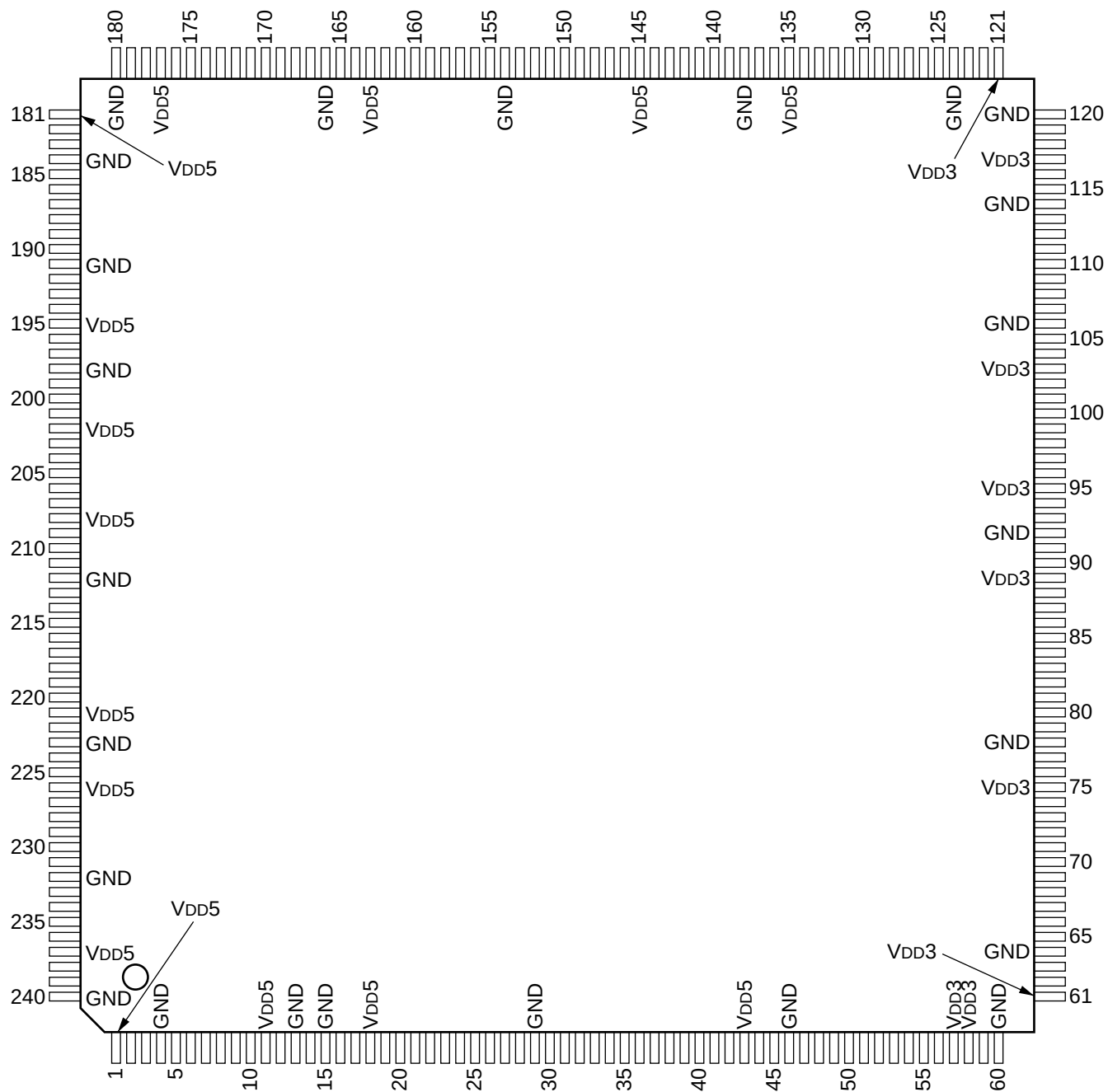


C-MOS DYNAMIC RAM CONTROL

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	V _{DD} 5	49	O	DRAMADR2	97	I/O	GDRAMD1	145	—	V _{DD} 5	193	O	GOD1
2	I	RIN6	50	O	DRAMADR3	98	I/O	GDRAMD2	146	O	PGD0	194	O	GOD2
3	I	RIN7	51	O	DRAMADR4	99	I/O	GDRAMD3	147	O	PGD1	195	—	V _{DD} 5
4	—	GND	52	O	DRAMADR5	100	I/O	GDRAMD4	148	O	PGD2	196	O	GOD3
5	I	RES	53	O	DRAMADR6	101	I/O	GDRAMD5	149	O	PGD3	197	O	GOD4
6	I	TEST	54	O	DRAMADR7	102	I/O	GDRAMD6	150	O	PGD4	198	—	GND
7	I	VD	55	O	DRAMADR8	103	—	V _{DD} 3	151	O	PGD5	199	O	GOD5
8	I	HD	56	O	DRAMADR9	104	I/O	GDRAMD7	152	O	PGD6	200	O	GOD6
9	I	VBLNKV	57	—	V _{DD} 3	105	I/O	GDRAMD8	153	O	PGD7	201	O	GOD7
10	I	VBLNKR	58	—	V _{DD} 3	106	—	GND	154	—	GND	202	—	V _{DD} 5
11	—	V _{DD} 5	59	O	DRAMCAS	107	I/O	GDRAMD9	155	O	PBD0	203	O	ROD0
12	I	CLK	60	—	GND	108	I/O	GDRAMD10	156	O	PBD1	204	O	ROD1
13	—	GND	61	—	V _{DD} 3	109	I/O	GDRAMD11	157	O	PBD2	205	O	ROD2
14	I	HCLK	62	O	DRAMRAS0	110	I/O	GDRAMD12	158	O	PBD3	206	O	ROD3
15	—	GND	63	O	DRAMRAS23	111	I/O	GDRAMD13	159	O	PBD4	207	O	ROD4
16	I	RD _n	64	—	GND	112	I/O	GDRAMD14	160	O	PBD5	208	O	ROD5
17	I	WR _n	65	O	DRAMRAS45	113	I/O	GDRAMD15	161	O	PBD6	209	—	V _{DD} 5
18	—	V _{DD} 5	66	O	DRAMRAS67	114	—	GND	162	O	PBD7	210	O	ROD6
19	I	CS0 _n	67	O	DRAMRAS0	115	I/O	BDRAMD0	163	—	V _{DD} 5	211	O	ROD7
20	I	CS1 _n	68	O	DRAMRAS1	116	I/O	BDRAMD1	164	O	PDCLK1	212	—	GND
21	I	CS2	69	O	DRAMRAS2	117	—	V _{DD} 3	165	O	PDCLK2	213	I	BIN0
22	I	CS3	70	O	DRAMRAS3	118	I/O	BDRAMD2	166	—	GND	214	I	BIN1
23	I	ADR0	71	O	DRAMRAS4	119	I/O	BDRAMD3	167	O	PDCLK3	215	I	BIN2
24	I	ADR1	72	O	DRAMRAS5	120	—	GND	168	O	PDCLK4	216	I	BIN3
25	I	ADR2	73	O	DRAMRAS6	121	—	V _{DD} 3	169	O	PRTACT	217	I	BIN4
26	I	ADR3	74	O	DRAMRAS7	122	I/O	BDRAMD4	170	O	PRTBUSY	218	I	BIN5
27	I	ADR4	75	—	V _{DD} 3	123	I/O	BDRAMD5	171	O	LFTCH _n _MON	219	I	BIN6
28	I	ADR5	76	I/O	RDRAMD0	124	—	GND	172	O	RABUSY	220	I	BIN7
29	—	GND	77	I/O	RDRAMD1	125	I/O	BDRAMD6	173	O	REFRESH	221	—	V _{DD} 5
30	I/O	D0	78	—	GND	126	I/O	BDRAMD7	174	O	HREFERENCE	222	I	GIN0
31	I/O	D1	79	I/O	RDRAMD2	127	I/O	BDRAMD8	175	O	MONITMG	223	—	GND
32	I/O	D2	80	I/O	RDRAMD3	128	I/O	BDRAMD9	176	O	PRTTMG	224	I	GIN1
33	I/O	D3	81	I/O	RDRAMD4	129	I/O	BDRAMD10	177	—	V _{DD} 5	225	I	GIN2
34	I/O	D4	82	I/O	RDRAMD5	130	I/O	BDRAMD11	178	O	VBLNK	226	—	V _{DD} 5
35	I/O	D5	83	I/O	RDRAMD6	131	I/O	BDRAMD12	179	O	BLNK	227	I	GIN3
36	I/O	D6	84	I/O	RDRAMD7	132	I/O	BDRAMD13	180	—	GND	228	I	GIN4
37	I/O	D7	85	I/O	RDRAMD8	133	I/O	BDRAMD14	181	—	V _{DD} 5	229	I	GIN5
38	I	SRC_MEMO _n	86	I/O	RDRAMD9	134	I/O	BDRAMD15	182	O	BOD0	230	I	GIN6
39	I	FTCH _n _MONI	87	I/O	RDRAMD10	135	—	V _{DD} 5	183	O	BOD1	231	I	GIN7
40	I	RATRG	88	I/O	RDRAMD11	136	O	PRD0	184	—	GND	232	—	GND
41	I	PDREQ	89	—	V _{DD} 3	137	O	PRD1	185	O	BOD2	233	I	RIN0
42	I	TRG4IN1	90	I/O	RDRAMD12	138	—	GND	186	O	BOD3	234	I	RIN1
43	—	V _{DD} 5	91	I/O	RDRAMD13	139	O	PRD2	187	O	BOD4	235	I	RIN2
44	O	DRAMOEn	92	—	GND	140	O	PRD3	188	O	BOD5	236	I	RIN3
45	O	DRAMWEn	93	I/O	RDRAMD14	141	O	PRD4	189	O	BOD6	237	—	V _{DD} 5
46	—	GND	94	I/O	RDRAMD15	142	O	PRD5	190	O	BOD7	238	I	RIN4
47	O	DRAMADR0	95	—	V _{DD} 3	143	O	PRD6	191	—	GND	239	I	RIN5
48	O	DRAMADR1	96	I/O	GDRAMD0	144	O	PRD7	192	O	GOD0	240	—	GND