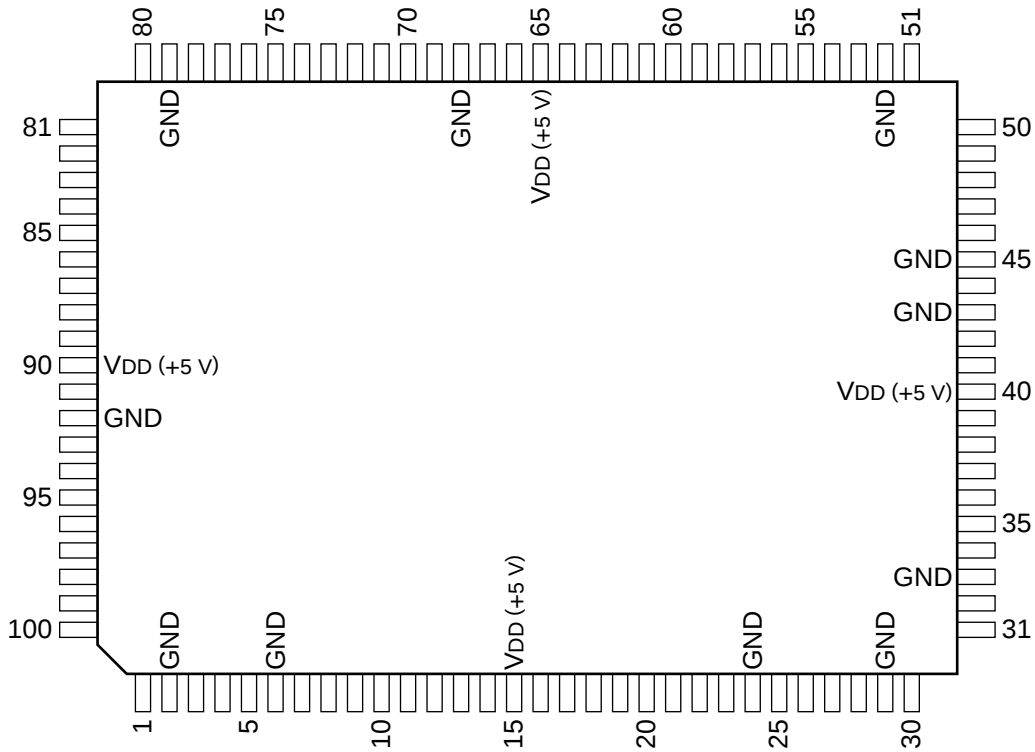

C-MOS PICTURE QUALITY CONTROL

—TOP VIEW—



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	NRES	21	I/O	DB5	41	O	PD1	61	O	STS8	81	I	FD0
2	—	GND	22	I/O	DB6	42	O	PD0	62	O	STS9	82	I	FD1
3	I	NCS	23	I/O	DB7	43	—	GND	63	O	STS10	83	I	FD2
4	I	NRD	24	—	GND	44	O	PDCK	64	O	STS11	84	I	FD3
5	I	NWR	25	I	NHAIN	45	—	GND	65	—	VDD	85	I	FD4
6	—	GND	26	I	PPIN	46	O	PDACT	66	O	XO	86	I	FD5
7	I	ADB0	27	O	STS0	47	I	PDREQ	67	I	XI	87	I	FD6
8	I	ADB1	28	O	STS1	48	I	TIN1	68	—	GND	88	I	FD7
9	I	ADB2	29	—	GND	49	I	TIN2	69	O	STS12	89	I	TIN9
10	I	ADB3	30	I	TIN0	50	I	TIN3	70	O	STS13	90	—	VDD
11	I	ADB4	31	O	PPOUT	51	I	TIN4	71	O	STS14	91	I	FDCK
12	I	ADB5	32	O	NHAOUT	52	—	GND	72	O	STS15	92	—	GND
13	I	ADB6	33	—	GND	53	I	TIN5	73	O	STS16	93	O	FDREQ
14	I	ADB7	34	O	PD7	54	I	TIN6	74	O	STS17	94	I	TIN10
15	—	VDD	35	O	PD6	55	O	STS2	75	O	STS18	95	I	TIN11
16	I/O	DB0	36	O	PD5	56	O	STS3	76	I	HNUM0	96	I	TIN12
17	I/O	DB1	37	O	PD4	57	O	STS4	77	I	HNUM1	97	I	TIN13
18	I/O	DB2	38	O	PD3	58	O	STS5	78	I	TIN7	98	I	NTEST
19	I/O	DB3	39	O	PD2	59	O	STS6	79	—	GND	99	I	NPREHT
20	I/O	DB4	40	—	VDD	60	O	STS7	80	I	TIN8	100	I	TIN14