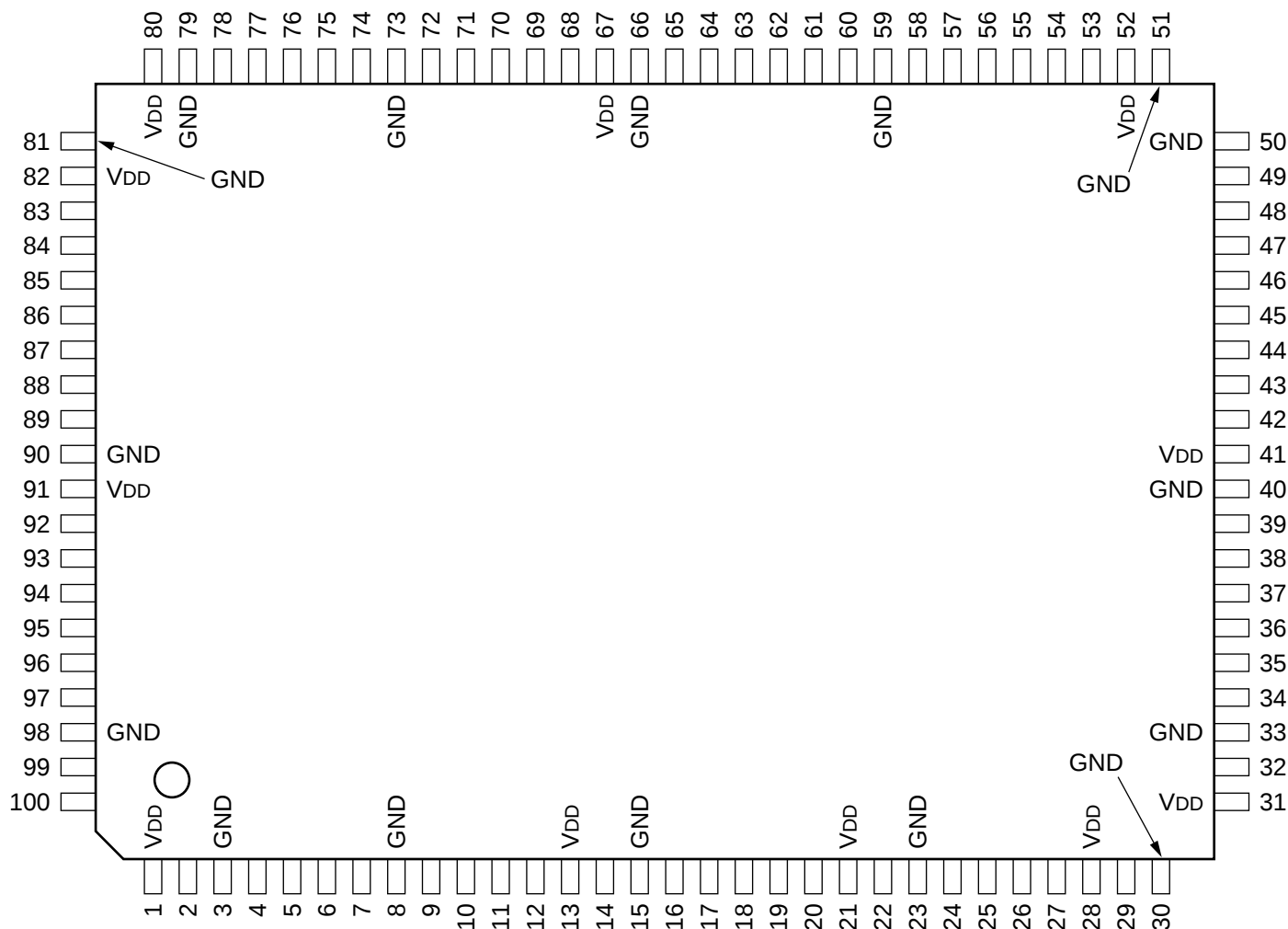


C-MOS CLOCK/TIMING GENERATOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	26	I	MCSI	51	—	GND	76	O	EXWR
2	I	RTCK	27	I	MCCK	52	—	V _{DD}	77	O	EXWE
3	—	GND	28	—	V _{DD}	53	O	PVTRP	78	O	EXRR
4	I	VAS	29	I	RDCK	54	O	PTRK1	79	—	GND
5	I	TEN	30	—	GND	55	O	PFLT1	80	—	V _{DD}
6	I	TEST	31	—	V _{DD}	56	O	PFLT2	81	—	GND
7	I	FLTA0	32	I	PDCK	57	O	PFLT3	82	—	V _{DD}
8	—	GND	33	—	GND	58	O	PFLT4	83	O	EXRE
9	I	RFLT0	34	I	RFOE	59	—	GND	84	O	EXOE
10	I	RTRK0	35	I	RHD	60	O	WEVN	85	O	PXWR
11	I	PFLT0	36	I	BPFS0	61	O	FOE	86	O	PXWE
12	I	PTRK0	37	I	RIEV1	62	O	VD	87	O	PXRR
13	—	V _{DD}	38	I	PIEV1	63	O	QXS1	88	O	PXRE
14	I	PTCK	39	O	FLTA1	64	O	MCS0	89	O	PXOE
15	—	GND	40	—	GND	65	O	BPFS1	90	—	GND
16	I	PDTK0	41	—	V _{DD}	66	—	GND	91	—	V _{DD}
17	I	PDFL0	42	O	RTRK1	67	—	V _{DD}	92	I	EE
18	I	PDFL1	43	O	RFLT1	68	O	DETW	93	I	PAL
19	I	RDFL0	44	O	RFLT2	69	O	BPFS0	94	I	NSE
20	I	RDTK1	45	O	RFLT3	70	O	RXWR	95	I	HSPD
21	—	V _{DD}	46	O	RFLT4	71	O	RXWE	96	I	OREN
22	I	RHCK	47	O	RVFRP	72	O	RXRR	97	I	ORDS
23	—	GND	48	O	RVTRP	73	—	GND	98	—	GND
24	I	RDFL1	49	O	PVFRP	74	O	RXRE	99	I	CLR
25	I	MCCS	50	—	GND	75	O	RXOE	100	I	RSTP