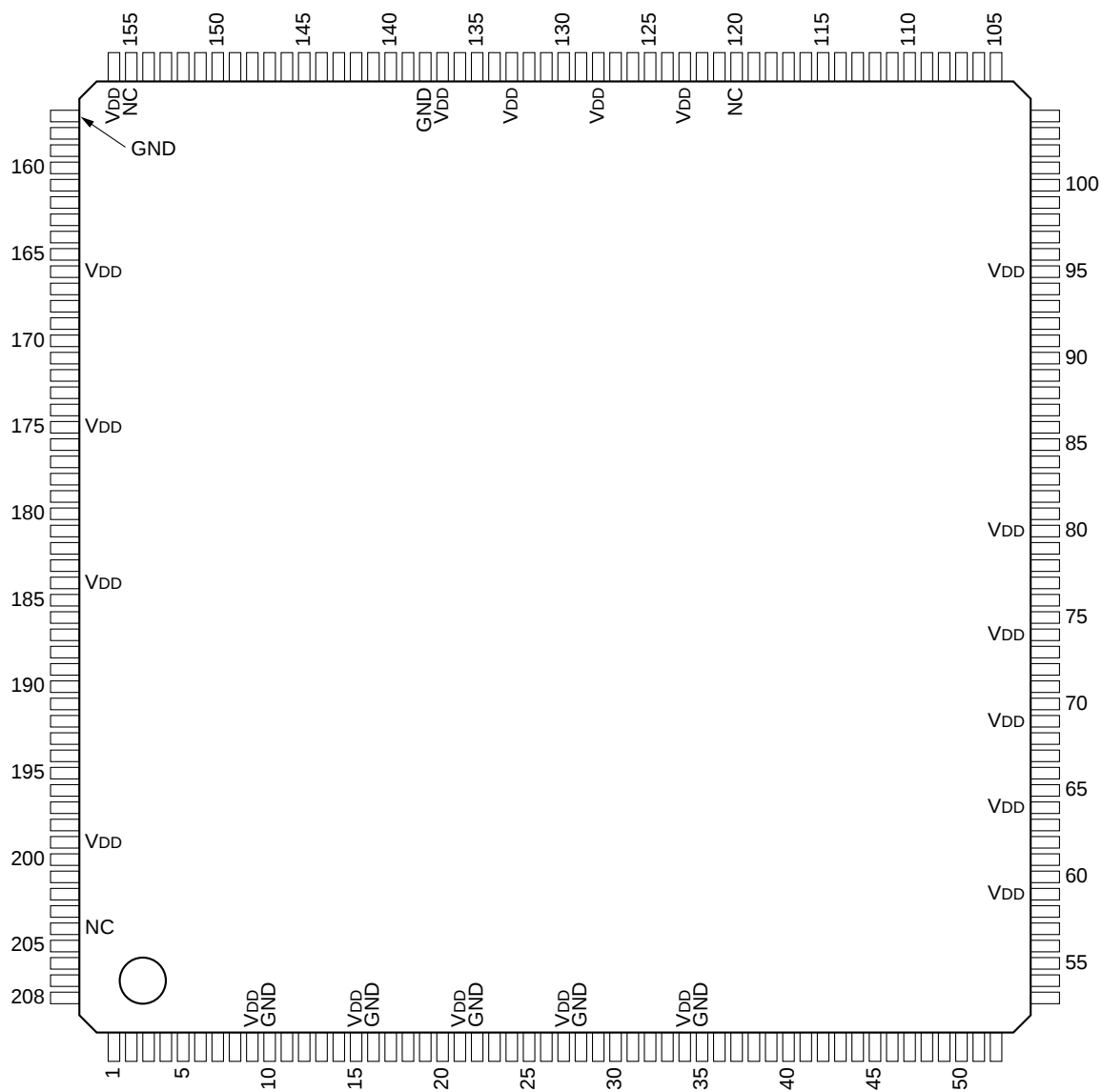


C-MOS INDEX PICTURE

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	RDY0	43	O	TSTOUTC2	85	O	XOEMC8	127	—	GND	169	I	JSYY
2	I	RDY1	44	O	TSTOUTC3	86	O	XOEMC7	128	O	ADRS13	170	O	BUSC
3	I	RDY2	45	I	RDC0	87	O	XOEMC6	129	O	ADRS12	171	I	JOOE
4	I	RDY3	46	I	RDC1	88	O	XOEMC5	130	O	ADRS11	172	I	LOFF
5	I	RDY4	47	I	RDC2	89	O	XOEMC4	131	O	ADRS10	173	I	SPCK
6	I	RDY5	48	I	RDC3	90	O	XOEMC3	132	O	ADRS9	174	O	BUSCC
7	I	RDY6	49	I	RDC4	91	O	XOEMC2	133	O	ADRS8	175	I	JSYC
8	I	RDY7	50	I	RDC5	92	O	XOEMC1	134	O	ADRS7	176	—	V _{DD}
9	—	V _{DD}	51	I	RDC6	93	O	XOEMC0	135	O	ADRS6	177	—	GND
10	—	GND	52	I	RDC7	94	—	V _{DD}	136	O	ADRS5	178	O	COUT0
11	O	RDAD0	53	—	V _{DD}	95	—	GND	137	—	V _{DD}	179	O	COUT1
12	O	RDAD1	54	—	GND	96	O	PCOKC	138	—	GND	180	O	COUT2
13	O	RDAD2	55	O	RDADC16	97	O	PCOK	139	O	ADRS4	181	O	COUT3
14	O	RDAD3	56	O	RDADC15	98	I	XSCK	140	O	ADRS3	182	O	COUT4
15	—	V _{DD}	57	O	RDADC14	99	I	SI	141	O	ADRS2	183	O	COUT5
16	—	GND	58	O	RDADC13	100	I	XCS	142	O	ADRS1	184	O	COUT6
17	O	RDAD4	59	—	V _{DD}	101	O	RDOKC	143	O	ADRS0	185	O	COUT7
18	O	RDAD5	60	—	GND	102	O	RDOK	144	I	TST1-8	186	—	V _{DD}
19	O	RDAD6	61	O	RDADC12	103	I	XBE-R	145	I	TST1-7	187	—	GND
20	O	RDAD7	62	O	RDADC11	104	I	XREAD	146	I	TST1-6	188	O	XOEM0
21	—	V _{DD}	63	O	RDADC10	105	I	XNTSC	147	I	TST1-5	189	O	XOEM1
22	—	GND	64	O	RDADC9	106	—	NC	148	I	TST1-4	190	O	XOEM2
23	O	RDAD8	65	—	V _{DD}	107	I	XRESET	149	I	TST1-3	191	O	XOEM3
24	O	RDAD9	66	—	GND	108	I	PSAVE	150	I	TST1-2	192	O	XOEM4
25	O	RDAD10	67	O	RDADC8	109	—	V _{DD}	151	I	TST1-1	193	O	XOEM5
26	O	RDAD11	68	O	RDADC7	110	—	GND	152	I	TST1-0	194	O	XOEM6
27	—	V _{DD}	69	O	RDADC6	111	O	SPDT7	153	I	BU _P	195	O	XOEM7
28	—	GND	70	O	RDADC5	112	O	SPDT6	154	I	TRCK	196	O	XOEM8
29	O	RDAD12	71	—	V _{DD}	113	O	SPDT5	155	—	NC	197	O	XOEM9
30	O	RDAD13	72	—	GND	114	O	SPDT4	156	—	V _{DD}	198	O	XOEM10
31	O	RDAD14	73	O	RDADC4	115	—	V _{DD}	157	—	GND	199	O	XOEM11
32	O	RDAD15	74	O	RDADC3	116	—	GND	158	O	YOUT0	200	O	XOEM12
33	O	RDAD16	75	O	RDADC2	117	O	SPDT3	159	O	YOUT1	201	O	XOEM13
34	—	V _{DD}	76	O	RDADC1	118	O	SPDT2	160	O	YOUT2	202	—	V _{DD}
35	—	GND	77	O	RDADC0	119	O	SPDT1	161	O	YOUT3	203	—	GND
36	I	TSTMODE	78	—	V _{DD}	120	O	SPDT0	162	O	YOUT4	204	O	TSTOUTY0
37	I	TSTIN0	79	—	GND	121	—	V _{DD}	163	O	YOUT5	205	O	TSTOUTY1
38	I	TSTIN1	80	O	XOEMC13	122	—	GND	164	O	YOUT6	206	O	TSTOUTY2
39	I	TSTIN2	81	O	XOEMC12	123	O	WTED	165	O	YOUT7	207	O	TSTOUTY3
40	I	TSTIN3	82	O	XOEMC11	124	I	XWRITE	166	—	V _{DD}	208	—	NC
41	O	TSTOUTC0	83	O	XOEMC10	125	O	XWE-S	167	—	GND			
42	O	TSTOUTC1	84	O	XOEMC9	126	—	V _{DD}	168	I	BKRC			

INPUT

ADRS0 - ADRS13	: READ ADDRESS from SRAM-S
BUF	: REFERENCE PULSE
BKRC	: VTR REC MODE
JOOE	: ODD/EVEN DETECT PULSE
JSYC	: C SYNCHRONOUS SIGNAL
JSYY	: Y SYNCHRONOUS SIGNAL
LOFF	: JOOE INVERSION
PSAVE	: POWER SAVE
RDC0 - RDC7	: C DATA
RDY0 - RDY7	: Y DATA
SI	: SERIAL COMMUNICATION DATA
SPCK	: READ CONTROL CLOCK
TRCK	: WRITE CONTROL CLOCK
TSTIN0 - TSTIN3	: TEST SIGNAL
TSTMODE	: TEST MODE
TST1-0 - TST1-8	: 9bit PARALLEL DATA
XBE-R	: OUTPUT CONTROL for XOE-M and ADR17
XCS	: SERIAL COMMUNICATION CHIP SELECT
XNTSC	: NTSC/PAL MODE SELECT
XREAD	: READ COMMAND from SRAM-M
XRESET	: SRAM RESET
XSCK	: SERIAL COMMUNICATION CLOCK
XWE-S	: WRITE ENABLE for SRAM-S
XWRITE	: WRITE COMMAND to SRAM-S

OUTPUT

BUSC	: DATA BUS SELECT SWITCH
BUSCC	: DATA BUS SELECT SWITCH
COUT0 - COUT7	: C DATA
PCOK	: Y SERIAL COMMUNICATION READ OK FLAG
PCOKC	: C SERIAL COMMUNICATION READ OK FLAG
RDAD0 - RDAD16	: Y READ ADDRESS for SRAM-M
RDADC0 - RDADC16	: C READ ADDRESS for SRAM-M
RDOK	: Y READ END FLAG from SRAM-M
RDOKC	: C READ END FLAG from SRAM-M
SPDT0 - SPDT7	: OUTPUT DATA from SRAM-S
TSTOUTC0 - TSTOUTC3	: C TEST SIGNAL
TSTOUTY0 - TSTOUTY3	: Y TEST SIGNAL
WTED	: WRITE END FLAG to SRAM-S
XOEM0 - XOEM13	: Y OUTPUT ENABLE for SRAM-M
XOEMC0 - XOEMC13	: C OUTPUT ENABLE for SRAM-M
YOUT0 - YOUT7	: Y DATA

