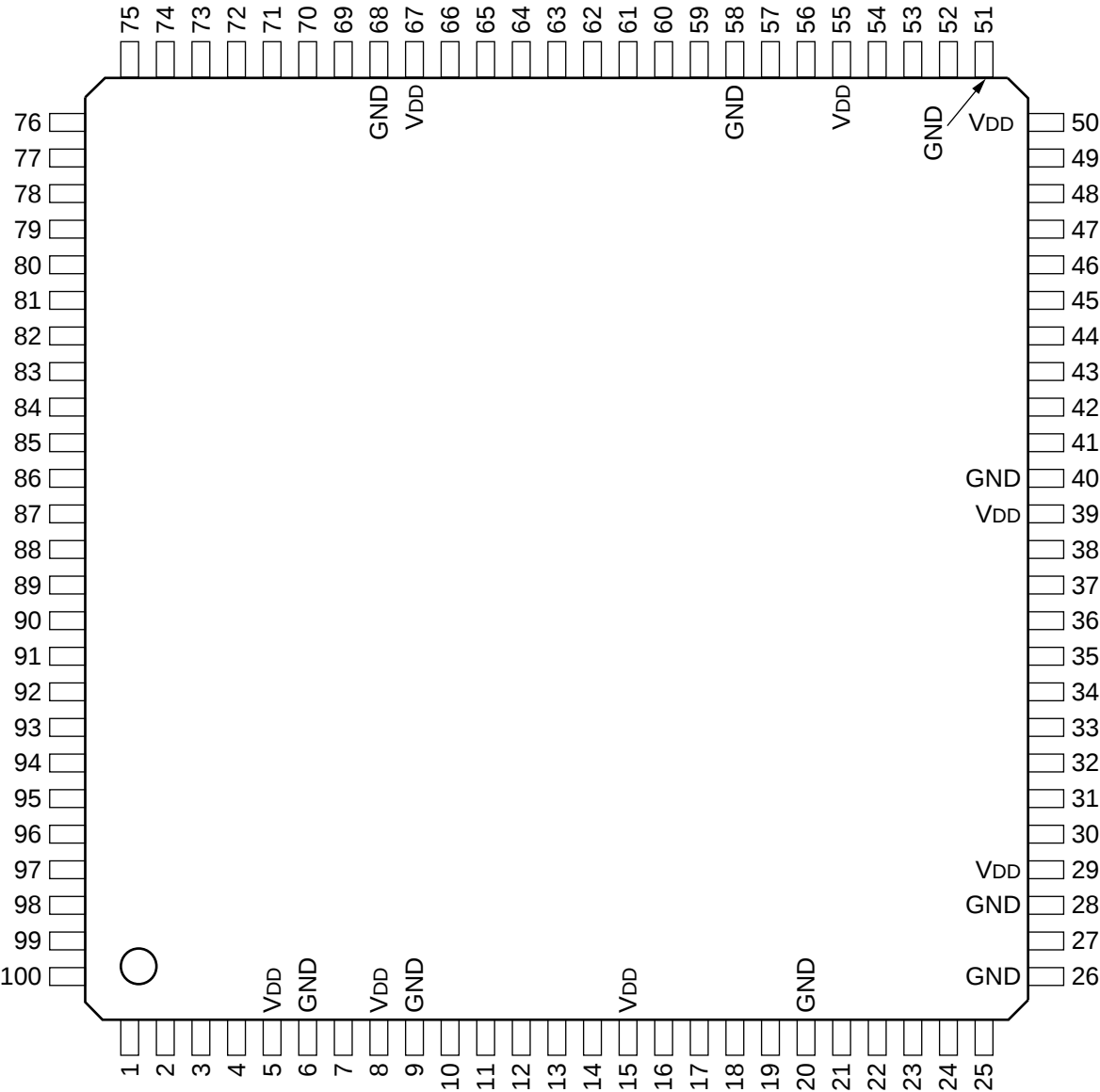


C-MOS VIDEO AUXILIARY DATA READ/WRITE

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	TST-CTL1	26	—	GND	51	—	GND	76	I	TST-AA7
2	I	TST-CTL2	27	I	CK	52	I	R/P SEL	77	I	TST-AB0
3	I	TST-CTL3	28	—	GND	53	I	ADV0	78	I	TST-AB1
4	I	TST-CTL4	29	—	V _{DD}	54	I	ADV1	79	I	TST-AB2
5	—	V _{DD}	30	I	SYS SEL	55	—	V _{DD}	80	I	TST-AB3
6	—	GND	31	I/O	SFDT15	56	I	TRK-PLS	81	I	TST-AB4
7	I	8-B COM	32	I/O	SFDT14	57	I	FLD-PLS	82	I	TST-AB5
8	—	V _{DD}	33	I/O	SFDT13	58	—	GND	83	I	TST-AB6
9	—	GND	34	I/O	SFDT12	59	O	PERI CNT0	84	I	TST-AB7
10	I	R _{ST}	35	I/O	SFDT11	60	O	PERI CNT1	85	I	TST-D0
11	I	SCK	36	I/O	SFDT10	61	O	PERI CNT2	86	I	TST-D1
12	O	S-DATA OUT	37	I/O	SFDT9	62	O	PERI CNT3	87	I	TST-D2
13	I	S-DATA IN	38	I/O	SFDT8	63	O	MODE0	88	I	TST-D3
14	I	CS	39	—	V _{DD}	64	O	MODE1	89	I	TST-D4
15	—	V _{DD}	40	—	GND	65	O	MODE2	90	I	TST-D5
16	I	THRU/NORM SEL	41	I/O	SFDT7	66	O	MODE3	91	I	TST-D6
17	O	AUX-IOEC	42	I/O	SFDT6	67	—	V _{DD}	92	I	TST-D7
18	I	IND EN	43	I/O	SFDT5	68	—	GND	93	O	TST-O0
19	I	VI-EN	44	I/O	SFDT4	69	I	TST-AA0	94	O	TST-O1
20	—	GND	45	I/O	SFDT3	70	I	TST-AA1	95	O	TST-O2
21	I	AU-DATA	46	I/O	SFDT2	71	I	TST-AA2	96	O	TST-O3
22	I	AU-START	47	I/O	SFDT1	72	I	TST-AA3	97	O	TST-O4
23	I	AU-DATA EN	48	I/O	SFDT0	73	I	TST-AA4	98	O	TST-O5
24	O	AUX-START	49	I	BUPFS	74	I	TST-AA5	99	O	TST-O6
25	I	P-SAVE	50	—	V _{DD}	75	I	TST-AA6	100	O	TST-O7

INPUT

8-B COM	: H : 8-BYTE COM, L : NORMAL
ADV0, ADV1	: OUTPUT TIMING ADVANCE CONTROL
AU-DATA	: AUDIO SERIAL DATA
AU-DATA EN	: AUDIO SERIAL DATA ENABLE
AU-START	: AUDIO START
BUPFS	: FDF DATA TIMING
CK	: MAIN CLOCK (18.1MHz)
CS	: CHIP SELECT
FLD-PLS	: FIELD REFERENCE PULSE
IND EN	: INDI AUDIO ENABLE
P-SAVE	: POWER SAVE (H : POWER SAVE)
R/P SEL	: REC/PB SEL (H : REC, L : PB)
$\overline{\text{RST}}$	: SYSTEM RESET
SCK	: SERIAL COMMUNICATION CLOCK
S-DATA IN	: SERIAL COMMUNICATION DATA
SYS SEL	: SYSTEM SELECT (H : PAL, L : NTSC)
THRU/NORM SEL	: THROUGH/NORMAL SELECT (H : THROUGH, L : NORMAL)
TRK-PLS	: TRACK REFERENCE PULSE
TST-AA0 - TST-AA7	: FOR TEST
TST-AB - TST-AB7	: FOR TEST
TST-CTL1 - TST-CTL4	: FOR TEST
TST-D0 - TST-D7	: FOR TEST
VI-EN	: VIDEO ENABLE

OUTPUT

AUX-IOEC	: I/O ENABLE CONTROL
AUX-START	: AUXILIARY START
MODE0 - MODE3	: MODE COMMAND
PERI CNT0 - PERI CNT3	: PERIPHERAL IC CONTROL COMMAND
S-DATA OUT	: SERIAL COMMUNICATION DATA
TST-O0 - TST-O7	: FOR TEST

INPUT/OUTPUT

SFDT15 - SFDT0	: SYF/FDF DATA BUS
----------------	--------------------