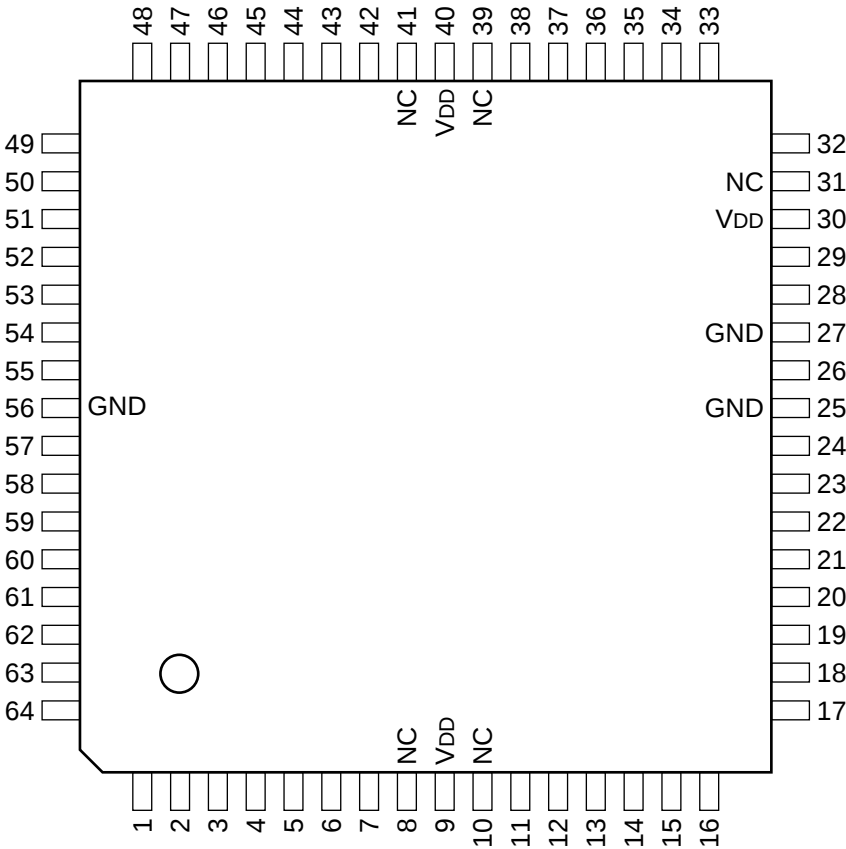


C-MOS SAMPLING RATE CONVERTER
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	DO	17	I/O	TB7	33	I/O	TB5	49	I/O	TB2
2	I	SEL	18	I	MDO0	34	I/O	TB4	50	I/O	TB1
3	O	SRDT	19	I	MDO1	35	I	MAO0	51	I	DATAI
4	I	SWDT	20	I	MUTE	36	I	MAO1	52	I	LRCKI
5	I	SCK	21	O	DATAO	37	O	ALRKO	53	I	BCKI
6	I	XLAT	22	I/O	BCKO	38	O	ABCKO	54	I	FIMCK
7	I	INIT	23	I/O	LRCKO	39	—	NC	55	O	FIMO
8	—	NC	24	I	TEST4	40	—	VDD	56	—	GND
9	—	VDD	25	—	GND	41	—	NC	57	I	TEST2
10	—	NC	26	I	XI	42	O	DAOR	58	I	TEST1
11	O	RBPH	27	—	GND	43	O	DAOL	59	I	FIS
12	O	STA	28	O	XO	44	I	TEST3	60	I	MI0
13	I	FRS	29	O	XOO	45	I	DEMP	61	I	MI1
14	I	FRM	30	—	VDD	46	I	FS1	62	I	DI0
15	I	SLAVE	31	—	NC	47	I	FS2	63	I	DI1
16	I	TEST5	32	I/O	TB6	48	I/O	TB3	64	I/O	TB0

INPUT

BCKI	: BIT CLOCK FOR THE INPUT SERIAL DATA
DATAI	: SERIAL DATA
DEMP	: DEEMPHASIS SETTING
DI0, DI1	: DATA
FIMCK	: MASTER CLOCK FOR THE INPUT DATA SYSTEM
FIS	: FIMCK DIVIDING RATIO SETTING
FRM	: Fs RATIO MEASURING MODE SELECT
FRS	: Fs RATIO MEASURING TIME SELECT
FS1, FS2	: Fsi FREQUENCY SELECT FOR DEEMPHASIS SETTING
INIT	: INITIALIZE
LRCKI	: 1 Fs WORD CLOCK FOR THE INPUT SERIAL DATA
MAO0, MAO1	: SERIAL DATA FORMAT SETTING FOR D/A OUTPUT
MDO0, MDO1	: SERIAL DATA FORMAT SETTING FOR THE STANDARD DATA OUTPUT
MI0, MI1	: INPUT SERIAL DATA FORMAT SETTING
MUTE	: OUTPUT MUTING
SCK	: ATTENUATION/MODE SETTING CLOCK
SEL	: DATA SELECT CONTROL
SLAVE	: OUTPUT SYNCHRONIZATION MODE SELECT
SWDT	: ATTENUATION/MODE SETTING DATA
TEST1 - TEST5	: IC CHIP TEST
XI	: CRYSTAL OSCILLATOR
XLAT	: ATTENUATION/MODE SETTING DATA LATCH PULSE

OUTPUT

ABCKO	: BIT CLOCK FOR D/A OUTPUT
ALRKO	: WORD CLOCK FOR D/A OUTPUT
DAOL	: 8 Fso/4 Fso DATA FOR L-CHANNEL D/A CONVERSION
DAOR	: 8 Fso/4 Fso DATA FOR R-CHANNEL D/A CONVERSION
DATAO	: STANDARD SERIAL DATA
DO	: DATA
FIMO	: INVERTING OUTPUT OF THE MASTER CLOCK
RBPH	: RING BUFFER READ/WRITE PHASE CONTROL MONITOR
SRDT	: ATTENUATION/MODE SETTING DATA
STA	: Fs CONVERTING RATIO MONITOR
XO	: CRYSTAL OSCILLATOR
XOO	: MASTER CLOCK FOR THE OUTPUT SYSTEM

INPUT/OUTPUT

TB0 - TB7	: TEST BUS
BCKO	: BIT CLOCK FOR THE STANDARD SERIAL DATA
LRCKO	: WORD CLOCK FOR THE STANDARD SERIAL DATA