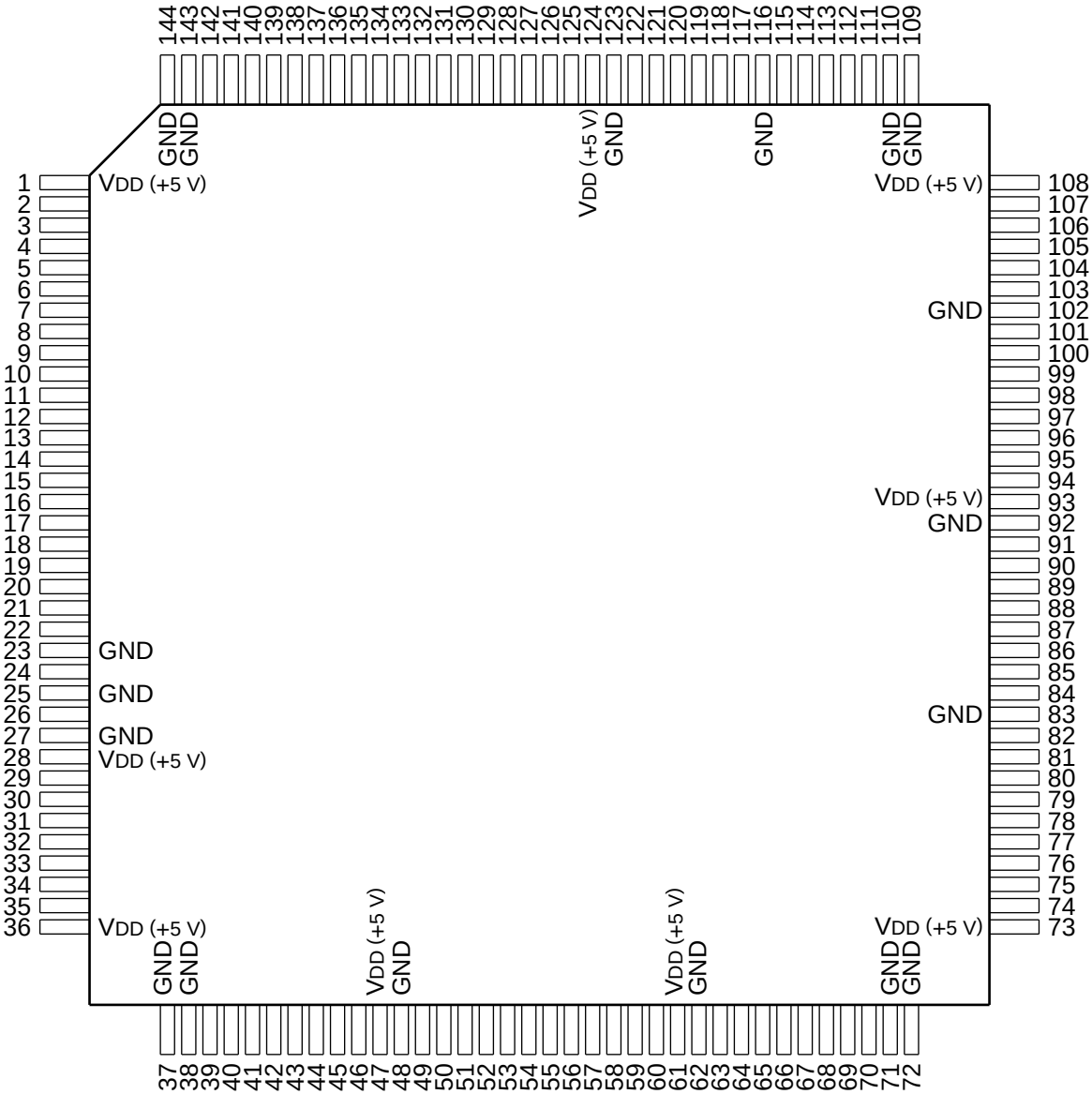


C-MOS GATE ARRAY
—TOP VIEW—



(V_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	V _{DD}	37	—	GND	73	—	V _{DD}	109	—	GND
2	—	NC	38	—	GND	74	—	NC	110	—	GND
3	I	RI3	39	O	BA3	75	I/O	GD7	111	O	RO4
4	I	RI2	40	O	BA2	76	I/O	GD6	112	O	RO5
5	I	RI1	41	O	BA1	77	I/O	GD5	113	O	RO6
6	I	RI0	42	O	BA0	78	I/O	GD4	114	O	RO7
7	I	GI7	43	O	GA3	79	I/O	GD3	115	O	XVLDO
8	I	GI6	44	O	GA2	80	I/O	GD2	116	—	GND
9	I	GI5	45	O	GA1	81	I/O	GD1	117	I/O	D5
10	I	GI4	46	O	GA0	82	I/O	GD0	118	I/O	D4
11	I	GI3	47	—	V _{DD}	83	—	GND	119	I/O	D3
12	I	GI2	48	—	GND	84	O	BO0	120	I/O	D2
13	I	GI1	49	O	GBA11	85	O	BO1	121	I/O	D1
14	I	GI0	50	O	GBA10	86	O	BO2	122	I/O	D0
15	I	BI7	51	O	GBA9	87	O	BO3	123	—	GND
16	I	BI6	52	O	GBA8	88	O	BO4	124	—	V _{DD}
17	I	BI5	53	O	GBA7	89	O	BO5	125	I	XRD
18	I	BI4	54	O	GBA6	90	O	BO6	126	I	XWR
19	I	BI3	55	O	GBA5	91	O	BO7	127	I	XCS0
20	I	BI2	56	O	GBA4	92	—	GND	128	I	CS1
21	I	BI1	57	O	GBA3	93	—	V _{DD}	129	I	XCS2
22	I	BI0	58	O	GBA2	94	O	GO0	130	I	TEST
23	—	GND	59	O	GBA1	95	O	GO1	131	I	XRST
24	I	PCLK	60	O	GBA0	96	O	GO2	132	I	RB
25	—	GND	61	—	V _{DD}	97	O	GO3	133	I	OSEL1
26	I	SYSCLK	62	—	GND	98	O	GO4	134	I	OSEL0
27	—	GND	63	I/O	BD7	99	O	GO5	135	I	ISEL1
28	—	V _{DD}	64	I/O	BD6	100	O	GO6	136	I	ISEL0
29	O	XWEB	65	I/O	BD5	101	O	GO7	137	I	XVVL
30	O	XOEB	66	I/O	BD4	102	—	GND	138	I	XHVLD
31	O	XWEG	67	I/O	BD3	103	O	RO0	139	I	RI7
32	O	XOEG	68	I/O	BD2	104	O	RO1	140	I	RI6
33	O	XBA4	69	I/O	BD1	105	O	RO2	141	I	RI5
34	O	BA4	70	I/O	BD0	106	O	RO3	142	I	RI4
35	—	NC	71	—	GND	107	—	NC	143	—	GND
36	—	V _{DD}	72	—	GND	108	—	V _{DD}	144	—	GND