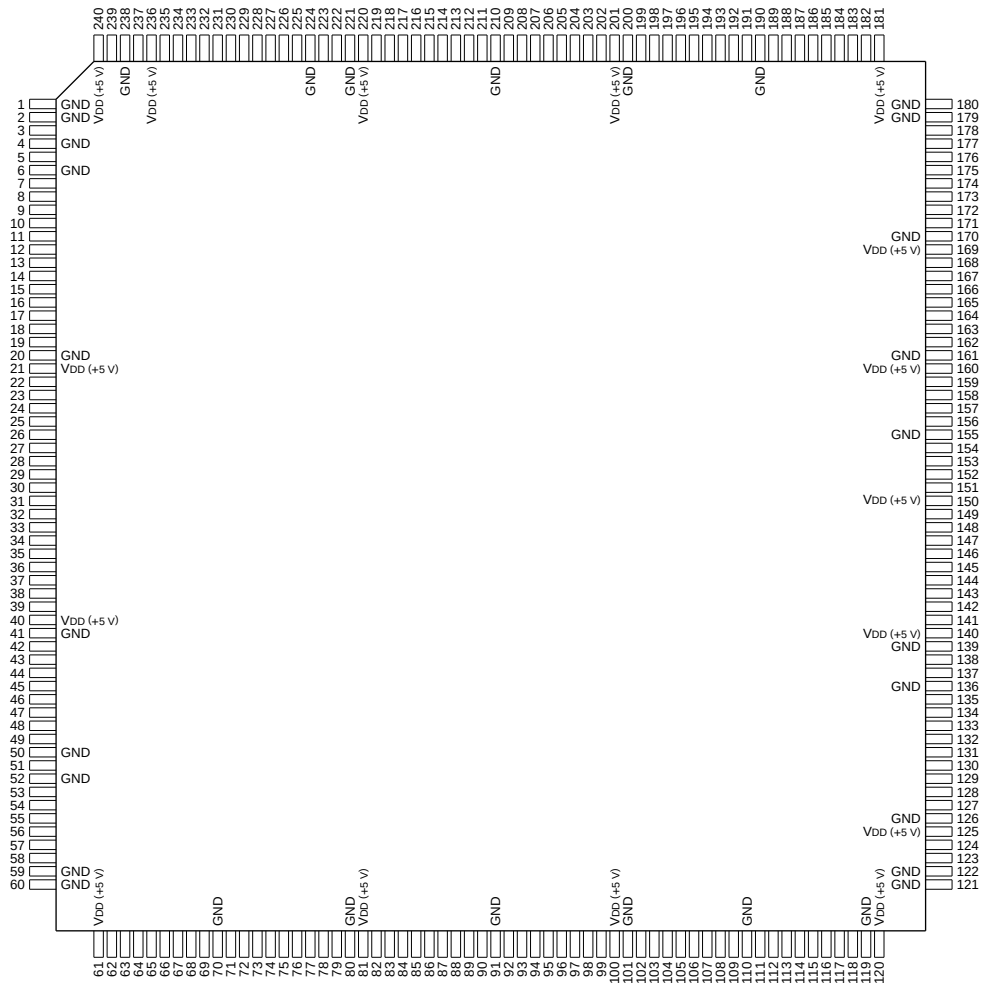


CELL BASED
—TOP VIEW—



(V_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	47	I/O	D2	93	I/O	TR1	139	—	GND
2	—	GND	48	I/O	D1	94	I/O	TR2	140	—	V _{DD}
3	I	XRESET	49	I/O	D0	95	I/O	TR3	141	O	XTWE-B3
4	—	GND	50	—	GND	96	I/O	TR4	142	O	XTWE-B2
5	I	CLK	51	I	XREQIN	97	I/O	TR5	143	O	XTWE-B1
6	—	GND	52	—	GND	98	I/O	TR6	144	O	XTWE-G3
7	I	XCS-REG	53	O	XREQOUT	99	I/O	TR7	145	O	XTWE-G2
8	I	XCS-DRAM	54	I/O	PCLK	100	—	V _{DD}	146	O	XTWE-G1
9	I	XWR	55	O	DCLK	101	—	GND	147	O	XTWE-R3
10	I	XRD	56	O	DCLK4	102	I/O	TG0	148	O	XTWE-R2
11	I	XAS	57	I/O	DB0	103	I/O	TG1	149	O	XTWE-R1
12	I	A20	58	I/O	DB1	104	I/O	TG2	150	—	V _{DD}
13	I	A19	59	—	GND	105	I/O	TG3	151	O	XKCAS
14	I	A18	60	—	GND	106	I/O	TG4	152	O	XTCAS
15	I	A17	61	—	V _{DD}	107	I/O	TG5	153	O	XKRAS
16	I	A16	62	I/O	DB2	108	I/O	TG6	154	O	XTRAS
17	I	A15	63	I/O	DB3	109	I/O	TG7	155	—	GND
18	I	A14	64	I/O	DB4	110	—	GND	156	O	TA10
19	I	A13	65	I/O	DB5	111	I/O	TB0	157	O	TA9
20	—	GND	66	I/O	DB6	112	I/O	TB1	158	O	TA8
21	—	V _{DD}	67	I/O	DB7	113	I/O	TB2	159	O	TA7
22	I	A12	68	I/O	DG0	114	I/O	TB3	160	—	V _{DD}
23	I	A11	69	I/O	DG1	115	I/O	TB4	161	—	GND
24	I	A10	70	—	GND	116	I/O	TB5	162	O	TA6
25	I	A9	71	I/O	DG2	117	I/O	TB6	163	O	TA5
26	I	A8	72	I/O	DG3	118	I/O	TB7	164	O	TA4
27	I	A7	73	I/O	DG4	119	—	GND	165	O	TA3
28	I	A6	74	I/O	DG5	120	—	V _{DD}	166	O	TA2
29	I	A5	75	I/O	DG6	121	—	GND	167	O	TA1
30	I	A4	76	I/O	DG7	122	—	GND	168	O	TA0
31	I	A3	77	I/O	DR0	123	O	XKOE-2	169	—	V _{DD}
32	I	A2	78	I/O	DR1	124	O	XKOE-1	170	—	GND
33	I	A1	79	I/O	DR2	125	—	V _{DD}	171	I/O	MB7
34	I	A0	80	—	GND	126	—	GND	172	I/O	MB6
35	I	TSTMOD	81	—	V _{DD}	127	O	XTOE-B3	173	I/O	MB5
36	I	TM0	82	I/O	DR3	128	O	XTOE-B2	174	I/O	MB4
37	I	TM1	83	I/O	DR4	129	O	XTOE-B1	175	I/O	MB3
38	I	TM2	84	I/O	DR5	130	O	XTOE-G3	176	I/O	MB2
39	I	TM3	85	I/O	DR6	131	O	XTOE-G2	177	I/O	MB1
40	—	V _{DD}	86	I/O	DR7	132	O	XTOE-G1	178	I/O	MB0
41	—	GND	87	I/O	K0	133	O	XTOE-R3	179	—	GND
42	I/O	D7	88	I/O	K1	134	O	XTOE-R2	180	—	GND
43	I/O	D6	89	I/O	K2	135	O	XTOE-R1	181	—	V _{DD}
44	I/O	D5	90	I/O	K3	136	—	GND	182	I/O	MG7
45	I/O	D4	91	—	GND	137	O	XKWE-2	183	I/O	MG6
46	I/O	D3	92	I/O	TR0	138	O	XKWE-1	184	I/O	MG5

(V_{DD} = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
185	I/O	MG4	199	O	XMOE-B3	213	O	XMWE-B1	227	O	MA8
186	I/O	MG3	200	—	GND	214	O	XMWE-G3	228	O	MA7
187	I/O	MG2	201	—	V _{DD}	215	O	XMWE-G2	229	O	MA6
188	I/O	MG1	202	O	XMOE-B2	216	O	XMWE-G1	230	O	MA5
189	I/O	MG0	203	O	XMOE-B1	217	O	XMWE-R3	231	O	MA4
190	—	GND	204	O	XMOE-G3	218	O	XMWE-R2	232	O	MA3
191	I/O	MR7	205	O	XMOE-G2	219	O	XMWE-R1	233	O	MA2
192	I/O	MR6	206	O	XMOE-G1	220	—	V _{DD}	234	O	MA1
193	I/O	MR5	207	O	XMOE-R3	221	—	GND	235	O	MA0
194	I/O	MR4	208	O	XMOE-R2	222	O	XMCAS	236	—	V _{DD}
195	I/O	MR3	209	O	XMOE-R1	223	O	XMRAS	237	O	XWAIT
196	I/O	MR2	210	—	GND	224	—	GND	238	—	GND
197	I/O	MR1	211	O	XMWE-B3	225	O	MA10	239	O	CPU-XTAL
198	I/O	MR0	212	O	XMWE-B2	226	O	MA9	240	—	V _{DD}