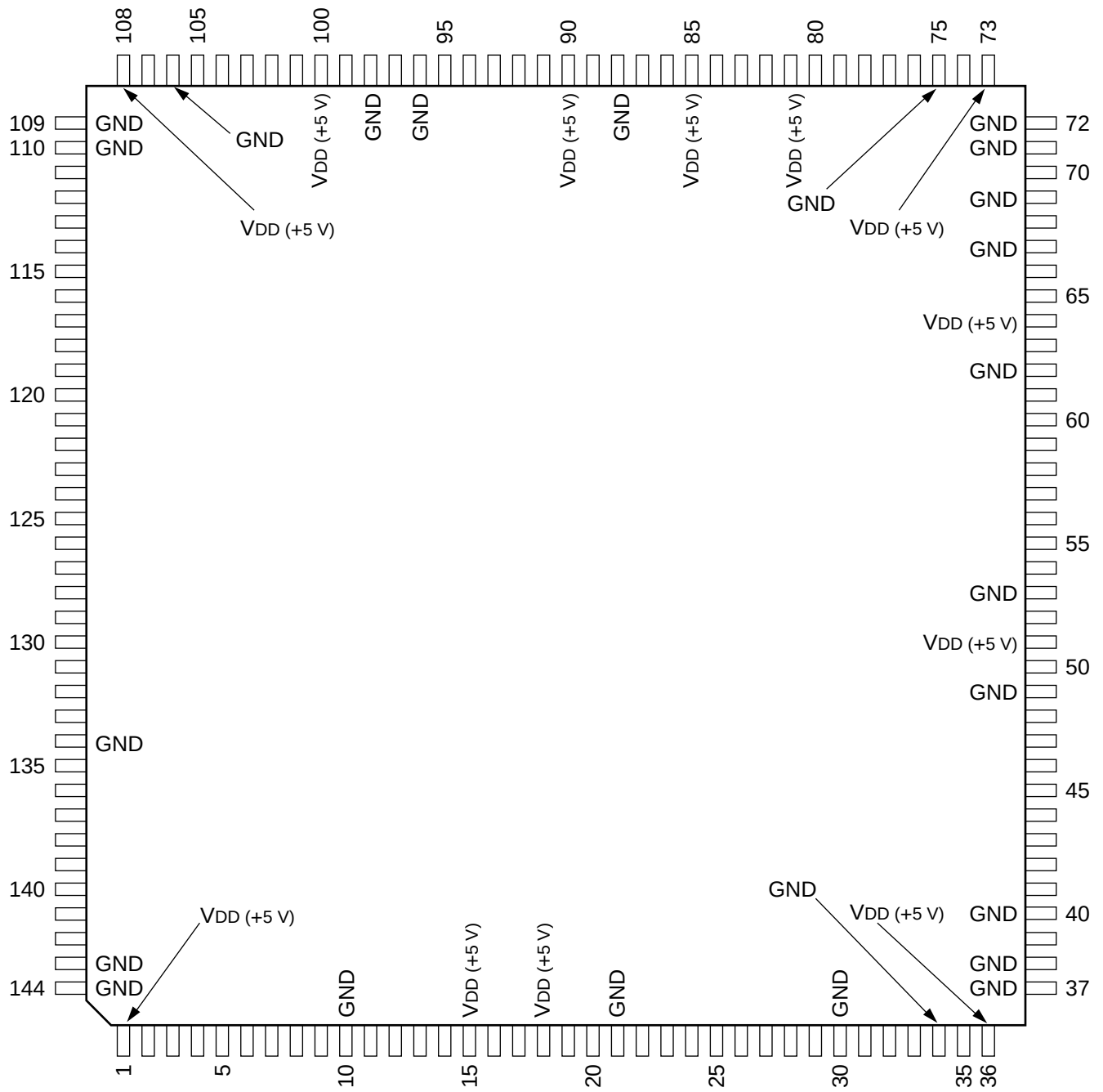


C-MOS GATE ARRAY

—TOP VIEW—



(V_{DD} = +5 V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	37	—	GND	73	—	V _{DD}	109	—	GND
2	I/O	D7	38	—	GND	74	—	NC	110	—	GND
3	I/O	D6	39	O	XCS SCSI	75	—	GND	111	I	A23
4	I/O	D5	40	—	GND	76	O	XCS JPSC	112	I	A22
5	I/O	D4	41	I/O	RA DATA0	77	O	XCS RAW	113	I	A21
6	I/O	D3	42	I/O	RA DATA1	78	O	XCS JPEG	114	I	A20
7	I/O	D2	43	I/O	RA DATA2	79	O	XCS C2PO	115	I	A19
8	I/O	D1	44	I/O	RA DATA3	80	O	XCS ATRA	116	I	A18
9	I/O	D0	45	I/O	RA DATA4	81	—	V _{DD}	117	I	A17
10	—	GND	46	I/O	RA DATA5	82	O	AT C2PO	118	I	A16
11	O	XDREQ AT	47	I/O	RA DATA6	83	O	AT ACDO	119	I	A15
12	O	XDREQ C2	48	I/O	RA DATA7	84	O	AT XABS	120	I	A14
13	O	XDREQ JP	49	—	GND	85	—	V _{DD}	121	I	A13
14	O	XDREQ SC	50	O	RA XACK	86	I	AT ACDI	122	I	A12
15	—	V _{DD}	51	—	V _{DD}	87	I	AT XARQ	123	I	A11
16	I	XCS3	52	I	RA XREQ	88	—	GND	124	I	A10
17	I	XRF5H	53	—	GND	89	I	AT CLOCK	125	I	A9
18	—	V _{DD}	54	I/O	JP DATA0	90	—	V _{DD}	126	I	A8
19	O	XRAS1	55	I/O	JP DATA1	91	O	FS1	127	I	A7
20	O	XRAS2	56	I/O	JP DATA2	92	O	XFS64	128	I	A6
21	—	GND	57	I/O	JP DATA3	93	O	FS64	129	I	A5
22	I/O	SC DATA7	58	I/O	JP DATA4	94	O	FS128	130	I	A4
23	I/O	SC DATA6	59	I/O	JP DATA5	95	O	FS256	131	I	A3
24	I/O	SC DATA5	60	I/O	JP DATA6	96	—	GND	132	I	A2
25	I/O	SC DATA4	61	I/O	JP DATA7	97	I	FS512	133	I	A1
26	I/O	SC DATA3	62	—	GND	98	—	GND	134	—	GND
27	I/O	SC DATA2	63	O	JP XBUSY	99	O	XCS ROM	135	I/O	D15
28	I/O	SC DATA1	64	—	V _{DD}	100	—	V _{DD}	136	I/O	D14
29	I/O	SC DATA0	65	I	JP XCWE	101	I	XLWR	137	I/O	D13
30	—	GND	66	I	JP XCAEN	102	I	XHWR	138	I/O	D12
31	O	SC XRED	67	—	GND	103	I	XRD	139	I/O	D11
32	O	SC XWED	68	I	JP CLOCK	104	I	XAS	140	I/O	D10
33	O	SC XDACK	69	—	GND	105	I	XCLEAR	141	I/O	D9
34	—	GND	70	I	TEST	106	—	GND	142	I/O	D8
35	I	SC DRQ	71	—	GND	107	I	CLOCK	143	—	GND
36	—	V _{DD}	72	—	GND	108	—	V _{DD}	144	—	GND

