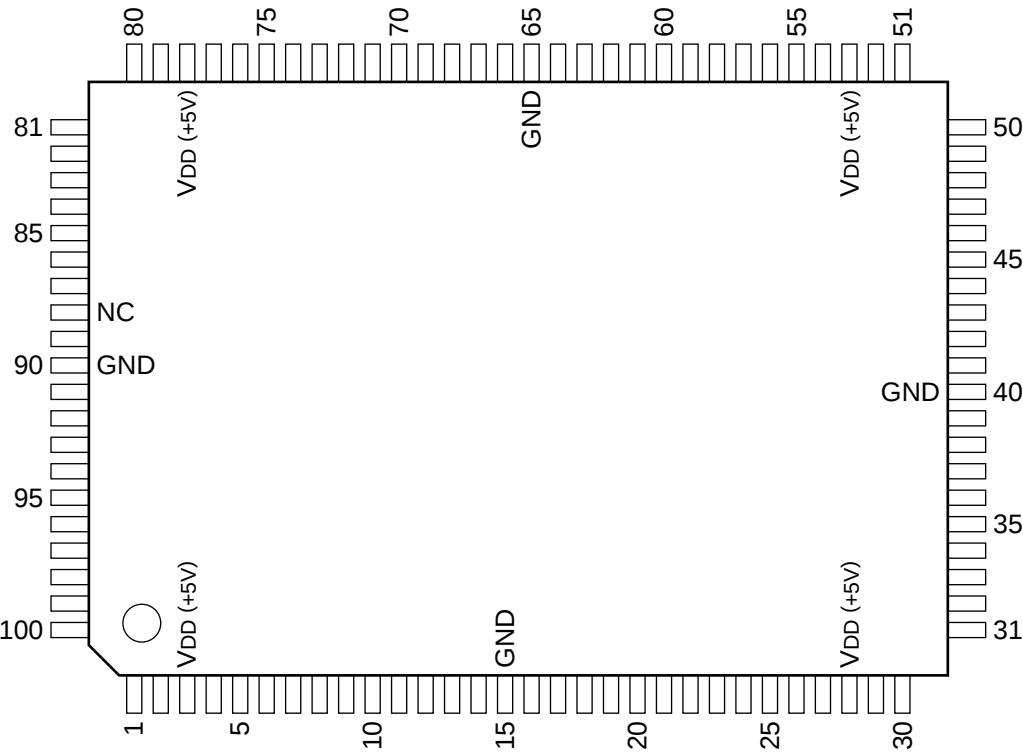
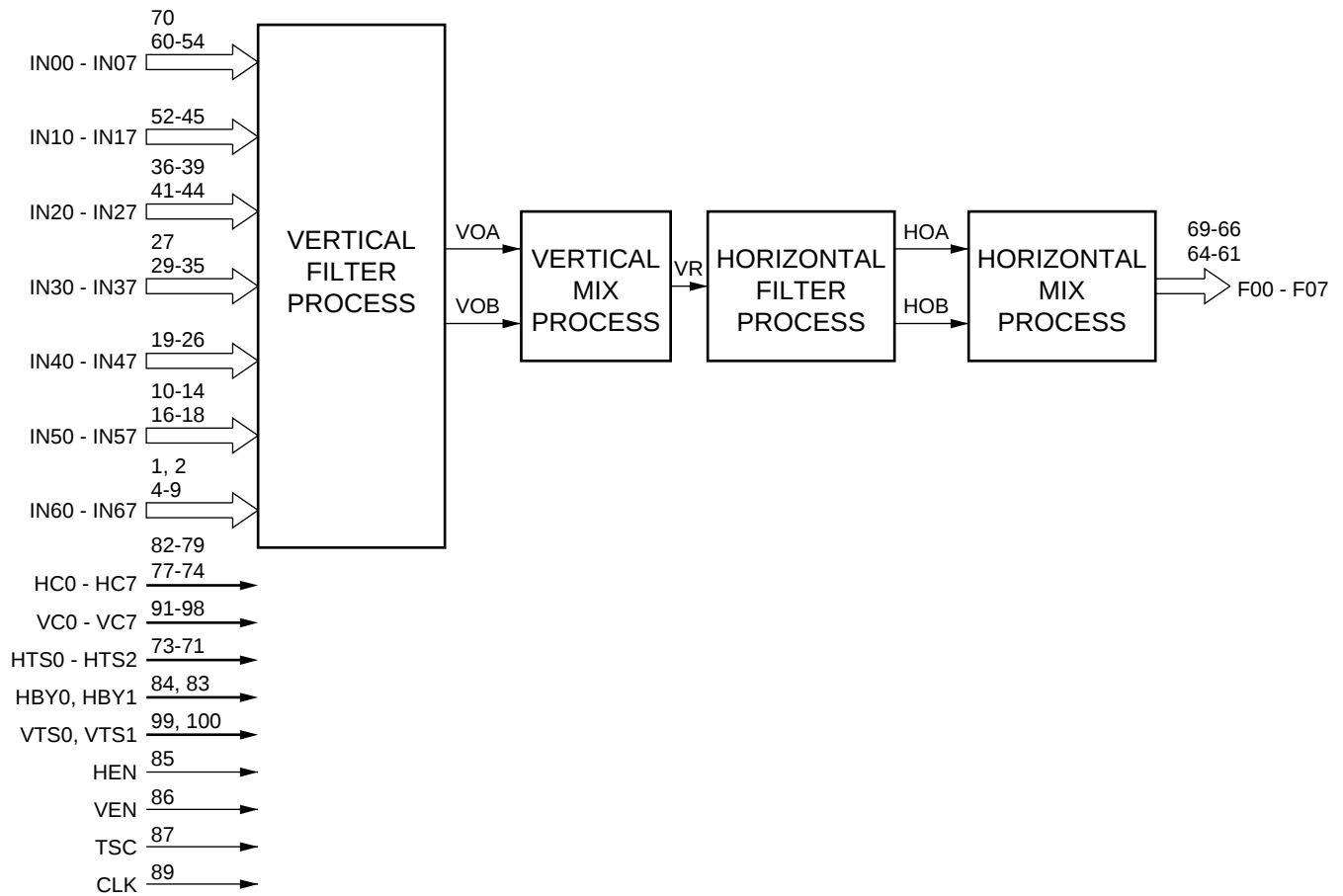

C-MOS 2 DIMENSIONAL VARIABLE LOW-PASS FILTER
-TOP VIEW-



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	IN60	21	I	IN42	41	I	IN24	61	O	F07	81	I	HC1
2	I	IN61	22	I	IN43	42	I	IN25	62	O	F06	82	I	HC0
3	—	VDD	23	I	IN44	43	I	IN26	63	O	F05	83	I	HBY1
4	I	IN62	24	I	IN45	44	I	IN27	64	O	F04	84	I	HBY0
5	I	IN63	25	I	IN46	45	I	IN17	65	—	GND	85	I	HEN
6	I	IN64	26	I	IN47	46	I	IN16	66	O	F03	86	I	VEN
7	I	IN65	27	I	IN30	47	I	IN15	67	O	F02	87	I	TSC
8	I	IN66	28	—	VDD	48	I	IN14	68	O	F01	88	—	NC
9	I	IN67	29	I	IN31	49	I	IN13	69	O	F00	89	I	CLK
10	I	IN50	30	I	IN32	50	I	IN12	70	I	IN00	90	—	GND
11	I	IN51	31	I	IN33	51	I	IN11	71	I	HTS2	91	I	VC0
12	I	IN52	32	I	IN34	52	I	IN10	72	I	HTS1	92	I	VC1
13	I	IN53	33	I	IN35	53	—	VDD	73	I	HTS0	93	I	VC2
14	I	IN54	34	I	IN36	54	I	IN07	74	I	HC7	94	I	VC3
15	—	GND	35	I	IN37	55	I	IN06	75	I	HC6	95	I	VC4
16	I	IN55	36	I	IN20	56	I	IN05	76	I	HC5	96	I	VC5
17	I	IN56	37	I	IN21	57	I	IN04	77	I	HC4	97	I	VC6
18	I	IN57	38	I	IN22	58	I	IN03	78	—	VDD	98	I	VC7
19	I	IN40	39	I	IN23	59	I	IN02	79	I	HC3	99	I	VTS0
20	I	IN41	40	—	GND	60	I	IN01	80	I	HC2	100	I	VTS1

[illegible]



INPUT

CLK ; CLOCK
 HBY0, HBY1 ; HORIZONTAL BYPASS SELECT
 HC0 - HC7 ; HORIZONTAL MIXING COEFFICIENT
 HEN ; HORIZONTAL CONTROL ENABLE
 HTS0 - HTS2 ; HORIZONTAL TAP SELECT
 IN00 - IN07 ; DATA 0
 IN10 - IN17 ; DATA 1
 IN20 - IN27 ; DATA 2
 IN30 - IN37 ; DATA 3
 IN40 - IN47 ; DATA 4
 IN50 - IN57 ; DATA 5
 IN60 - IN67 ; DATA 6
 TSC ; OPERATING MODE SELECT
 VC0 - VC7 ; VERTICAL MIXING COEFFICIENT
 VEN ; VERTICAL CONTROL ENABLE
 VTS0, VTS1 ; VERTICAL TAP SELECT

OUTPUT

F00 - F07 ; FILTER OUT