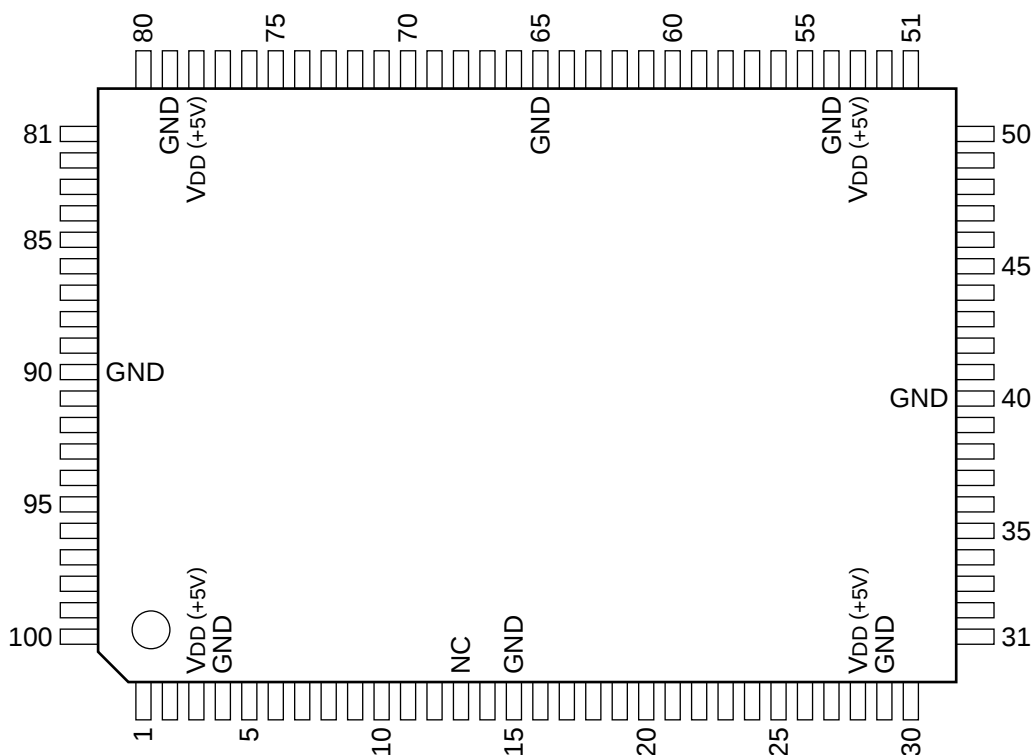

C-MOS NON-ADDITIVE MIXER AND DELAY ADJUSTER

-TOP VIEW-



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	AL4	21	I	AH0	41	I	CLK	61	I	DH3	81	O	OL2
2	O	AEBL	22	I	AH1	42	I	BH7	62	I	DH2	82	O	OL1
3	—	VDD	23	I	AH2	43	I	CH7	63	I	DH1	83	O	OL0
4	—	GND	24	I	AH3	44	I	CH6	64	I	DH0	84	I	CL3
5	O	ALBL	25	I	AH4	45	I	CH5	65	—	GND	85	I	CL2
6	I	AL5	26	I	AH5	46	I	CH4	66	I	DL3	86	I	CL1
7	I	AL6	27	O	AEBH	47	I	CH3	67	I	DL2	87	I	CL0
8	I	AL7	28	—	VDD	48	I	CH2	68	I	DL1	88	I	BL0
9	I	SL0	29	—	GND	49	O	OH7	69	I	DL0	89	I	BL1
10	I	SL1	30	O	ALBH	50	O	OH6	70	I	CL7	90	—	GND
11	I	TCL	31	I	AH6	51	O	OH5	71	I	CL6	91	I	BL2
12	I	PNL	32	I	AH7	52	O	OH4	72	I	CL5	92	I	BL3
13	—	NC	33	I	BH0	53	—	GND	73	I	CL4	93	I	BL4
14	I	NAM0	34	I	BH1	54	—	VDD	74	O	OL7	94	I	BL5
15	—	GND	35	I	BH2	55	O	OH3	75	O	OL6	95	I	BL6
16	I	NAM1	36	I	BH3	56	O	OH2	76	O	OL5	96	I	BL7
17	I	PNH	37	I	BH4	57	O	OH1	77	O	OL4	97	I	AL0
18	I	TCH	38	I	BH5	58	O	OH0	78	—	VDD	98	I	AL1
19	I	SH0	39	I	BH6	59	I	CH1	79	—	GND	99	I	AL2
20	I	SH1	40	—	GND	60	I	CH0	80	O	OL3	100	I	AL3

32	AH7	OH7	49
31	AH6	OH6	50
26	AH5	OH5	51
25	AH4	OH4	52
24	AH3	OH3	55
23	AH2	OH2	56
22	AH1	OH1	57
21	AH0	OH0	58
42	BH7	OL7	74
39	BH6	OL6	75
38	BH5	OL5	76
37	BH4	OL4	77
36	BH3	OL3	80
35	BH2	OL2	81
34	BH1	OL1	82
33	BH0	OL0	83
43	CH7		
44	CH6	ALBH	30
45	CH5	AEBH	27
46	CH4		
47	CH3	ALBL	5
48	CH2	AEBL	2
59	CH1		
60	CH0		
70	CL7		
71	CL6		
72	CL5		
73	CL4		
84	CL3		
85	CL2		
86	CL1		
87	CL0		
96	BL7		
95	BL6		61
94	BL5	DH3	62
93	BL4	DH2	63
92	BL3	DH1	64
91	BL2	DH0	
89	BL1	SH1	20
88	BL0	SH0	19
8	AL7	DL3	66
7	AL6	DL2	67
6	AL5	DL1	68
1	AL4	DL0	69
100	AL3	SL1	10
99	AL2	SL0	9
98	AL1		
97	AL0		
	CLK		
	TCH		
	PNH		
	NAM0		
	NAM1		
	TCL		
	PNL		
41	18	17	14
	16	11	12

CXD8558Q (2/3)

INPUT

AH0 - AH7	; A DATA (HIGHER BYTE)
AL0 - AL7	; A DATA (LOWER BYTE)
BH0 - BH7	; B DATA (HIGHER BYTE)
BL0 - BL7	; B DATA (LOWER BYTE)
CH0 - CH7	; C DATA (HIGHER BYTE)
CL0 - CL7	; C DATA (LOWER BYTE)
CLK	; CLOCK
DH0 - DH3	; DELAY DATA (HIGHER)
DL0 - DL3	; DELAY DATA (LOWER)
NAM0, NAM1	; NAM MODE
PNH	; NAM POLARITY (HIGHER)
PNL	; NAM POLARITY (LOWER)
SH0, SH1	; SELECT MODE (HIGHER)
SL0, SL1	; SELECT MODE (LOWER)
TCH	; NAM DATA FORMAT (HIGHER)
TCL	; NAM DATA FORMAT (LOWER)

OUTPUT

AEBH	; COMPARISON I (AH & BH)
AEBH	; COMPARISON II (AL & BL)
ALBH	; COMPARISON III (AH & BH)
ALBL	; COMPARISON IV (AL & BL)
OH0 - OH7	; DATA OUT (HIGHER)
OL0 - OL7	; DATA OUT (LOWER)

