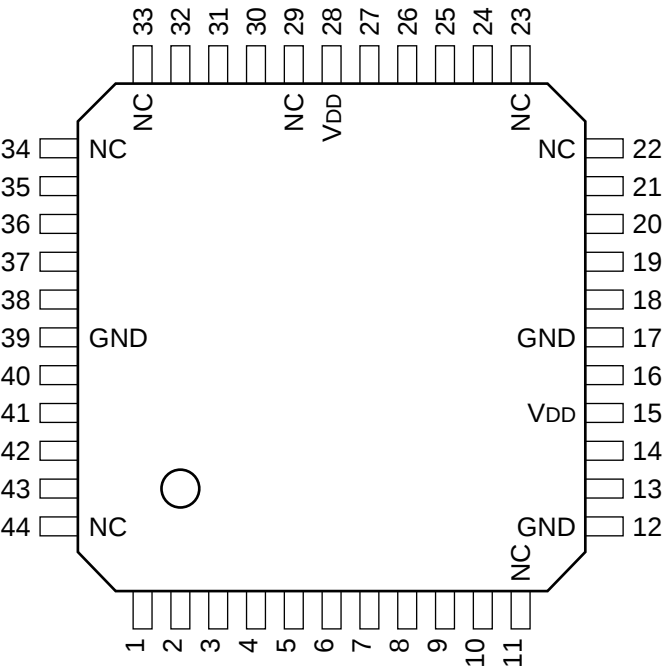


C-MOS SAMPLING FREQUENCY CONVERTER FOR DIGITAL AUDIO

—TOP VIEW—



| PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL |
|---------|-----|--------|---------|-----|--------|---------|-----|--------|---------|-----|--------|
| 1       | I   | DATAI  | 12      | —   | GND    | 23      | —   | NC     | 34      | —   | NC     |
| 2       | I   | LRCKI  | 13      | I   | XI     | 24      | I   | TEST1  | 35      | I   | MUTE   |
| 3       | I   | BCKI   | 14      | I/O | XO     | 25      | I   | TEST2  | 36      | I   | DEMP   |
| 4       | I   | MI0    | 15      | —   | VDD    | 26      | I   | TEST3  | 37      | I   | FS1    |
| 5       | I   | MI1    | 16      | O   | XO2    | 27      | O   | STA    | 38      | I   | FS2    |
| 6       | —   | VDD    | 17      | —   | GND    | 28      | —   | VDD    | 39      | —   | GND    |
| 7       | I   | FI128  | 18      | I   | PASS   | 29      | —   | NC     | 40      | I   | XLAT   |
| 8       | I   | MO0    | 19      | I   | FIS0   | 30      | O   | DATAO  | 41      | I   | SCK    |
| 9       | I   | MO1    | 20      | I   | FIS1   | 31      | I/O | BCKO   | 42      | I   | SWDT   |
| 10      | I   | INIT   | 21      | O   | TEST   | 32      | I/O | LRCKO  | 43      | I   | SLAVE  |
| 11      | —   | NC     | 22      | —   | NC     | 33      | —   | NC     | 44      | —   | NC     |

**INPUT**

|               |                                                 |
|---------------|-------------------------------------------------|
| BCKI          | : BIT CLOCK FOR INPUT SERIAL DATA               |
| DATAI         | : 1FS SERIAL DATA                               |
| DEMP          | : DE-EMPHASIS SETTING                           |
| FI128         | : REFERENCE FS CLOCK INPUT DATA SYSTEM          |
| FIS0, FIS1    | : FI128 CLOCK FREQUENCY DIVISION SETTING        |
| FS1, FS2      | : FSO SETTING (FOR DE-EMPHASIS OUTPUT)          |
| INIT          | : INITIALIZE                                    |
| LRCKI         | : 1FS WORD CLOCK FOR INPUT SERIAL DATA          |
| MI0, MI1      | : FORMAT SELECT FOR INPUT SERIAL DATA           |
| MO0, MO1      | : FORMAT SELECT FOR OUTPUT SERIAL DATA          |
| MUTE          | : DATA OUTPUT MUTING                            |
| PASS          | : THROUGH OUTPUT MODE SELECT                    |
| SCK           | : CLOCK FOR ATTENUATION MODE SETTING            |
| SLAVE         | : SYNC MODE SETTING                             |
| SWDT          | : DATA FOR ATTENUATION MODE SETTING             |
| TEST1 - TEST3 | : TEST                                          |
| XI            | : CRYSTAL OSCILLATOR (512 FSO MASTER CLOCK)     |
| XLAT          | : DATA LATCH PULSE FOR ATTENUATION MODE SETTING |

**OUTPUT**

|       |                                |
|-------|--------------------------------|
| DATAO | : SERIAL DATA (FSO)            |
| STA   | : FS CONVERTING STATUS MONITOR |
| TEST  | : TEST                         |
| XO2   | : 1/2 MASTER CLOCK (256 FSO)   |

**INPUT/OUTPUT**

|       |                                         |
|-------|-----------------------------------------|
| BCKO  | : BIT CLOCK FOR OUTPUT SERIAL DATA      |
| LRCKO | : FSO WORD CLOCK FOR OUTPUT SERIAL DATA |
| XO    | : CRYSTAL OSCILLATOR                    |