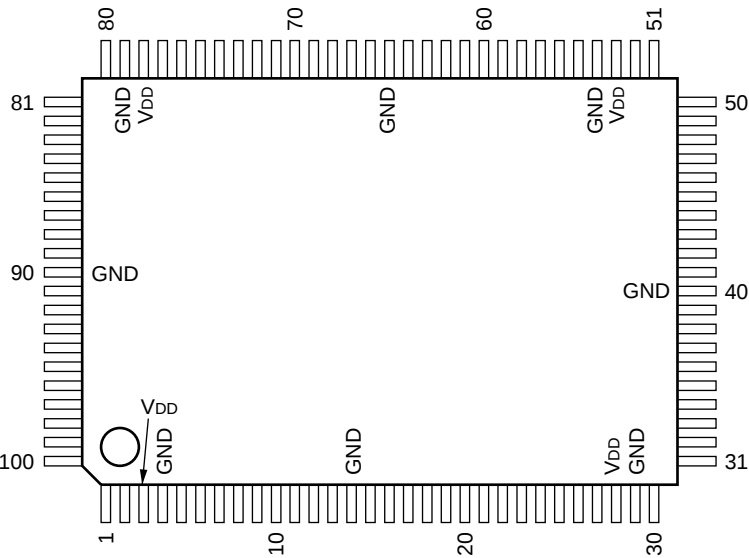


C-MOS 4:2:2 COMPONENT DECODER
—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	PERROR	26	O	VD2	51	I	DICK	76	I	D1J2
2	O	POUT	27	O	VD1	52	I	DI0	77	I	FFWON
3	—	VDD	28	—	VDD	53	—	VDD	78	—	VDD
4	—	GND	29	—	GND	54	—	GND	79	—	GND
5	O	CRCC	30	O	VD0	55	I	DI1	80	I	DATASEL0
6	O	CF2	31	O	FFW	56	I	DI2	81	I	DATASEL1
7	O	CF1	32	O	TFW	57	I	DI3	82	I	TSTENA
8	O	CF0	33	O	XER	58	I	DI4	83	I	TEST
9	O	KF	34	O	D0	59	I	DI5	84	I	CF0SEL
10	O	KVVD	35	O	D1	60	I	DI6	85	I	VDMUTE
11	O	KHHD	36	O	D2	61	I	DI7	86	I	FIFOWIN
12	O	VD9	37	O	D3	62	I	DI8	87	I	FIFORSTW
13	O	VD8	38	O	SEPCK1	63	I	DI9	88	I	FIFOWCK
14	O	VD7	39	O	VDEOR	64	I	HIZ	89	I	FIFORX
15	—	GND	40	—	GND	65	—	GND	90	—	GND
16	O	VD6	41	O	KCK	66	I	TCLR	91	I	SEL
17	O	VD5	42	O	FMX	67	I	THCNT	92	I	LIM
18	O	VD4	43	O	D4	68	I	TFCNT	93	I	ANCDEL
19	O	FREERUN	44	O	D5	69	I	TCNT	94	I	INDEXDEL
20	O	LSTD	45	O	D6	70	I	TFCK	95	I	INDEXENA
21	O	FFWCONT	46	O	TRSDET	71	I	TCH01	96	I	ALLDEL
22	O	INDEXCOM	47	O	KAH	72	I	TCH02	97	I	TWV
23	I	CFINC	48	O	D7	73	I	THCOR	98	I	INXB0B2
24	I	TRSMODE	49	O	D8	74	I	XCOR	99	I	INXBB0
25	O	VD3	50	O	D9	75	I	VFMD	100	I	INXBB1

INPUT

ALLDEL	: DELETE CONTROL FOR BETWEEN EAV AND SAV
ANCDL	: ANC DELETE CONTROL
CF0SEL	: CF0 SELECT
CFINC	: CF INCRIMENT CONTROL
D1J2	: MODE SELECT
DATASEL0, 1	: D0 - D9 DATA CONTROL
DI0 - 9	: VIDEO DATA BUS
DICK	: 27MHz CLOCK
FFWON	: SYNCHRONIZE SYSTEM FLYWHEEL/DIRECT SELECT
FIFORSTW, FIFORX, FIFOWCK, FIFOWIN	: SELF-DIAGNOSIS
HIZ	: OUTPUT SYSTEM HI-IMPEDANCE CONTROL
INDEXDEL	: INX DELETE CONTROL
INDEXENA	: INX ENABLE CONTROL
INXB0B2	: INX CONTROL
INXBB0, 1	: MOTOR CONTROL
LIM	: LIMITER ON/OFF CONTROL
SEL	: LIMITER MODE SELECT
TCH01 - 02, TCLR, TCNT,	: FOR TEST
TFCK, TFCNT, THCNT, THCOR, TSTENA	
TRSMODE	: TRS DETECT MODE SELECT
TWV	: NOT CONSECUTIVE DETECT CONTROL
VDMUTE	: MUTE CONTROL
VFMD	: 525,V HYSTERESIS PROCESS
XCOR	: TRS XY-BYTE PROCESS

OUTPUT

CF0 - 2	: COLOR FRAME
CRCC	: CLASS 2-1CRCC+CF0/RF0DET
D0 - 9	: INTERNAL DATA INFORMATION
FFW, TFW	: FLYWHEEL STATUS
FFWCONT	: FLY/DIRECT SYNCHRONIZE SELECT CONTROL
FMX	: F COUNTER PULSE STATE
FREERUN	: ERROR STATUS OF INX FOUR CONSECUTIVE TIMES
INDEXCOM	: COMPOSITE/COMPONENT SIGNAL
KAH	: ANALOG SYNCHRONIZE
KCK	: 13.5 MHz CLOCK
KF	: KF
KHHD	: H SYNCHRONIZE
KVVD	: V SYNCHRONIZE
LSTD	: 525/625 DETECT
PERROR	: SELF-DIAGNOSIS DETECT
POUT	: SELF-DIAGNOSIS PARITY BETWEEN BOARD
SEPCK1	: 6.75 MHz CLOCK
TRSDT	: TRS DETECT
VD0 - 9	: VIDEO DATA BUS
VDEOR	: VIDEO DATA EOR
XER	: TRS XY-BYTE HAMMING PROCESS

