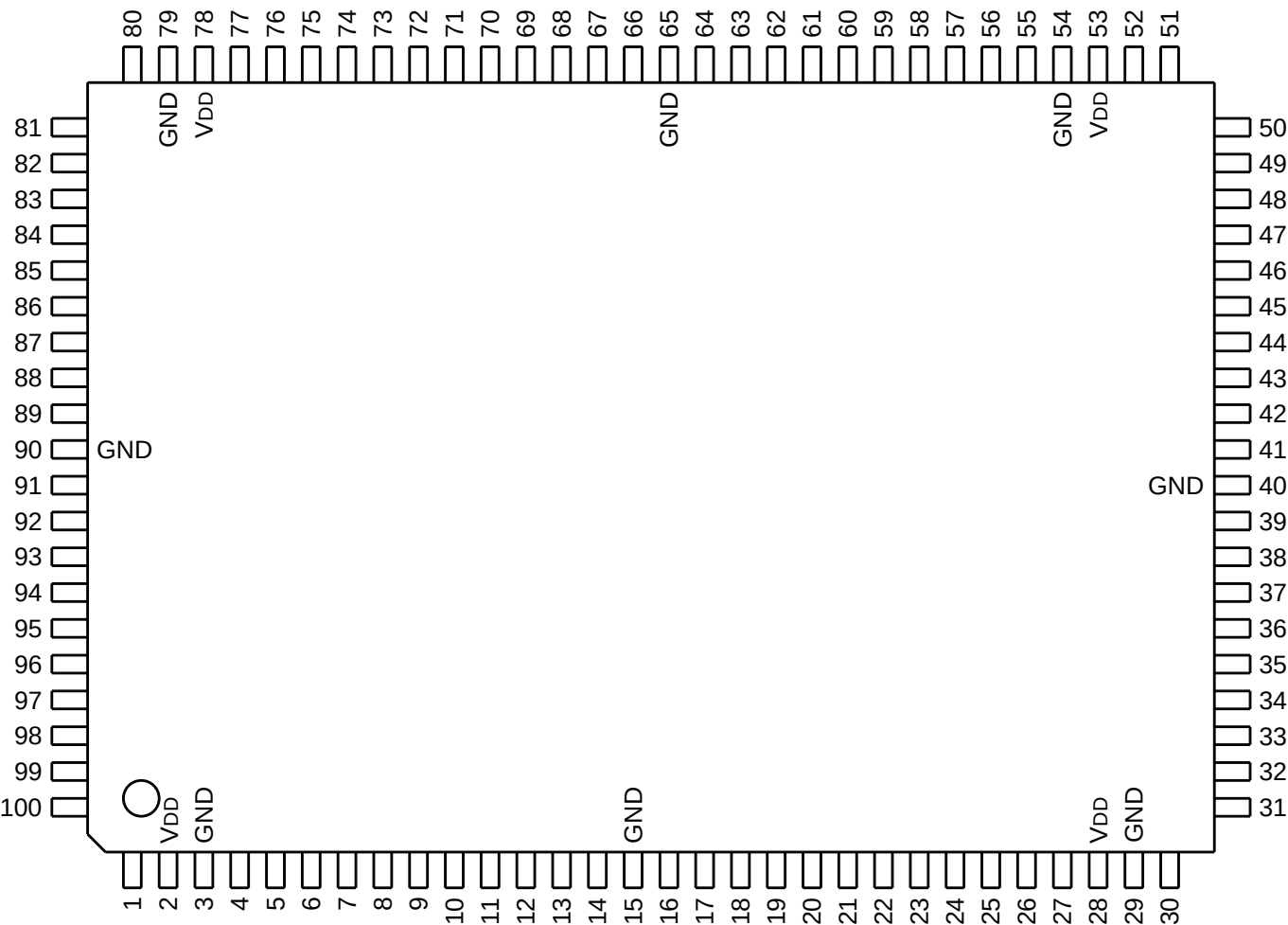


C-MOS LTC READER/GENERATOR
—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I/O	D2	26	I	CTLI	51	I	FRMI	76	O	INTG
2	I/O	D3	27	I	FWD	52	I	LSHI	77	O	INTL
3	—	V _{DD}	28	—	V _{DD}	53	—	V _{DD}	78	—	V _{DD}
4	—	GND	29	—	GND	54	—	GND	79	—	GND
5	I/O	D4	30	O	RSYC	55	O	LSHO	80	O	INTC
6	I/O	D5	31	O	LREV	56	O	VCEO	81	O	INTR
7	I/O	D6	32	O	DCLK	57	I	VCI1	82	O	INTX
8	I/O	D7	33	O	DATA	58	I	VCI2	83	I	TEST1
9	I	$\overline{\text{CS}}$	34	O	BER	59	O	LTCO	84	I	TEST2
10	I	$\overline{\text{WR}}$	35	I	DMCK	60	O	CFNG	85	I	TEST3
11	I	$\overline{\text{RD}}$	36	I	DEMI	61	I	REF	86	I	TEST4
12	I	A0	37	I	K16I	62	O	$\overline{\text{XOUT}}$	87	I	ALCK
13	I	A1	38	O	$\overline{\text{XBK}}$	63	O	PORT	88	O	CK2M
14	I	A2	39	I	$\overline{\text{BKIN}}$	64	I	NORM	89	I	CK1I
15	—	GND	40	—	GND	65	—	GND	90	—	GND
16	I	A3	41	O	BKO	66	O	NT4F	91	O	CK1O
17	I	A4	42	I	CSHD	67	O	PL4F	92	O	DCCK
18	I	A5	43	I	CSVD	68	O	PL8F	93	I	INTE
19	O	X1BE	44	I	HDI	69	O	PM8F	94	I	TEST5
20	I	X1TC	45	O	HDO	70	I	TEST11	95	I	TEST6
21	O	REC	46	O	VDO	71	I/O	LRRV	96	I	TEST7
22	O	OUT	47	O	IFRM	72	O	$\overline{\text{XINT}}$	97	I	TEST8
23	O	READ	48	O	FRAM	73	I	$\overline{\text{XRES}}$	98	I	TEST9
24	I	PBTC	49	I	DLYI	74	I	$\overline{\text{BKUP}}$	99	I/O	D0
25	I	LTCI	50	O	DLYO	75	O	INTM	100	I/O	D1

12	A0	NT4F	66	INPUT	
13	A1	PL4F	67	A0 - A5	; ADDRESS BUS IN
14	A2	PL8F	68	ALCK	; ARITHMETIC LOGIC UNIT CLOCK IN
16	A3	PM8F	69	BKIN	; BACKUP IN (NEGATIVE LOGIC)
17	A4			BKUP	; BACKUP EXECUTION IN (NEGATIVE LOGIC)
18	A5	D0	99	CK1I	; SYSTEM CLOCK IN
9	CS	D1	100	CSDH	; COMPOSITE SYNC/HD IN
10	WR	D2	1	CSVD	; COMPOSITE SYNC/VD IN
11	RD	D3	2	CTLI	; CTL SIGNAL IN
		D4	5	DEMI	; LONGITUDINAL TIME AND CONTROL CODE IN
42	CSDH	D5	6	DLYI	; INPUT TO DELAY CIRCUIT
43	CSVD	D6	7	DMCK	; LTC DEMODULATE CLOCK IN
		D7	8	FRMI	; FRAME SIGNAL IN
				FWD	; CTL DIRECTION SIGNAL IN
71	LRRV	RSYC	30	HDI	; HORIZONTAL SYNC INPUT
27	FWD	LREV	31	INTE	; EXTERNAL INTERRUPT IN (POSITIVE LOGIC)
26	CTLI	DCLK	32	K16I	; 16KHz CLOCK IN
24	PBTC	DATA	33		BACKUP CLOCK IN
25	LTCI	BER	34	LRRV	; DIRECTION INPUT FOR LTC READER CALCULATION
20	X1TC			LTCI	; GENERATED LTC IN
		X1BE	19	LSHI	; LTC SYNCHRONOUS H INPUT
72	XINIT	REC	21	NORM	; NORMAL/BACKUP MODE SELECT
73	XRES	OUT	22	TEST1 - TEST9	; TEST IN
74	BKUP	READ	23	TEST11	
87	ALCK			PBTC	; PLAYBACK LTC IN
		XBK	38	RD	; READ IN (NEGATIVE LOGIC)
37	K16I	HDO	45	REF	; REFERENCE OF LTC GENERATOR
44	HDI	VDO	46	VCI1	; LTC CLOCK IN-1
49	DLYI	IFRM	47	VCI2	; EXTERNAL LTC IN
51	FRMI	FRAM	48	X1TC	; LTC CLOCK IN-2
52	LSHI	DLYO	50	XRES	; SYSTEM RESET IN (NEGATIVE LOGIC)
57	VCI1	LSHO	55	WR	; WRITE IN (NEGATIVE LOGIC)
58	VCI2	VCEO	56		
61	REF	LTCO	59	OUTPUT	
		CFNG	60	BER	; BI-PHASE MARK ERROR (BIT ERROR)
35	DMCK			BKO	; BACKUP CONTROL OUT-2 (POSITIVE LOGIC)
36	DEMI	INTG	76	CFNG	; COLOR FRAME ERROR FLAG OUT
39	BKIN	INTL	77	CK10	; SYSTEM CLOCK OUT
89	CK1I	INTC	80	CK2M	; CLOCK 2M OUT
93	INTE	INTR	81	CS	; CHIP SELECT IN (NEGATIVE LOGIC)
83	TEST1	INTX	82	D0 - D7	; DATA BUS
84	TEST2			DATA	; DEMODULATED SERIAL DATA OUT
85	TEST3	CK2M	88	DCCK	; LTC DECODE CLOCK OUT
86	TEST4	BKO	41	DCLK	; DEMODULATED CLOCK OUT
94	TEST5	CK10	91	DLYO	; OUTPUT FROM DELAY CIRCUIT
95	TEST6	DCCK	92	FRAM	; FRAME SIGNAL OUT -2
96	TEST7	INTM	75	HDO	; HORIZONTAL SYNC DRIVE OUT
97	TEST8	XOUT	62	IFRM	; FRAME SIGNAL OUT -1
98	TEST9	PORT	63	INTC	; CTL INTERRUPT OUT (POSITIVE LOGIC)
64	NORM	XINT	72	INTG	; GENERATOR INTERRUPT OUT (POSITIVE LOGIC)
70	TEST11			INTL	; READER INTERRUPT -1 OUT (POSITIVE LOGIC)
				INTM	; INTERRUPT MIX OUT (POSITIVE LOGIC)
				INTR	; READER INTERRUPT -2 OUT (POSITIVE LOGIC)
				INTX	; EXTERNAL LTC INTERRUPT OUT (POSITIVE LOGIC)
				LREV	; READ REV/FWD BIT OUT
				LSHO	; LTC SYNCHRONOUS SIGNAL H OUT
				LTCO	; LTC SIGNAL OUT
				NT4F, PL4F, PL8F, PM8F	; DEMODULATED COLOR FRAME OUT
				OUT	; EXTERNAL OUTPUT LTC OUT
				PORT	; PORT
				READ	; READ LTC OUT
				REC	; RECORD LTC OUT
				RSYC	; READ SYNC WORD DATA OUT
				VCEO	; LTC CLOCK OUT
				VDO	; VERTICAL SYNC DRIVE OUT
				X1BE	; BI-PHASE MARK ERROR (NORMAL SPEED READER)
				XBK	; BACKUP CONTROL OUT-1 (NEGATIVE LOGIC)
				XINT	; INVERTED INTERRUPT MIX OUT (NEGATIVE LOGIC)
				XOUT	; INVERTED EXTERNAL OUTPUT LTC OUT

