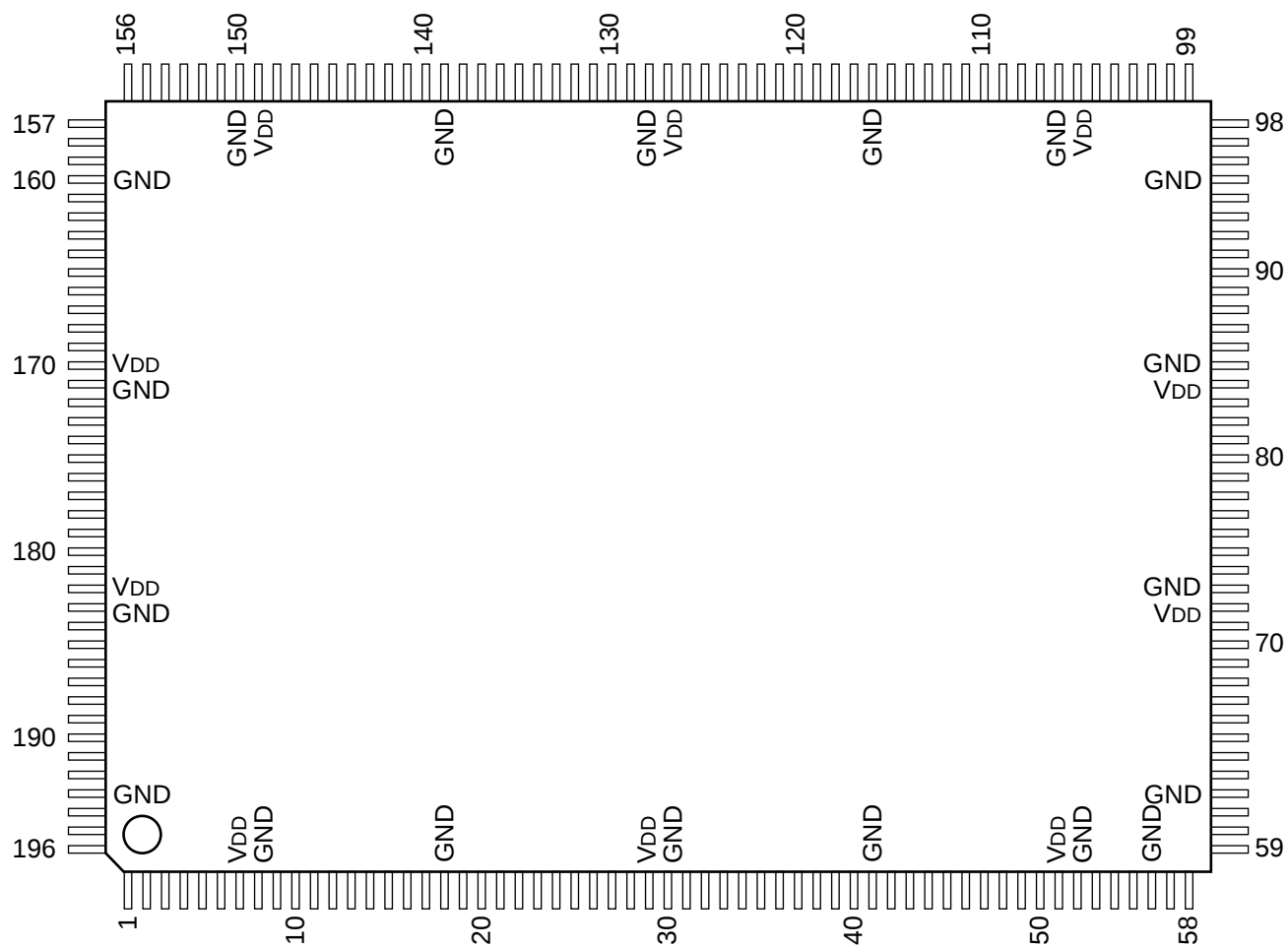


C-MOS DIGITAL ENCODER

—TOP VIEW—



INPUT

CADDR 0 - 2	: SCH SETUP OF OUTPUT SIGNAL
CK 8FSC	: STANDARD CLOCK FOR 8 fsc MODE
CK27	: STANDARD CLOCK FOR 27 MHz MODE
DD SCK	: CLOCK FOR SERIAL COMMUNICATION BETWEEN MICROCOMPUTER
DD START	: STATUS PULSE FOR SERIAL COMMUNICATION BETWEEN MICROCOMPUTER
ICADDR 0 - 3	: ADDRESS AUTHORIZE
MODE DATA	: DATA FOR SERIAL COMMUNICATION BETWEEN MICROCOMPUTER
PANABLK	: ANALOG BLANKING ON/OFF CONTROL
PAL/NTSC	: INITIAL SETUP
PB CF	: COLOR FRAME (3-BIT) OF IMAGE DATA
PB DATA 00 - 09	: IMAGE DATA
PB HD	: HD OF IMAGE DATA
PB VD	: VD OF IMAGE DATA
PB PARITY	: PARITY BIT OF IMAGE DATA
PFILON	: CHROMA FILTER ON/OFF CONTROL
POWER ON RESET	: RESET TERMINAL
REF CF	: COLOR FRAME OF REFERENCE
REF HD	: HD OF REFERENCE
REF VD	: VD OF REFERENCE
ROMSEL 0, 1	: CHROMA FILTER COEFFICIENT SELECT

OUTPUT

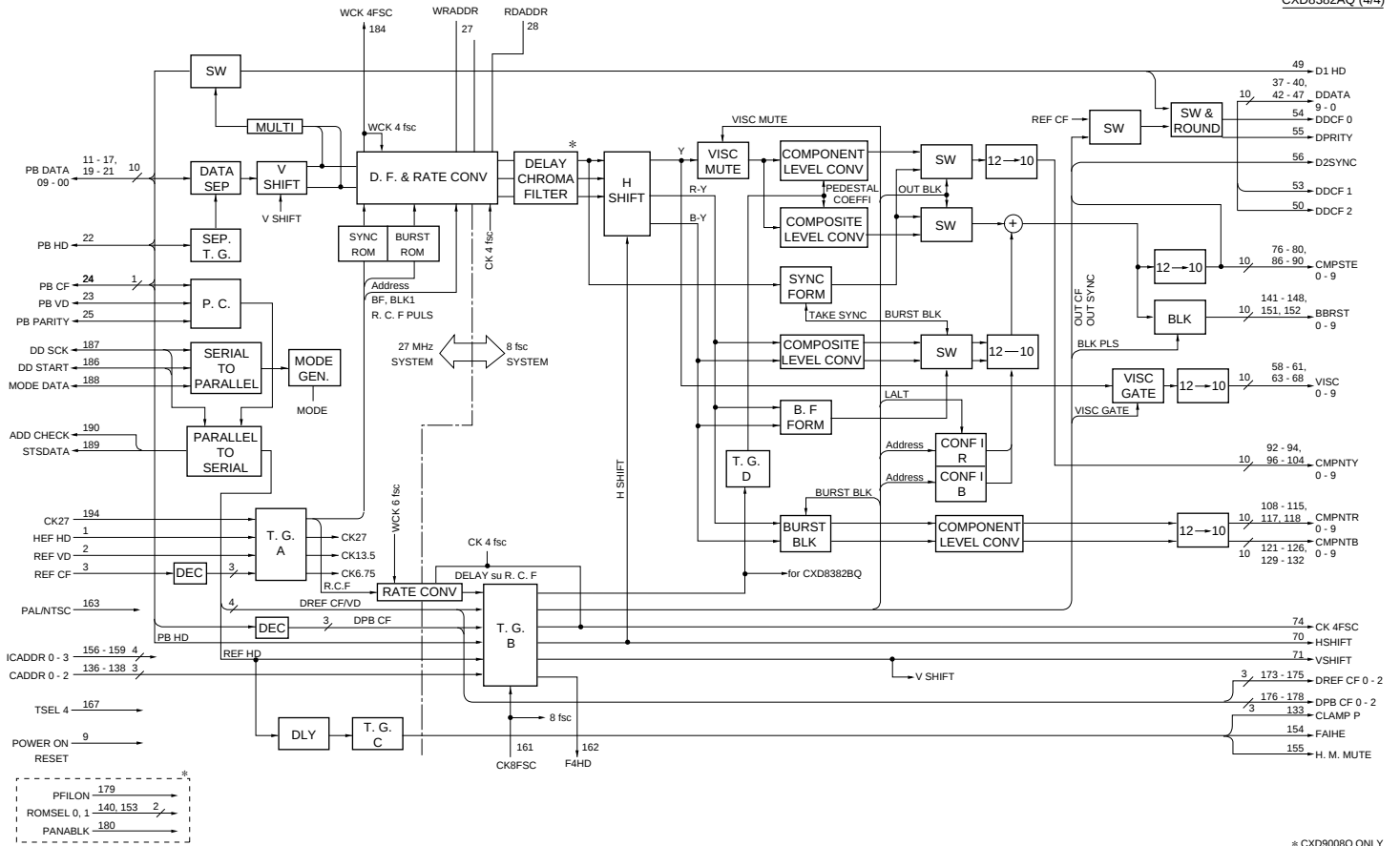
ADD CHECK	: CLOCK FOR SERIAL COMMUNICATION RECEIVE CHECK BETWEEN MICROCOMPUTER
BBRST 0 - 9	: DIGITAL DATA FOR BLACK BURST
CK 4FSC	: 4 fsc CLOCK AT 8 fsc COUNT DOWN
CLAMP P	: CLAMP PULSE
CMPNTB 0 - 9	: COMPONENT B DIGITAL DATA
CMPNTR 0 - 9	: COMPONENT R DIGITAL DATA
CMPNTY 0 - 9	: COMPONENT Y DIGITAL DATA
CMPSTE 0 - 9	: COMPONENT DIGITAL DATA
DDATA 0 - 9	: IMAGE DATA AT D1 OUTPUT MODE, D2 DATA AT D2 OUTPUT MODE
D PARITY	: PARITY BIT FOR DIGITAL DATA
D1 HD	: HD OF 27 MHz (PHASE AGREE WITH DDATA)
DDCF 0	: ON THE BIT 0 OF COLOR FRAME AT D1 AND D2 OUTPUT MODE
DDCF 1	: COLOR FRAME (3-BIT) AT D1 OUTPUT MODE ON THE BIT1 OF COLOR FRAME AT D2 OUTPUT MODE
DDCF 2	: ON THE BIT2 OF COLOR FRAME AT D2 OUTPUT MODE
DPB CF 0 - 2	: PB CF DECODER DATA
DREF CF 0 - 2	: REF CF DECODER DATA
F4 HD	: HD MAKE OF 4 fsc
FAIHD	: HD OF 27MHz SYNC PHASE AGREE WITH ANALOG DATA
H. H MUTE	: MAKE DATA FAIHD
HSHIFT	: H SHIFT MODE
RDADDR	: FIFO READ ADDRESS MSB DATA FOR RATE CONVERT
STSDATA	: RESPONCE FOR SERIAL COMMUNICATION BETWEEN MICROCOMPUTER
VSHIFT	: V SHIFT MODE
VISC 0 - 9	: VISC DIGITAL DATA
WCK 4FSC	: 4 fsc CLOCK FOR RATE CONVERT
WRADDR	: FIFO WRITE ADDRESS MSB DATA FOR RATE CONVERT

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	REF HD	41	—	GND	81	—	*1	121	O	CMPNTB 0	161	I	CK8FSC
2	I	REF VD	42	O	DDATA 5	82	I	T04	122	O	CMPNTB 1	162	O	F4HD
3	I	REF CF	43	O	DDATA 4	83	I	R05	123	O	CMPNTB 2	163	I	PAL/NTSC
4	I	T00	44	O	DDATA 3	84	—	VDD	124	O	CMPNTB 3	164	I	TSEL 1
5	I	T01	45	O	DDATA 2	85	—	GND	125	O	CMPNTB 4	165	I	TSEL 2
6	I	T02	46	O	DDATA 1	86	O	CMPSTE 5	126	O	CMPNTB 5	166	I	TSEL 3
7	—	VDD	47	O	DDATA 0	87	O	CMPSTE 6	127	—	VDD	167	I	MUTE/TSEL 4
8	—	GND	48	—	T12	88	O	CMPSTE 7	128	—	GND	168	I	TSEL 5
9	I	POWER ON RESET	49	O	D1 HD	89	O	CMPSTE 8	129	O	CMPNTB 6	169	I	TSEL 6
10	I	T26	50	O	DDCF 2	90	O	CMPSTE 9	130	O	CMPNTB 7	170	—	VDD
11	I	PB DATA 09	51	—	VDD	91	I	T16	131	O	CMPNTB 8	171	—	GND
12	I	PB DATA 08	52	—	GND	92	O	CMPNTY 0	132	O	CMPNTB 9	172	I	TSEL 7
13	I	PB DATA 07	53	O	DDCF 1	93	O	CMPNTY 1	133	O	CLAMP P	173	O	DREF CF 0
14	I	PB DATA 06	54	O	DDCF 0	94	O	CMPNTY 2	134	I	T22	174	O	DREF CF 1
15	I	PB DATA 05	55	O	DPRITY	95	—	GND	135	I	T23	175	O	DREF CF 2
16	I	PB DATA 04	56	O	D2SYNC	96	O	CMPNTY 3	136	I	CADDR 0	176	O	DPB CF 0
17	I	PB DATA 03	57	I	T13	97	O	CMPNTY 4	137	I	CADDR 1	177	O	DPB CF 1
18	—	GND	58	O	VISC 0	98	O	CMPNTY 5	138	I	CADDR 2	178	O	DPB CF 2
19	I	PB DATA 02	59	O	VISC 1	99	O	CMPNTY 6	139	—	GND	179	I	*2
20	I	PB DATA 01	60	O	VISC 2	100	O	CMPNTY 7	140	I	*2	180	I	*2
21	I	PB DATA 00	61	O	VISC 3	101	O	CMPNTY 8	141	O	BBRST 0	181	I	T28
22	I	PB HD	62	—	GND	102	O	CMPNTY 9	142	O	BBRST 1	182	—	VDD
23	I	PB VD	63	O	VISC 4	103	I	T17	143	O	BBRST 2	183	—	GND
24	I	PB CF	64	O	VISC 5	104	I	T18	144	O	BBRST 3	184	O	WCK4FSC
25	I	PB PARITY	65	O	VISC 6	105	—	VDD	145	O	BBRST 4	185	I	T27
26	I	T03	66	O	VISC 7	106	—	GND	146	O	BBRST 5	186	I	DB START
27	O	WRADDR	67	O	VISC 8	107	I	T19	147	O	BBRST 6	187	I	DD SCK
28	O	RDADDR	68	O	VISC 9	108	O	CMPNTR 0	148	O	BBRST 7	188	I	MODE DATA
29	—	VDD	69	I	T14	109	O	CMPNTR 1	149	—	VDD	189	O	STSDATA
30	—	GND	70	O	HSHIFT	110	O	CMPNTR 2	150	—	GND	190	O	ADD CHECK
31	I	T06	71	O	VSHIFT	111	O	CMPNTR 3	151	O	BBRST 8	191	I	T29
32	I	T07	72	—	VDD	112	O	CMPNTR 4	152	O	BBRST 9	192	I	T30
33	I	T08	73	—	GND	113	O	CMPNTR 5	153	I	*2	193	—	GND
34	I	T09	74	O	CK4FSC	114	O	CMPNTR 6	154	O	FAIHD	194	I	CK27
35	I	T10	75	I	T15	115	O	CMPNTR 7	155	O	H.H MUTE	195	I	T31
36	I	T11	76	O	CMPSTE 0	116	—	GND	156	I	ICADDR 0	196	I	T32
37	O	DDATA 9	77	O	CMPSTE 1	117	O	CMPNTR 8	157	I	ICADDR 1			
38	O	DDATA 8	78	O	CMPSTE 2	118	O	CMPNTR 9	158	I	ICADDR 2			
39	O	DDATA 7	79	O	CMPSTE 3	119	I	T20	159	I	ICADDR 3			
40	O	DDATA 6	80	O	CMPSTE 4	120	I	T21	160	—	GND			

*1 Connect Pin 81 to ground.

*2

TYPE	PIN NO.			
	140	153	179	180
CXD8382AQ CXD8382BQ	T24	T25	T33	T34
CXD9008Q	ROMSEL0	ROMSEL1	PFILON	PANABLK



* CXD9008Q ONLY