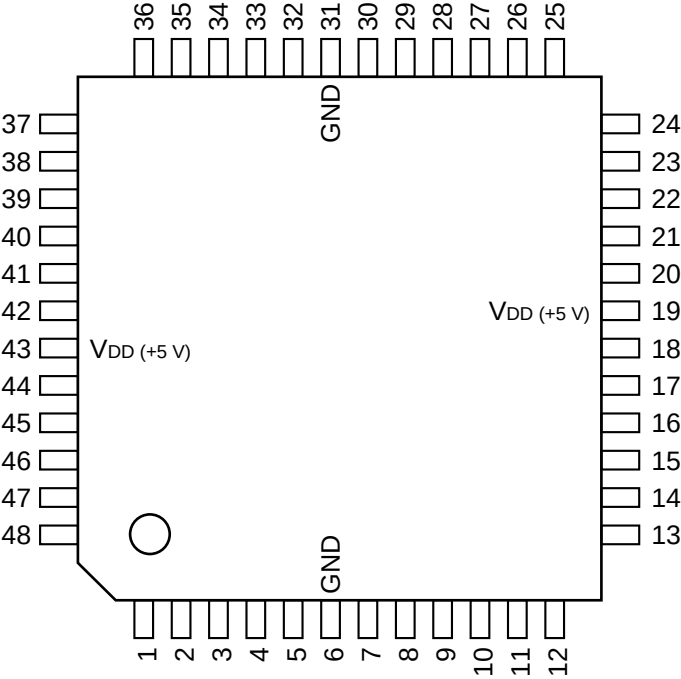
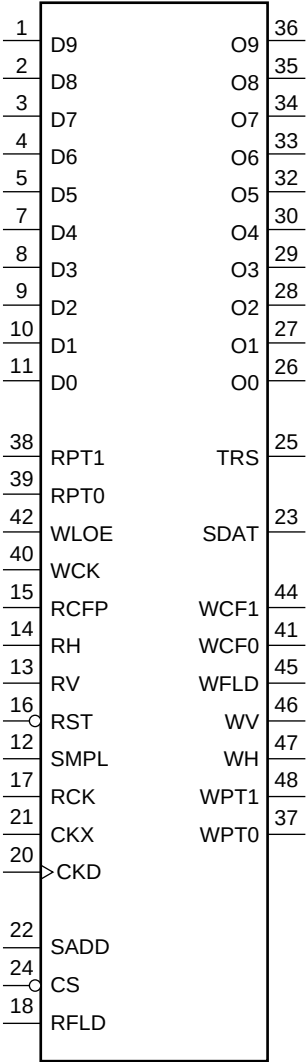

C-MOS TBC CONTROL
—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	D9	25	O	TRS
2	I	D8	26	O	O0
3	I	D7	27	O	O1
4	I	D6	28	O	O2
5	I	D5	29	O	O3
6	—	GND	30	O	O4
7	I	D4	31	—	GND
8	I	D3	32	O	O5
9	I	D2	33	O	O6
10	I	D1	34	O	O7
11	I	D0	35	O	O8
12	I	SMPL	36	O	O9
13	I	RV	37	O	WPT0
14	I	RH	38	I	RPT1
15	I	RCFP	39	I	RPT0
16	I	RST	40	I	WCK
17	I	RCK	41	O	WCF0
18	I	RFLD	42	I	WLOE
19	—	VDD	43	—	VDD
20	I	CKD	44	O	WCF1
21	I	CKX	45	O	WFLD
22	I	SADD	46	O	WV
23	I/O	SDAT	47	O	WH
24	I	CS	48	O	WPT1



INPUT

ADR ; SERIAL ADDRESS
 CKD ; SERIAL INTERFACE CLOCK
 CKX ; SWITCHING TIMING PULSE
 $\overline{\text{CS}}$; CHIP SELECT
 D0 - D9 ; DATA IN
 RCFP ; REF. COLOR FRAME PULSE
 RCK ; READ CLOCK
 RFLD ; REF. FIELD
 RH ; REF. HD
 RPT0, RPT1 ; READ PORT 0, 1
 $\overline{\text{RST}}$; RESET
 RV ; REF. VD
 SMPL ; SAMPLE PULSE
 WCK ; WRITE CLOCK

OUTPUT

O0 - O9 ; DATA OUT
 TRS ; TIMING REF. SIGNAL
 WCF0, WCF1 ; WRITE COLOR FRAME 0, 1
 WFLD ; WRITE FIELD
 WH ; WRITE HD
 WLOE ; WRITE LINE ODD/EVEN
 WPT0, WPT1 ; WRITE PORT 0, 1
 WV ; WRITE VD

INPUT/OUTPUT

SDAT ; SERIAL DATA

