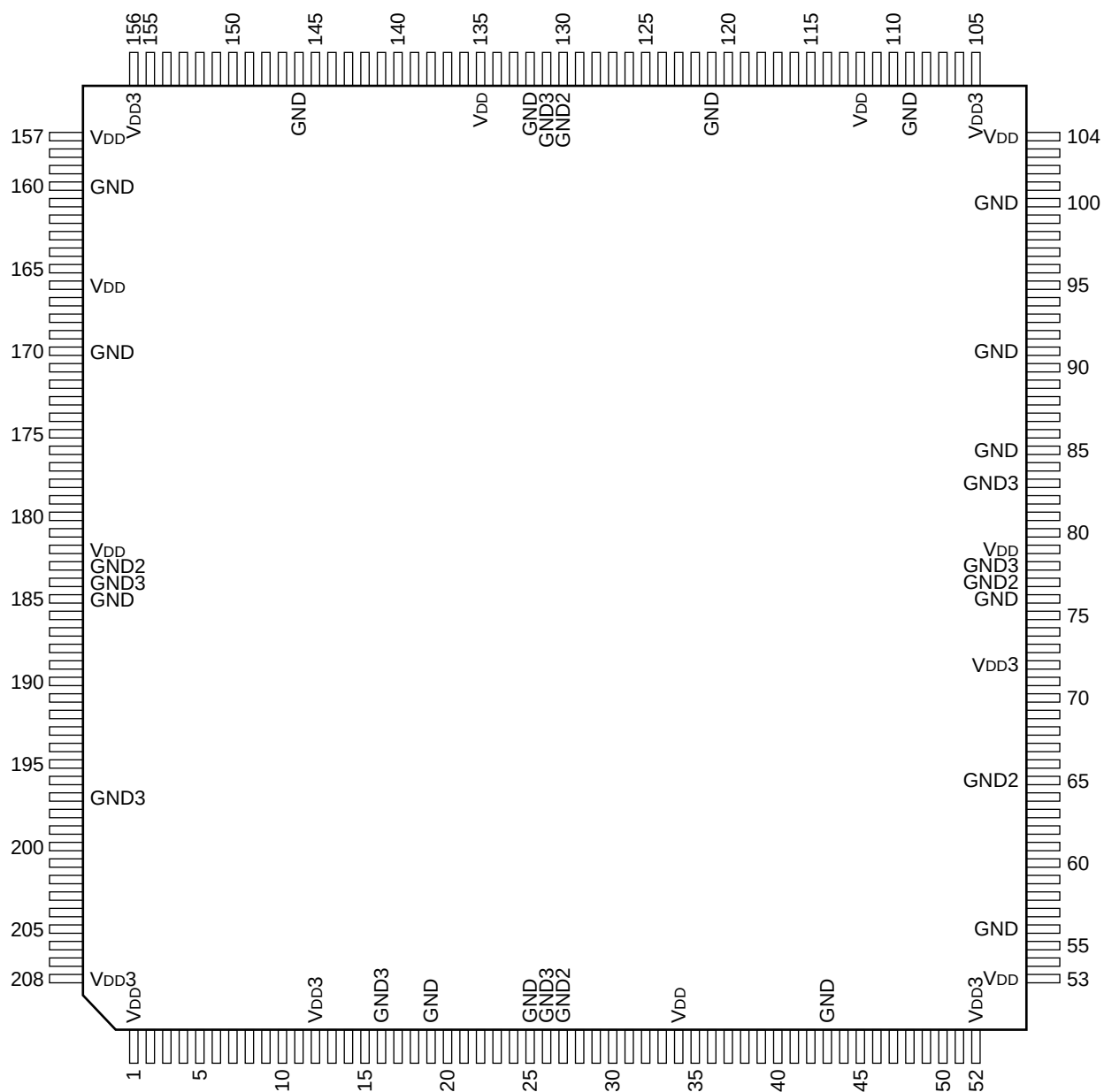


—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	53	—	VDD	105	—	VDD3	157	—	VDD
2	I	B9	54	O	$\overline{\text{WE}}$	106	O	RW4	158	O	GB0
3	I	B8	55	O	$\overline{\text{OE}}$	107	O	RW3	159	O	TST8
4	I	B7	56	—	GND	108	O	RW2	160	—	GND
5	I	B6	57	I	TPD0	109	—	GND	161	O	TST7
6	I	B5	58	I	TPD1	110	O	RW1	162	O	TST6
7	I	B4	59	I	TPD2	111	O	RW0	163	O	TST5
8	I	B3	60	I	TPD3	112	—	VDD	164	O	TST4
9	I	B2	61	I	TPD4	113	O	GW7	165	O	TST3
10	I	B1	62	I	TPD5	114	O	GW6	166	—	VDD
11	I	B0	63	I	TPD6	115	O	GW5	167	O	TST2
12	—	VDD3	64	I	TPD7	116	O	GW4	168	O	TST1
13	I/O	ICCK	65	—	GND2	117	O	GW3	169	O	TST0
14	I/O	SICDT	66	I	TPW	118	O	GW2	170	—	GND
15	I	ASET	67	I	TPR	119	O	GW1	171	O	FBB3
16	—	GND3	68	I	TPA0	120	O	GW0	172	O	FBB2
17	O	ICDS	69	I	TPA1	121	—	GND	173	O	FBB1
18	O	TDO	70	I	TPA2	122	O	BB9	174	O	FBB0
19	—	GND	71	I	TPA3	123	O	BB8	175	O	FBR3
20	I	STCK	72	—	VDD3	124	O	BB7	176	O	FBR2
21	I	STDI	73	I	TPA4	125	O	BB6	177	O	FBR1
22	I	STMS	74	I	TPA5	126	O	BB5	178	O	FBR0
23	I	STRST	75	I	TPA6	127	O	BB4	179	O	FBG3
24	I	CLR	76	—	GND	128	O	BB3	180	O	FBG2
25	—	GND	77	—	GND2	129	O	BB2	181	O	FBG1
26	—	GND3	78	—	GND3	130	—	GND2	182	—	VDD
27	—	GND2	79	—	VDD	131	—	GND3	183	—	GND2
28	O	A13	80	I	TPA7	132	—	GND	184	—	GND3
29	O	A12	81	I	HD	133	O	BB1	185	—	GND
30	O	A11	82	I	VD	134	O	BB0	186	O	FBG0
31	O	A10	83	—	GND3	135	—	VDD	187	I	G9
32	O	A9	84	I	CLK	136	O	RB9	188	I	G8
33	O	A8	85	—	GND	137	O	RB8	189	I	G7
34	—	VDD	86	O	TP1	138	O	RB7	190	I	G6
35	O	A7	87	O	TP2	139	O	RB6	191	I	G5
36	O	A6	88	O	ADHD	140	O	RB5	192	I	G4
37	O	A5	89	O	ADV D	141	O	RB4	193	I	G3
38	O	A4	90	O	TPOUT	142	O	RB3	194	I	G2
39	O	A3	91	—	GND	143	O	RB2	195	I	G1
40	O	A2	92	O	BW7	144	O	RB1	196	I	G0
41	O	A1	93	O	BW6	145	O	RB0	197	—	GND3
42	O	A0	94	O	BW5	146	—	GND	198	I	R9
43	—	GND	95	O	BW4	147	O	GB9	199	I	R8
44	I/O	D7	96	O	BW3	148	O	GB8	200	I	R7
45	I/O	D6	97	O	BW2	149	O	GB7	201	I	R6
46	I/O	D5	98	O	BW1	150	O	GB6	202	I	R5
47	I/O	D4	99	O	BW0	151	O	GB5	203	I	R4
48	I/O	D3	100	—	GND	152	O	GB4	204	I	R3
49	I/O	D2	101	O	RW7	153	O	GB3	205	I	R2
50	I/O	D1	102	O	RW6	154	O	GB2	206	I	R1
51	I/O	D0	103	O	RW5	155	O	GB1	207	I	R0
52	—	VDD3	104	—	VDD	156	—	VDD3	208	—	VDD3

INPUT

ASET	: IIC ASET
B0 - B9	: B CHANNEL
CLK	: 18 MHz CLOCK
CLR	: POWER ON RESET
G0 - G9	: G CHANNEL
HD	: HORIZONTAL SYNC
R0 - R9	: R CHANNEL
STCK	: SELF-DIAGNOSIS CLOCK
STDI	: SELF-DIAGNOSIS DATA
STMS	: SELF-DIAGNOSIS MODE SELECT
STRST	: SELF-DIAGNOSIS RESET
TPA0 - TPA6	: (NO CONNECTION)
TPD0 - TPD7	: (NO CONNECTION)
TPA7, TPR	: (CONNECT TO GROUND)
TPW	: (NO CONNECTION)
VD	: VERTICAL SYNC

OUTPUT

A0 - A13	: RAM ADDRESS
ADHD	: ADVANCED HD
ADVD	: ADVANCED VD
BB0 - BB9	: B CHANNEL BLACK SHADING COMPENSATION DATA
BW0 - BW7	: B CHANNEL WHITE SHADING COMPENSATION DATA
FBB0 - FBB3	: B CHANNEL FEEDBACK CLAMP DATA
FBG0 - FBG3	: G CHANNEL FEEDBACK CLAMP DATA
FBR0 - FBR3	: R CHANNEL FEEDBACK CLAMP DATA
GB0 - GB9	: G CHANNEL BLACK SHADING COMPENSATION DATA
GW0 - GW7	: G CHANNEL WHITE SHADING COMPENSATION DATA
ICDS	: IIC ACK
$\overline{OE}$	: RAM OUTPUT ENABLE SIGNAL
RB0 - RB9	: R CHANNEL BLACK SHADING COMPENSATION DATA
RW0 - RW7	: R CHANNEL WHITE SHADING COMPENSATION DATA
TDO	: SELF-DIAGNOSIS DATA
TP1, TP2	: (NO CONNECTION)
TPOUT	: (NO CONNECTION)
TST0 - TST8	: TEST SAW/RECTANGLE
$\overline{WE}$	: RAM WRITE ENABLE SIGNAL

INPUT/OUTPUT

D0 - D7	: RAM DATA
ICCK	: IIC SCL
SICDT	: IIC SDA