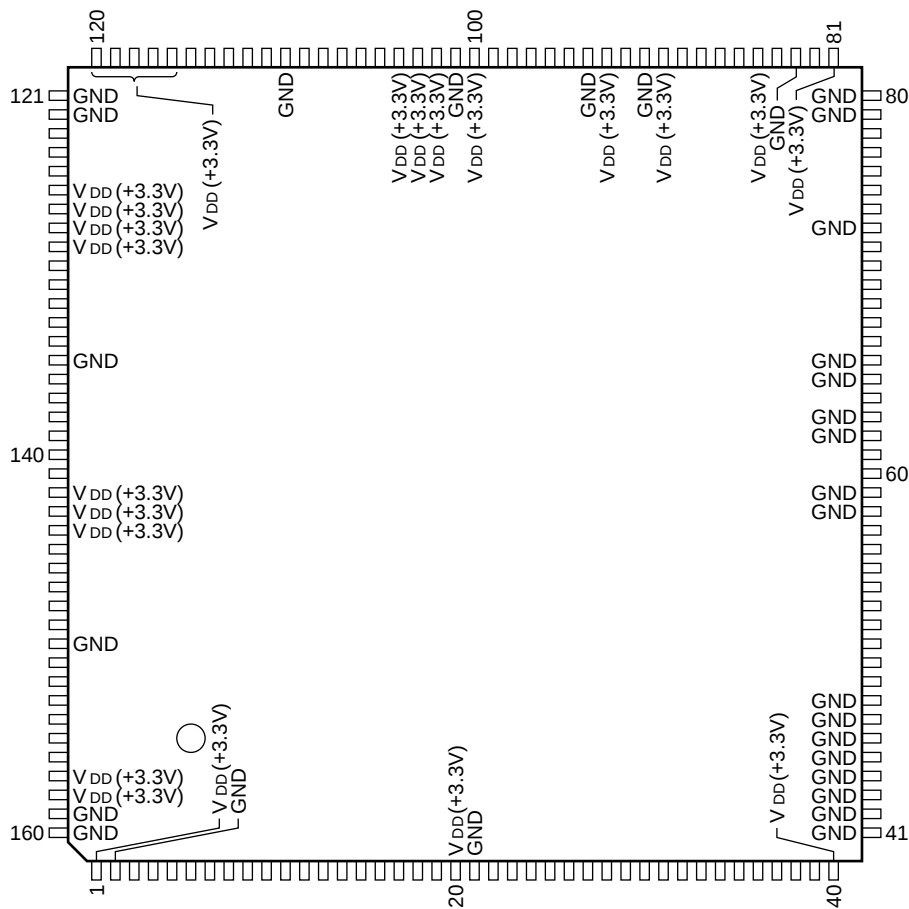

C-MOS RATE CONVERTER
- TOP VIEW -



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	–	V _{DD}	41	–	GND	81	–	V _{DD}	121	–	GND
2	–	GND	42	–	GND	82	I	CK36	122	–	GND
3	I	CYC0	43	–	GND	83	–	GND	123	I	PB
4	I	CYC1	44	–	GND	84	I	CK27	124	I	REC
5	I	CCC0	45	–	GND	85	–	V _{DD}	125	I	IOEN
6	I	CCC1	46	–	GND	86	I	HDI	126	–	V _{DD}
7	I	CYV0	47	–	GND	87	I	VDI	127	–	V _{DD}
8	I	CYV1	48	–	GND	88	I	SYI	128	–	V _{DD}
9	I	CCV0	49	I	TIIC	89	I	CFI	129	–	V _{DD}
10	I	CCV1	50	I	IID0	90	–	V _{DD}	130	I/O	CC0
11	I	I360	51	I	IID1	91	–	GND	131	I/O	CC1
12	I	I270	52	I	IID2	92	O	VK27	132	I/O	CC2
13	I	I180	53	I	IID3	93	–	V _{DD}	133	I/O	CC3
14	I	I135	54	I	IID4	94	–	GND	134	I/O	CC4
15	I	EDI0	55	I	IID5	95	O	HDO	135	–	GND
16	I	EDI1	56	I	IID6	96	O	VDO	136	I/O	CC5
17	I	EDO0	57	I	IID7	97	O	SYO	137	I/O	CC6
18	I	EDO1	58	–	GND	98	O	CFO	138	I/O	CC7
19	I	TLD	59	–	GND	99	O	BLKO	139	I/O	CC8
20	–	V _{DD}	60	I	IIA0	100	–	V _{DD}	140	I/O	CC9
21	–	GND	61	I	IIA1	101	–	GND	141	I/O	CC10
22	I	TTS0	62	–	GND	102	–	V _{DD}	142	–	V _{DD}
23	I	TTS1	63	–	GND	103	–	V _{DD}	143	–	V _{DD}
24	I	TTS2	64	I	IICW	104	–	V _{DD}	144	–	V _{DD}
25	I	TTS3	65	–	GND	105	I/O	VYC0	145	I/O	CY0
26	I	TTS4	66	–	GND	106	I/O	VYC1	146	I/O	CY1
27	I	TDT0	67	I/O	ICD	107	I/O	VYC2	147	I/O	CY2
28	I	TDT1	68	I	ICK	108	I/O	VYC3	148	I/O	CY3
29	I	TDT2	69	O	ICS	109	I/O	VYC4	149	I/O	CY4
30	I	TDT3	70	O	TDO	110	–	GND	150	–	GND
31	I	TDT4	71	I	ASET	111	I/O	VYC5	151	I/O	CY5
32	I	TDT5	72	I	PRST	112	I/O	VYC6	152	I/O	CY6
33	I	TDT6	73	–	GND	113	I/O	VYC7	153	I/O	CY7
34	I	TDT7	74	I	TCK	114	I/O	VYC8	154	I/O	CY8
35	I	TDT8	75	I	TDI	115	I/O	VYC9	155	I/O	CY9
36	I	TDT9	76	I	TMS	116	–	V _{DD}	156	I/O	CY10
37	I	TYFL	77	I	TRST	117	–	V _{DD}	157	–	V _{DD}
38	I	TCFL	78	I	BLKI	118	–	V _{DD}	158	–	V _{DD}
39	I	TCOE	79	–	GND	119	–	V _{DD}	159	–	GND
40	–	V _{DD}	80	–	GND	120	–	V _{DD}	160	–	GND

INPUT

ASET ; CONTROLLED ADDRESS
 BLKI ; BLANKING
 CCC0, 1 ; C COEFFICIENT SELECT SIGNAL DELAY (REC)
 CCV0, 1 ; C COEFFICIENT SELECT SIGNAL DELAY (PB)
 CFI ; COLOR FRAMING
 CK27 ; 27MHz
 CK36 ; 36MHz
 CYC0, 1 ; Y COEFFICIENT SELECT SIGNAL DELAY (REC)
 CYV0, 1 ; Y COEFFICIENT SELECT SIGNAL DELAY (PB)
 EDI0, 1 ; ENC DELAY FOR Y FROM PROCESS
 EDO0, 1 ; ENC DELAY FOR Y TO PROCESS
 HDI ; HD
 I135 ; 13.5MHz FREQUENCY RESET VALUE
 I180 ; 18MHz FREQUENCY RESET VALUE
 I270 ; 27MHz FREQUENCY RESET VALUE
 I360 ; 36MHz FREQUENCY RESET VALUE
 ICK ; SCL
 IIA0,1 ; IIC WRITE ADDRESS (TEST MODE)
 IICW ; IIC WRITE GATE (TEST MODE)
 IID0-7 ; IIC DATA (TEST MODE)
 IOEN ; TWO-WAYS BUFFER CONTROL
 PB ; PLAY BACK
 PRST ; POWER ON RESET
 REC ; RECORD
 SYI ; SYNC
 TCFL ; C FILTER (TEST MODE)
 TCK ; BOUNDARY SCAN CLOCK
 TCOE ; Y/C FILTER COEFFICIENT (TEST MODE)
 TDI ; BOUNDARY SCAN DATA
 TDT0-9 ; FILTER COEFFICIENT (TEST MODE)
 TIIC ; IIC DATA (TEST MODE)
 TLD ; FILTER COEFFICIENT LOAD (TEST MODE)
 TMS ; SELF-DIAGNOSIS MODE (TEST MODE SELECT)
 TRST ; BOUNDARY SCAN RESET
 TTS0-4 ; FILTER TAP SELECT (TEST MODE)
 TYFL ; Y FILTER (TEST MODE)
 VDI ; VD

OUTPUT

BLKO ; BLANKING
 CFO ; COLOR FLAMING
 HDO ; HD
 ICS ; ACK
 SYO ; SYNC
 TDO ; BOUNDARY SCAN DATA
 VDO ; VD
 VK27 ; 27MHz

INPUT/OUTPUT

CC0-10 ; CAMERA CHROMA DATA
 CY0-10 ; CAMERA LUMINANCE DATA
 ICD ; SDA
 VYC0-9 ; VTR Y/C VIDEO DATA

