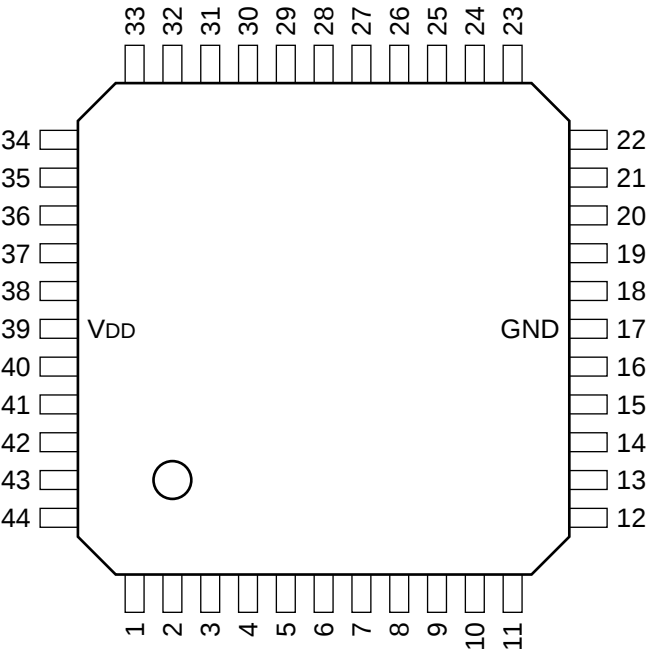


C-MOS PULSE GENERATOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	I01	12	I	I11	23	I	TEST	34	O	OT10
2	I	I02	13	I	I12	24	O	OT00	35	O	OT11
3	I	I03	14	I	I13	25	O	OT01	36	O	OT12
4	I	I04	15	I	I14	26	O	OT02	37	O	OT13
5	I	I05	16	I	I15	27	O	OT03	38	O	OT14
6	I	I06	17	—	GND	28	O	OT04	39	—	VDD
7	I	I07	18	I	A19	29	O	OT05	40	O	OT15
8	I	I08	19	I	I17	30	O	OT06	41	I	CLK
9	I	MODE	20	I	RST	31	O	OT07	42	O	OT16
10	I	I09	21	I	CKH	32	O	OT08	43	O	RAM
11	I	I10	22	I	TES2	33	O	OT09	44	O	ROM

INPUT

A19 : ADDRESS INPUT

CKH : FOR TEST

CLK : CLOCK

I01 - I17 : I NPUT

MODE : MODE SELECT

(H : WINDOW GEN/L : CHIP SELECT LOGIC)

RST : RESET

TES2 : FOR TEST

TEST : FOR TEST

OUTPUT

OT00 - OT16 : OUTPUT

RAM : RAM OUT

ROM : ROM OUT

