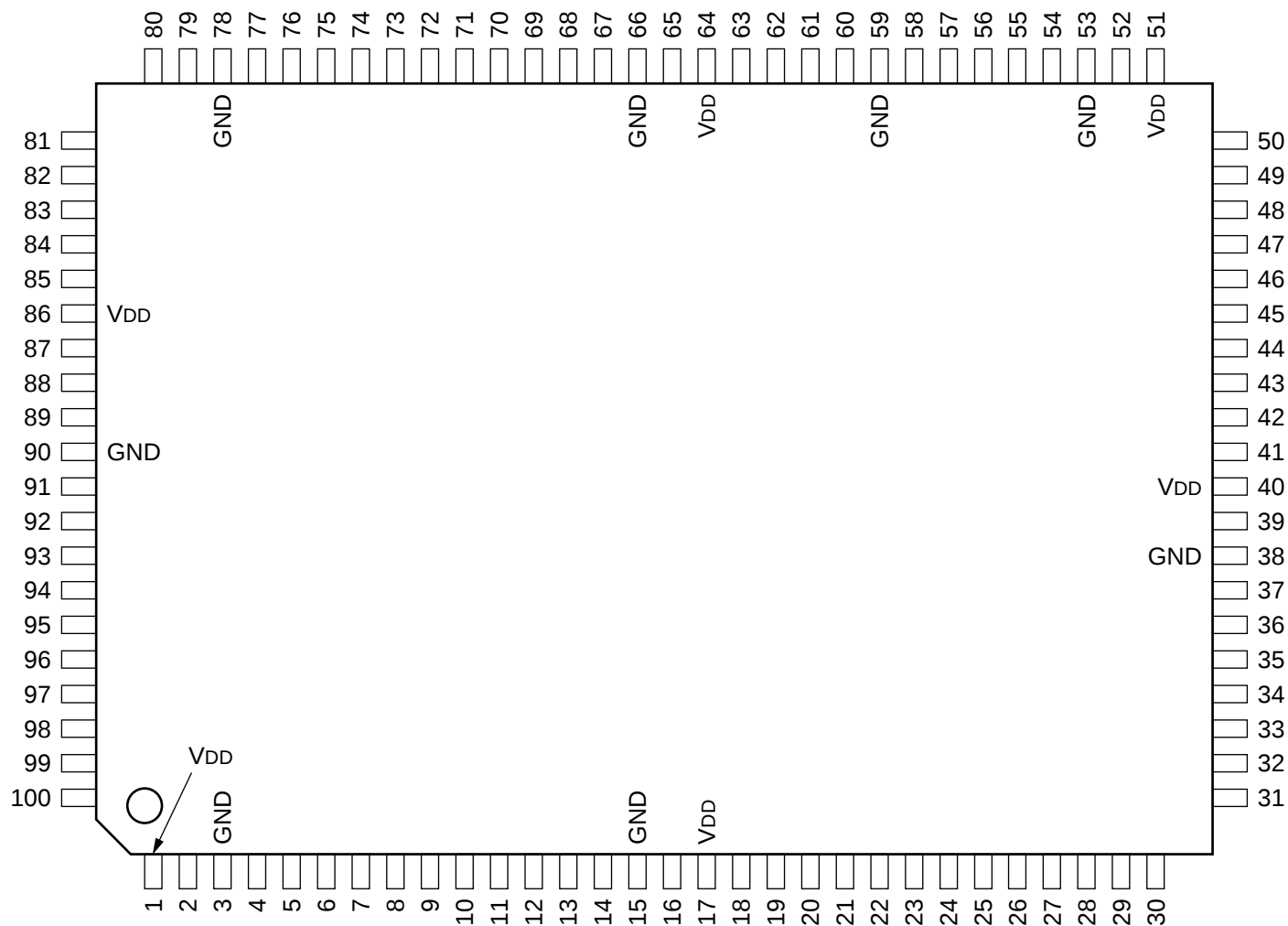


C-MOS VIDEO OUTPUT PROCESSOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	V _{DD}	26	I	V8	51	—	V _{DD}	76	I	V BLK FIX
2	I	EWRST	27	I	V9	52	O	CKO	77	I	DBLK ABLK
3	—	GND	28	—	GND	53	—	GND	78	—	GND
4	I	ERRST	29	I	HD	54	O	DATA PHS	79	I	BLK MODE0
5	I	U0	30	I	FD	55	O	DATA PHS	80	I	BLK MODE1
6	I	U1	31	I	CFP	56	O	TRS JST	81	I	BLK MODE2
7	I	U2	32	I	PARA/SERI	57	O	SEL STAT	82	I	MUX MODE0
8	I	U3	33	I	CKX	58	O	SEL STAT	83	I	MUX MODE1
9	I	U4	34	I	CS	59	—	GND	84	I	525/625
10	I	U5	35	I	SADRS	60	I	TEST MODE0	85	I	D1/D2
11	I	U6	36	I	CKD	61	I	TEST MODE1	86	—	V _{DD}
12	I	U7	37	I/O	SDATA	62	I	TEST MODE2	87	I	BLK GATE
13	I	U8	38	—	GND	63	I	TNCON	88	I	TRS SEL0
14	I	U9	39	I	CLK	64	—	V _{DD}	89	I	TRS SEL1
15	—	GND	40	—	V _{DD}	65	O	TOUT	90	—	GND
16	I	DIAG SMPL	41	O	OUT0	66	—	GND	91	I	Y0
17	—	V _{DD}	42	O	OUT1	67	I	RDCLR	92	I	Y1
18	I	V0	43	O	OUT2	68	I	DR ON	93	I	Y2
19	I	V1	44	O	OUT3	69	I	TRS EN	94	I	Y3
20	I	V2	45	O	OUT4	70	I	BURST EN	95	I	Y4
21	I	V3	46	O	OUT5	71	I	SYNC EN	96	I	Y5
22	I	V4	47	O	OUT6	72	I	AUTO TRS	97	I	Y6
23	I	V5	48	O	OUT7	73	I	CK INV	98	I	Y7
24	I	V6	49	O	OUT8	74	I	CLIP	99	I	Y8
25	I	V7	50	O	OUT9	75	I	LIMIT	100	I	Y9

INPUT

525/625	: 525/625 SELECT
AUTO TRS	: AUTO TRS ADD MODE
BLK GATE	: BRANKING SIGNAL
BLK MODE0 - 2	: BRANKING MODE
BURST EN	: BURST ADD ENABLE
CKD	: SERIAL INTERFACE SERIAL DATA
CK INV	: CLOCK INVERT ENABLE
CKX	: CONTROL REGISTER OPERATION TIMING
CLIP	: WHITE CLIP AND DARK CLIP ENABLE
CLK	: SYSTEM CLOCK
CPF	: SYSTEM CFP (D2 MODEL)
CS	: CHIP SELECTOR
D1/D2	: CONTROL REGISTER PARALLEL DATA
DBLK ABLK	: DIGITAL/ANALOG BLANKING SELECT
DIAGSMPL	: SELF DIAGNOSIS SAMPLE PULS
DR ON	: ROUND ENABLE
ERRST	: INPUT READ RESET PULSE
EWRST	: INPUT WRITE RESET PULSE
FD	: SYSTEM FD (D1 MODEL)
HD	: SYSTEM HD
LIMIT	: LIMITER ENABLE
MUX MODE0, 1	: MULTIPLEX MODE
PARA/SERI	: PARALLEL/SERIAL SELECT
RDCLR	: CLEAR PULSE
SADRS	: CONTROL REGISTER SIGNAL ADDRESS
SYNC EN	: SYNC ADD ENABLE
TEST0 - 2	: TEST POINT
TNCON	: TEST POINT
TRS EN	: TRS ADD ENABLE
TRS SEL0, 1	: MANUAL MODE TRS MIX
V BLK FIX	: VERTICAL BLANKING LENGTH FIX
U0 - U9, Y0 - Y9, V0 - V9	: VIDEO SIGNAL

OUTPUT

CKO	: CLOCK
DATA PHS	: OUTPUT VIDEO DATA PHASE
OUT0 - 9	: VIDEO SIGNAL
SEL START	: SYNC, TRS ADD TIMING
TOUT	: TEST POINT
TRS JST	: TRS PHASE CHECK PULSE

INPUT/OUTPUT

SDATA	: CONTROL REGISTER SERIAL DATA
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