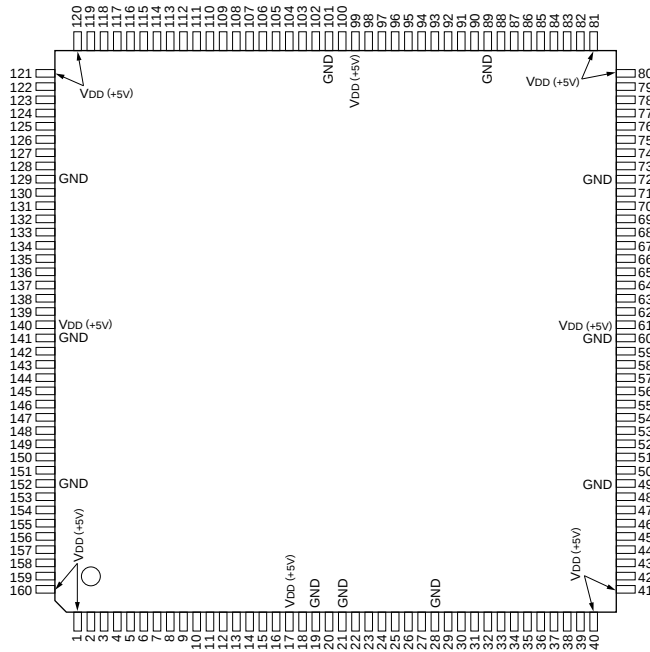

C-MOS COMBINER PROCESSOR
-TOP VIEW-



35	KA0	KOUT0	57	
36	KA1	KOUT1	58	
37	KA2	KOUT2	59	
38	KA3	KOUT3	62	
39	KA4	KOUT4	63	
42	KA5	KOUT5	64	
43	KA6	KOUT6	65	
44	KA7	KOUT7	66	
45	KA8	KOUT8	67	
46	KA9	KOUT9	68	
47	KA10	KOUT10	69	
48	KA11	KOUT11	70	
73	KA12	KOUT12	71	
74	KB0	KAB0	22	
75	KB1	KAB1	23	
76	KB2	KAB2	24	
77	KB3	KAB3	25	
78	KB4	KAB4	26	
79	KB5	KAB5	27	
82	KB6	KAB6	50	
83	KB7	KAB7	51	
84	KB8	KAB8	52	
85	KB9	KAB9	53	
86	KB10	KAB10	54	
87	KB11	KAB11	55	
88	KB12	KAB12	56	
107	ZA0	GZ0	90	
108	ZA1	GZ1	91	
109	ZA2	GZ2	92	
110	ZA3	GZ3	93	
111	ZA4	GZ4	94	
112	ZA5	GZ5	95	
113	ZA6	GZ6	96	
114	ZA7	GZ7	97	
115	ZA8	GZ8	98	
116	ZA9	GZ9	100	
117	ZA10	GZ10	102	
118	ZA11	GZ11	103	
119	ZA12	GZ12	104	
122	ZA13	ZOUT0	130	
123	ZA14	ZOUT1	131	
124	ZA15	ZOUT2	132	
125	ZA16	ZOUT3	133	
126	ZA17	ZOUT4	134	
127	ZA18	ZOUT5	135	
128	ZA19	ZOUT6	136	
153	ZB0	ZOUT7	137	
154	ZB1	ZOUT8	138	
155	ZB2	ZOUT9	139	
156	ZB3	ZOUT10	142	
157	ZB4	ZOUT11	143	
158	ZB5	ZOUT12	144	
159	ZB6	ZOUT13	145	
3	ZB7	ZOUT14	146	
4	ZB8	ZOUT15	147	
5	ZB9	ZOUT16	148	
6	ZB10	ZOUT17	149	
7	ZB11	ZOUT18	150	
8	ZB12	ZOUT19	151	
9	ZB13	ZOUT20	152	
10	ZB14			
11	ZB15	SADRS	29	
12	ZB16	SDATA	30	
13	ZB17	CS	31	
14	ZB18	CKX	32	
15	ZB19	CKD	33	
16	ZB20	RST	34	
18	CKH	TNCON	106	
20		TOUT	105	

INPUT

CKD : CLOCK FOR SERIAL INTERFACE
 CKX : REGISTER EXECUTE CLOCK
 CLK : SYSTEM CLOCK INPUT
 CS : CHIP SELECTER
 KA0 - KA12 : KA MUX MODE = 0
 CMB MODE : INTERNAL KEY INPUT
 OVL MODE : OVER KEY INPUT
 FADE MODE : KEY INPUT
 DIM MODE : LUMINANCE (Y) INPUT
 KA MUX MODE = 1
 MULTIPLEXED SIGNAL OF KA AND KB INPUT
 KB0 - KB12 : KB MUX MODE = 0
 CMB MODE : EXTERNAL KEY INPUT
 OVL MODE : UNDER KEY INPUT
 FADE MODE : NOT USE
 DIM MODE : CHROMINANCE (C) INPUT
 KB MUX MODE = 1
 NOT USE
 RST : REGISTER RESET
 SADR5 : SERIAL ADDRESS
 TNCON : TEST PIN (FIX HIGH LEVEL)
 ZA0 - ZA19 : ZA MUX MODE = 00
 CMB MODE : INTERNAL Z INPUT
 OVL MODE : OVER Z INPUT
 FADE MODE : Z INPUT
 DIM MODE : Z INPUT
 ZA MUX MODE = 01
 MULTIPLEXED SIGNAL OF ZA ZB
 ZA MUX MODE = 10
 UPPER 10 BITS : ZA MULTIPLEXED SIGNAL INPUT
 UNDER 10 BITS : ZB MULTIPLEXED SIGNAL INPUT
 ZB0 - ZB19 : ZB MUX MODE = 00
 CMB MODE : INTERNAL Z INPUT
 OVL MODE : UNDER Z INPUT
 FADE MODE : KEY OFFSET INPUT
 DIM MODE : Y OFFSET AND C OFFSET INPUT
 ZB MUX MODE = 01
 MULTIPLEXED SIGNAL OF DELAYED ZA - ZB AND Z
 GAIN INPUT
 ZB MUX MODE = 10
 UPPER 11 BITS : ZA - ZB MULTIPLEXED SIGNAL
 UNDER 10 BITS : Z GAIN MULTIPLEXED SIGNAL

OUTPUT

KAB0 - KAB12 : KOUT MUX MODE = 0
 CMB MODE : MIX GAIN OUTPUT
 OVL MODE : MIX GAIN OUTPUT
 FADE MODE : NOT USE
 DIM MODE : DIMMED CHROMINANCE OUTPUT
 KOUT MUX MODE = 1
 NOT USE
 KOUT0 - KOUT12 : KOUT MUX MODE = 0
 CMB MODE : OUTPUT KEY
 OVL MODE : OUTPUT KEY
 FADE MODE : FADED OUTPUT KEY
 DIM MODE : DIMMED LUMINANCE
 KOUT MUX MODE = 1
 KOUT AND KAB MULTIPLEXED SIGNAL
 TOUT : TEST OUT
 ZOUT0 - ZOUT20 : ZOUT MUX MODE = 00
 CMB MODE : Z OUTPUT
 OVL MODE : Z OUTPUT
 FADE MODE : NOT USE
 DIM MODE : NOT USE
 ZB MUX MODE = 01
 ZA - ZB & ZOUT MULTIPLEXED SIGNAL OUTPUT
 ZB MUX MODE = 10
 UPPER 11 BITS : ZA - ZB MULTIPLEXED SIGNAL
 UNDER 10 BITS : ZOUT MULTIPLEXED SIGNAL
INPUT/OUTPUT
 GZ0 - GZ12 : CMB MODE : PRIORITY INPUT / OUTPUT
 OVL MODE : KEY SOFTNESS INPUT / OUTPUT
 FADE MODE : FADE GAIN INPUT / OUTPUT
 DIM MODE : DIM GAIN INPUT / OUTPUT
 SDATA : SERIAL DATA

