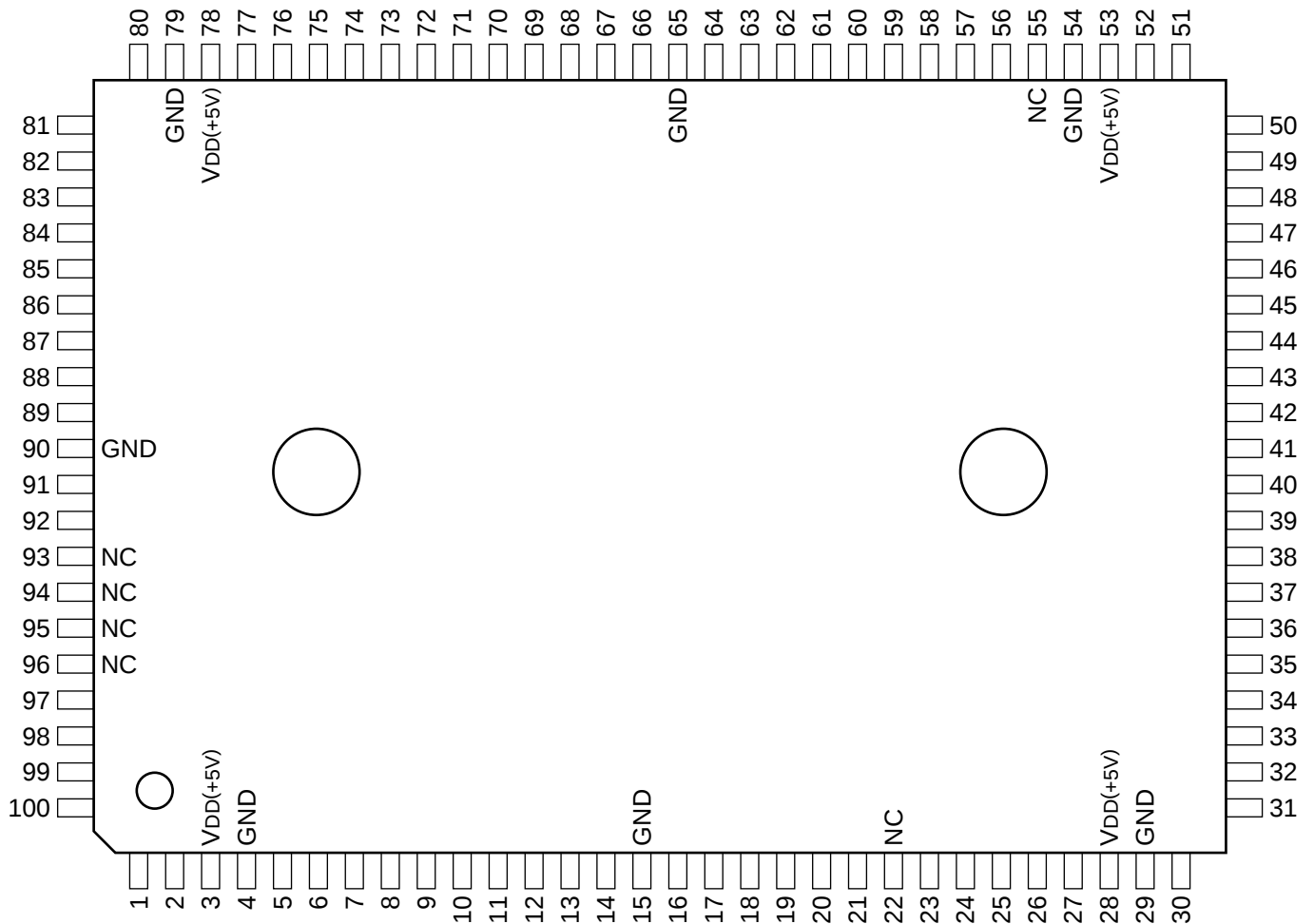

C-MOS ANTI-ALIAS FILTER & DEFOCUS FILTER

-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	CI5	21	I	OE	41	O	FO4	61	I	MFM3	81	I	DEN6
2	I	CI4	22	—	NC	42	O	FO3	62	I	MFM2	82	I	DEN5
3	—	VDD	23	I	FI9	43	O	FO2	63	I	MFM1	83	I	DEN4
4	—	GND	24	I	FI8	44	O	FO1	64	I	RST	84	I	DEN3
5	I	CI3	25	I	FI7	45	O	FO0	65	—	GND	85	I	DEN2
6	I	CI2	26	I	FI6	46	I	CKX	66	I	MI0	86	I	DEN1
7	I	CI1	27	I	FI5	47	I	CKD	67	I	MI1	87	I	DEN0
8	I	CI0	28	—	VDD	48	I/O	DIO	68	I	VI0	88	I	CK3
9	I	SMP	29	—	GND	49	I	ADR	69	I	VI1	89	I	CK2
10	O	VO9	30	I	FI4	50	I	CS	70	I	VI2	90	—	GND
11	O	VO8	31	I	FI3	51	I	TEST	71	I	VI3	91	I	CK1
12	O	VO7	32	I	FI2	52	I	AREA	72	I	VI4	92	I	EDGE
13	O	VO6	33	I	FI1	53	—	VDD	73	I	VI5	93	—	NC
14	O	VO5	34	I	FI0	54	—	GND	74	I	VI6	94	—	NC
15	—	GND	35	O	FO9	55	—	NC	75	I	VI7	95	—	NC
16	O	VO4	36	O	FO8	56	I	MFM8	76	I	VI8	96	—	NC
17	O	VO3	37	O	FO7	57	I	MFM7	77	I	VI9	97	I	CI9
18	O	VO2	38	O	FO6	58	I	MFM6	78	—	VDD	98	I	CI8
19	O	VO1	39	O	FO5	59	I	MFM5	79	—	GND	99	I	CI7
20	O	VO0	40	—	GND	60	I	MFM4	80	I	DEN7	100	I	CI6

		9	21	49	51	52	64	92			
77	VI9	SMP	OE	ADR	TEST	AREA	RST	EDGE	VO9	10	
76	VI8								VO8	11	
75	VI7								VO7	12	
74	VI6								VO6	13	
73	VI5								VO5	14	
72	VI4								VO4	16	
71	VI3								VO3	17	
70	VI2								VO2	18	
69	VI1								VO1	19	
68	VI0								VO0	20	
23	FI9								FO9	35	
24	FI8								FO8	36	
25	FI7								FO7	37	
26	FI6								FO6	38	
27	FI5								FO5	39	
30	FI4								FO4	41	
31	FI3								FO3	42	
32	FI2								FO2	43	
33	FI1								FO1	44	
34	FI0								FO0	45	
97	CI9								DIO	48	
98	CI8										
99	CI7										
100	CI6										
1	CI5										
2	CI4										
5	CI3										
6	CI2										
7	CI1										
8	CI0										
80	DEN7										
81	DEN6										
82	DEN5										
83	DEN4										
84	DEN3										
85	DEN2										
86	DEN1										
87	DEN0										
56	MFM8										
57	MFM7										
58	MFM6										
59	MFM5										
60	MFM4										
61	MFM3										
62	MFM2										
63	MFM1										
		MI0	MI1	CK1	CK2	CK3	CKX	CKD	CS		
		66	67	91	89	88	46	47	50		

INPUT

ADR	; SERIAL ADDRESS
AREA	; DEFOCUS-FILTER AREA
CI0 - CI9	; COMPENSATER RATE IN
CK1 - CK3	; SYSTEM CLOCK
CKD	; SERIAL CLOCK
CKX	; SERIAL SET TIMING
CS	; SERIAL TIMING PULSE
DEN0 - DEN7	; DEFOCUS DENOMINATOR
EDGE	; ANTI-FILTER AREA
FI0 - FI9	; RECURSIVE VIDEO IN
MFM1 - MFM8	; FILTER DEPTH IN
MI0, MI1	; FILTER DEPTH IN
OE	; OUTPUT ENABLE
RST	; RESET
SMP	; SAMPLING PULSE
TEST	; TEST
VI0 - VI9	; VIDEO INPUT

OUTPUT

FO0 - FO9	; FILTER VIDEO OUT
VO0 - VO9	; VIDEO OUT

INPUT/OUTPUT

DIO	; SERIAL DATA I/O
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