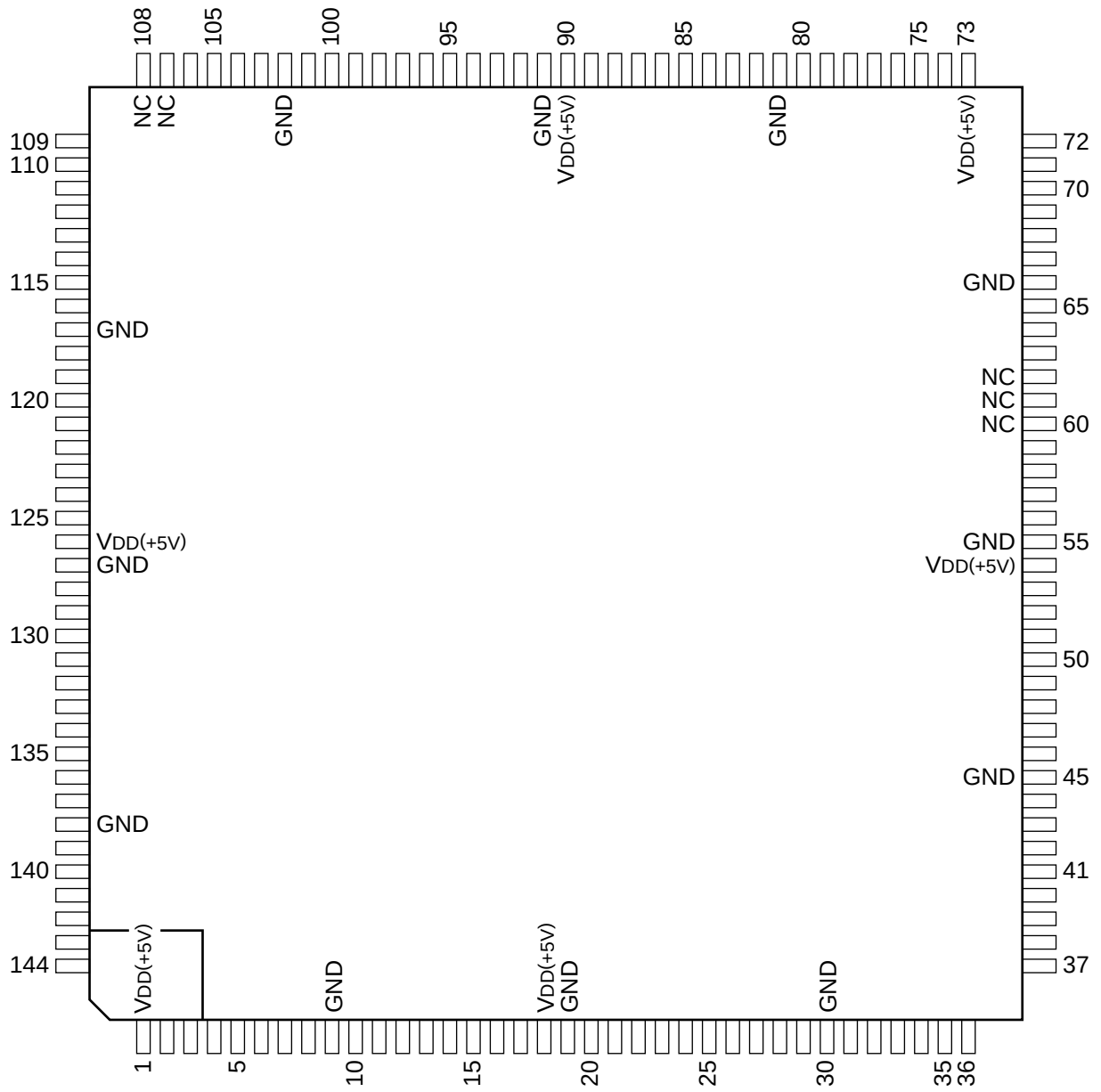

C-MOS 2 X 2 INTERPOLATOR
-TOP VIEW-



(VDD = +5V)

		20	63	64	72	92	93	94			
		H1	SW	SAMPUL	ADR	TEST	FLOE	SMP			
122	A9								D9	37	
123	A8								D8	38	
124	A7								D7	39	
125	A6								D6	40	
128	A5								D5	41	
129	A4								D4	42	
130	A3								D3	43	
131	A2								D2	44	
132	A1								D1	46	
133	A0								D0	47	
134	B9								E9	2	
135	B8								E8	3	
136	B7								E7	4	
137	B6								E6	5	
139	B5								E5	6	
140	B4								E4	7	
141	B3								E3	8	
142	B2								E2	10	
143	B1								E1	11	
144	B0								E0	12	
48	C9								F9	26	
49	C8								F8	27	
50	C7								F7	28	
51	C6								F6	29	
52	C5								F5	31	
53	C4								F4	32	
56	C3								F3	33	
57	C2								F2	34	
58	C1								F1	35	
59	C0								F0	36	
85	BG9								IP9	106	
84	BG8								IP8	105	
83	BG7								IP7	104	
82	BG6								IP6	103	
79	BG5								IP5	101	
78	BG4								IP4	100	
77	BG3								IP3	99	
76	BG2								IP2	98	
75	BG1								IP1	97	
74	BG0								IP0	96	
17	KE4								HM5	114	
16	KE3								HM4	113	
15	KE2								HM3	112	
14	KE1								HM2	111	
13	KE0								HM1	110	
25	KF4								HM0	109	
24	KF3										
23	KF2								VM5	121	
22	KF1								VM4	120	
21	KF0								VM3	119	
89	PA4								VM2	118	
88	PA3								VM1	116	
87	PA2								VM0	115	
86	PA1										
67	CK1										
65	CK3										
68	CKX										
69	CS										
70	CKD										
95	OE										
71	DIO										
80	RST										

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	37	I	D9	73	—	VDD	109	I	HM0
2	I	E9	38	I	D8	74	I	BG0	110	I	HM1
3	I	E8	39	I	D7	75	I	BG1	111	I	HM2
4	I	E7	40	I	D6	76	I	BG2	112	I	HM3
5	I	E6	41	I	D5	77	I	BG3	113	I	HM4
6	I	E5	42	I	D4	78	I	BG4	114	I	HM5
7	I	E4	43	I	D3	79	I	BG5	115	I	VM0
8	I	E3	44	I	D2	80	I	RST	116	I	VM1
9	—	GND	45	—	GND	81	—	GND	117	—	GND
10	I	E2	46	I	D1	82	I	BG6	118	I	VM2
11	I	E1	47	I	D0	83	I	BG7	119	I	VM3
12	I	E0	48	I	C9	84	I	BG8	120	I	VM4
13	I	KE0	49	I	C8	85	I	BG9	121	I	VM5
14	I	KE1	50	I	C7	86	I	PA1	122	I	A9
15	I	KE2	51	I	C6	87	I	PA2	123	I	A8
16	I	KE3	52	I	C5	88	I	PA3	124	I	A7
17	I	KE4	53	I	C4	89	I	PA4	125	I	A6
18	—	VDD	54	—	VDD	90	—	VDD	126	—	VDD
19	—	GND	55	—	GND	91	—	GND	127	—	GND
20	I	H1	56	I	C3	92	I	TEST	128	I	A5
21	I	KF0	57	I	C2	93	I	FLOE	129	I	A4
22	I	KF1	58	I	C1	94	I	SMP	130	I	A3
23	I	KF2	59	I	C0	95	I	OE	131	I	A2
24	I	KF3	60	—	NC	96	O	IP0	132	I	A1
25	I	KF4	61	—	NC	97	O	IP1	133	I	A0
26	I	F9	62	—	NC	98	O	IP2	134	I	B9
27	I	F8	63	I	SW	99	O	IP3	135	I	B8
28	I	F7	64	I	SAMPUL	100	O	IP4	136	I	B7
29	I	F6	65	I	CK3	101	O	IP5	137	I	B6
30	—	GND	66	—	GND	102	—	GND	138	—	GND
31	I	F5	67	I	CK1	103	O	IP6	139	I	B5
32	I	F4	68	I	CKX	104	O	IP7	140	I	B4
33	I	F3	69	I	CS	105	O	IP8	141	I	B3
34	I	F2	70	I	CKD	106	O	IP9	142	I	B2
35	I	F1	71	I/O	DIO	107	—	NC	143	I	B1
36	I	F0	72	I	ADR	108	—	NC	144	I	B0

INPUT

A0-A9 ; VIDEO DATA (A)
 ADR ; SERIAL ADDRESS
 B0-B9 ; VIDEO DATA (B)
 BG0-BG9 ; BACK GROUND VIDEO
 C0-C9 ; VIDEO DATA (C)
 CK1 ; 27.0 MHz/28.6 MHz
 CK3 ; 6.75 MHz/7.15 MHz
 CKD ; SERIAL CLOCK
 CKX ; SERIAL SET TIMING
 CS ; SERIAL TIMING PULSE
 D0-D9 ; VIDEO DATA (D)
 E0-E9 ; VIDEO DATA (E)
 F0-F9 ; VIDEO DATA (F)
 FLOE ; FIELD ODD/EVEN
 H1 ; H-ADDRESS
 HM0-HM5 ; H-ADDRESS
 KE0- KE4 ; MOTION-IN (A-B SIDE)
 KF0-KF4 ; MOTION-IN (C-D SIDE)
 OE ; OUTPUT ENABLE
 PA1-PA4 ; PARALLEL ADDRESS
 RST ; RESET
 SAMPUL ; INPUT SAMPLING
 SMP ; SAMPLING PULSE
 SW ; ACTIVE VIDEO AREA
 TEST ; TEST
 VM0-VM5 ; V-ADDRESS

OUTPUT

IP0-IP9 ; VIDEO OUT

INPUT/OUTPUT

DIO ; SERIAL DATA I/O

