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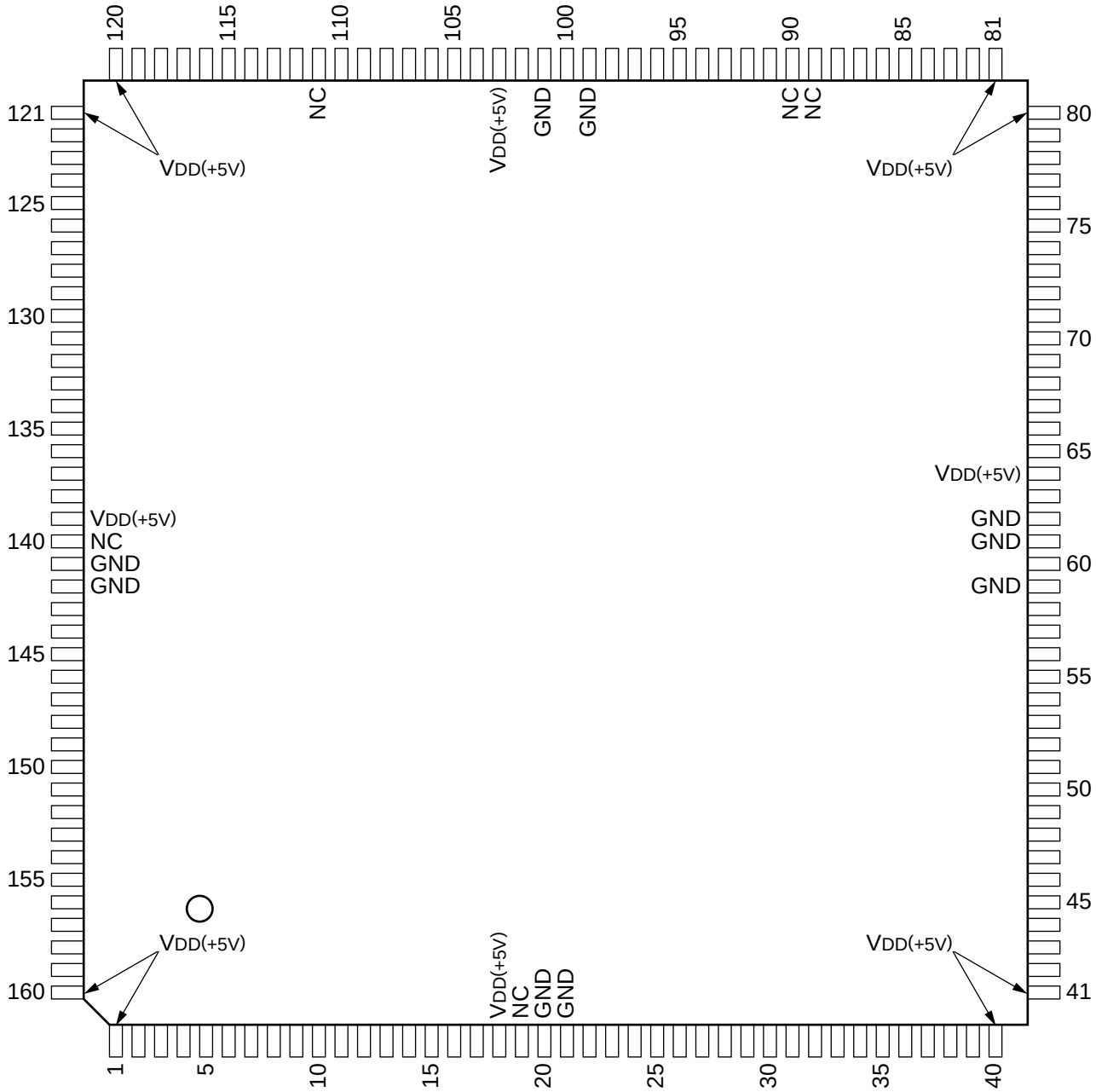
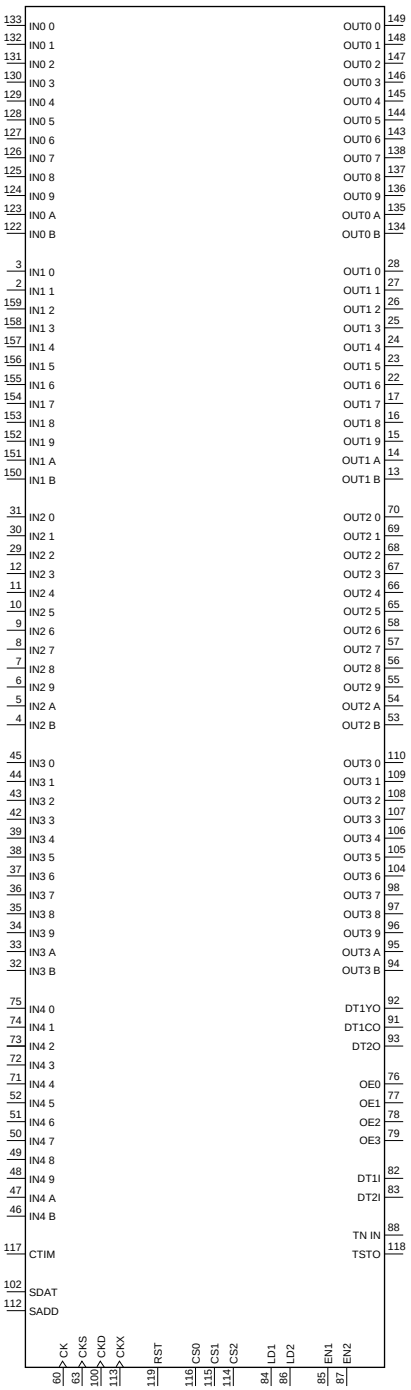
C-MOS VIDEO MODIFIER

-TOP VIEW-





(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	41	—	VDD	81	—	VDD	121	—	VDD
2	I	IN1 1	42	I	IN3 3	82	I	DT1I	122	I	IN0 B
3	I	IN1 0	43	I	IN3 2	83	I	DT2I	123	I	IN0 A
4	I	IN2 B	44	I	IN3 1	84	I	LD1	124	I	IN0 9
5	I	IN2 A	45	I	IN3 0	85	I	EN1	125	I	IN0 8
6	I	IN2 9	46	I	IN4 B	86	I	LD2	126	I	IN0 7
7	I	IN2 8	47	I	IN4 A	87	I	EN2	127	I	IN0 6
8	I	IN2 7	48	I	IN4 9	88	I	TN IN	128	I	IN0 5
9	I	IN2 6	49	I	IN4 8	89	—	NC	129	I	IN0 4
10	I	IN2 5	50	I	IN4 7	90	—	NC	130	I	IN0 3
11	I	IN2 4	51	I	IN4 6	91	O	DT1CO	131	I	IN0 2
12	I	IN2 3	52	I	IN4 5	92	O	DT1YO	132	I	IN0 1
13	O	OUT1 B	53	O	OUT2 B	93	O	DT2O	133	I	IN0 0
14	O	OUT1 A	54	O	OUT2 A	94	O	OUT3 B	134	O	OUT0 B
15	O	OUT1 9	55	O	OUT2 9	95	O	OUT3 A	135	O	OUT0 A
16	O	OUT1 8	56	O	OUT2 8	96	O	OUT3 9	136	O	OUT0 9
17	O	OUT1 7	57	O	OUT2 7	97	O	OUT3 8	137	O	OUT0 8
18	—	VDD	58	O	OUT2 6	98	O	OUT3 7	138	O	OUT0 7
19	—	NC	59	—	GND	99	—	GND	139	—	VDD
20	—	GND	60	I	CK	100	I	CKD	140	—	NC
21	—	GND	61	—	GND	101	—	GND	141	—	GND
22	O	OUT1 6	62	—	GND	102	I/O	SDAT	142	—	GND
23	O	OUT1 5	63	I	CKS	103	—	VDD	143	O	OUT0 6
24	O	OUT1 4	64	—	VDD	104	O	OUT3 6	144	O	OUT0 5
25	O	OUT1 3	65	O	OUT2 5	105	O	OUT3 5	145	O	OUT0 4
26	O	OUT1 2	66	O	OUT2 4	106	O	OUT3 4	146	O	OUT0 3
27	O	OUT1 1	67	O	OUT2 3	107	O	OUT3 3	147	O	OUT0 2
28	O	OUT1 0	68	O	OUT2 2	108	O	OUT3 2	148	O	OUT0 1
29	I	IN2 2	69	O	OUT2 1	109	O	OUT3 1	149	O	OUT0 0
30	I	IN2 1	70	O	OUT2 0	110	O	OUT3 0	150	I	IN1 B
31	I	IN2 0	71	I	IN4 4	111	—	NC	151	I	IN1 A
32	I	IN3 B	72	I	IN4 3	112	I	SADD	152	I	IN1 9
33	I	IN3 A	73	I	IN4 2	113	I	CKX	153	I	IN1 8
34	I	IN3 9	74	I	IN4 1	114	I	CS2	154	I	IN1 7
35	I	IN3 8	75	I	IN4 0	115	I	CS1	155	I	IN1 6
36	I	IN3 7	76	I	OE0	116	I	CS0	156	I	IN1 5
37	I	IN3 6	77	I	OE1	117	I	CTIM	157	I	IN1 4
38	I	IN3 5	78	I	OE2	118	O	TSTO	158	I	IN1 3
39	I	IN3 4	79	I	OE3	119	I	RST	159	I	IN1 2
40	—	VDD	80	—	VDD	120	—	VDD	160	—	VDD

INPUT

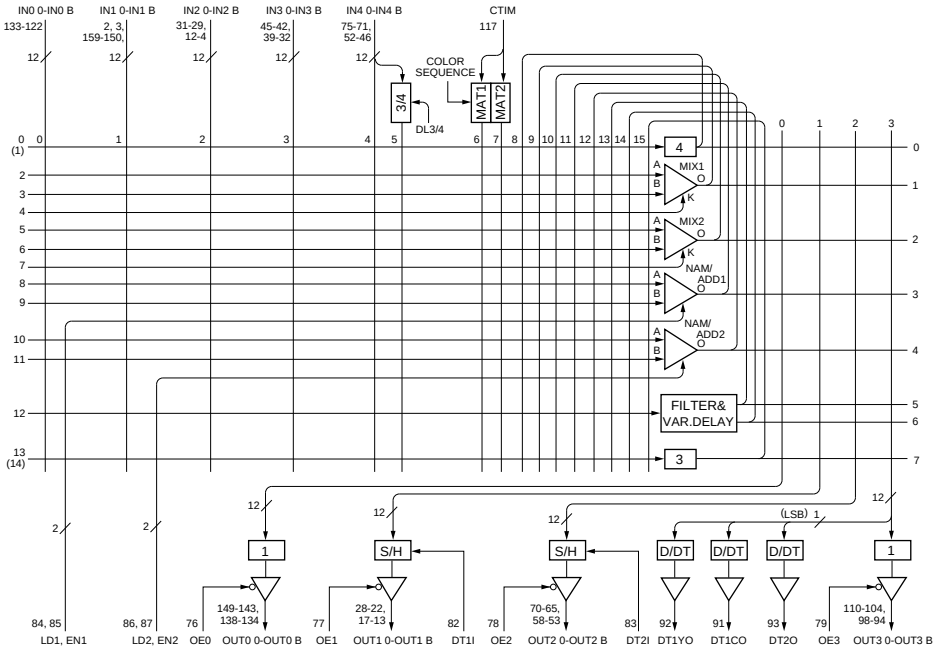
CK ; SYSTEM CLOCK
CKD ; CLOCK FOR CS0, CS1, CS2, SADD, SDAT
CKS ; CLOCK FOR SAMPLE AND HOLD CIRCUIT IN OUT1
CKX ; TIMING SIGNAL FOR SERIAL CONTROL EXECUTION
CS0-CS2 ; CHIP SELECT
CTIM ; COLOR TIMING SPECIFIED
DT1I, DT2I ; SAMPLE PULSE FOR SAMPLE AND HOLD CIRCUIT IN OUT1, OUT2
EN1, EN2 ; NAMADD1, NAMADD2 CIRCUIT ENABLE (IN COUNTER MODE)
IN0 0-IN0 B, IN1 0-IN1 B, IN2 0-IN2 B, IN3 0-IN3 B, IN4 0-IN4 B
; DATA
LD1, LD2 ; NAMADD1, NAMADD2 CIRCUIT LOAD PIN (IN COUNTER MODE)
OE0-OE3 ; OUTPUT ENABLE FOR OUT0, OUT1, OUT2, OUT3
SADD ; SERIAL ADDRESS
RST ; POWER RESET
TN IN ; TEST TERMINAL

OUTPUT

DT1YO, DT1CO, DT2O
; SAMPLE PULSE
OUT0 0-OUTO B, OUT1 0-OUT1 B, OUT2 0-OUT2 B, OUT3 0-OUT3 B
; DATA
TSTO ; TEST TERMINAL

INPUT/OUTPUT

SDAT ; SERIAL DATA


IN0 0-IN0 B
IN1 0-IN1 B
IN2 0-IN2 B
IN3 0-IN3 B
IN4 0-IN4 B
CTIM
133-122
2, 3, 159-150,
31-29, 12-4
45-42, 39-32
75-71, 52-46
117
COLOR SEQUENCE
3/4
DL3/4
MAT1
MAT2
4
MIX1
MIX2
NAM/ ADD1
NAM/ ADD2
FILTER& VAR.DELAY
3
1
S/H
S/H
D/DT
D/DT
D/DT
1
(LSB)
149-143, 138-134
28-22, 17-13
70-65, 58-53
110-104, 98-94
84, 85
LD1, EN1
86, 87
LD2, EN2
76
OE0
OUT0 0-OUT0 B
77
OE1
OUT1 0-OUT1 B
82
DT1I
78
OE2
OUT2 0-OUT2 B
83
DT2I
92
DT1YO
91
DT1CO
93
DT2O
79
OE3
OUT3 0-OUT3 B