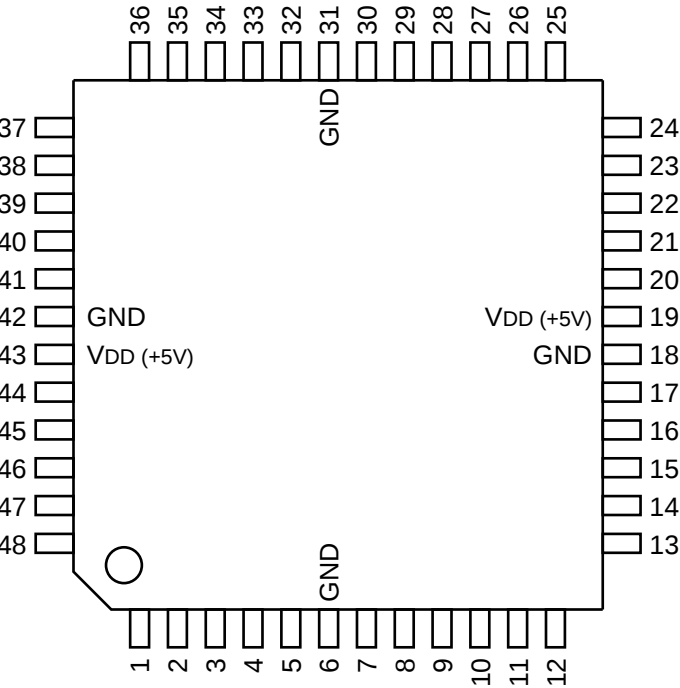
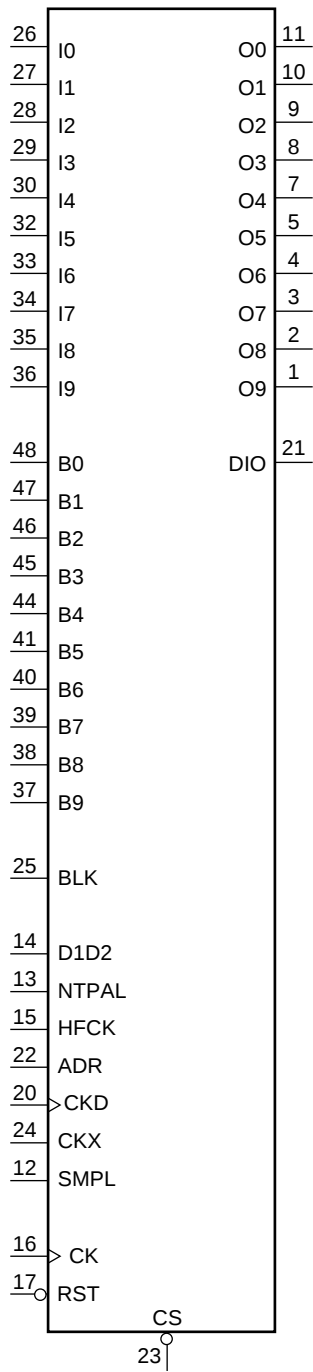

C-MOS OUTPUT PROCESSOR

— TOP VIEW —



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	O9	13	I	NTPAL	25	I	BLK	37	I	B9
2	O	O8	14	I	D1D2	26	I	I0	38	I	B8
3	O	O7	15	I	HFCK	27	I	I1	39	I	B7
4	O	O6	16	I	CK	28	I	I2	40	I	B6
5	O	O5	17	I	RST	29	I	I3	41	I	B5
6	—	GND	18	—	GND	30	I	I4	42	—	GND
7	O	O4	19	—	VDD	31	—	GND	43	—	VDD
8	O	O3	20	I	CKD	32	I	I5	44	I	B4
9	O	O2	21	I/O	DIO	33	I	I6	45	I	B3
10	O	O1	22	I	ADR	34	I	I7	46	I	B2
11	O	O0	23	I	CS	35	I	I8	47	I	B1
12	I	SMPL	24	I	CKX	36	I	I9	48	I	B0



INPUT
ADR ; SERIAL ADDRESS
B0 - B9 ; 10 BIT DIGITAL BLANKING IN
BLK ; BLANKING GATE PULSE
CK ; SYSTEM CLOCK
CKD ; SERIAL INTERFACE CLOCK
CKX ; SWITCHING TIMING PULSE
CS ; CHIP SELECT (LOW : ACTIVE)
D1D2 ; D1/D2 FORMAT SELECT
(LOW : D1, HIGH : D2)
HFCK ; HALF CLOCK (13.5MHz) (ONLY D1 MODE)
I0 - I9 ; 10 BIT DIGITAL VIDEO IN (D1 OR D2 FORMAT)
NTPAL ; NTSC/PAL FORMAT SELECT (ONLY D2 MODE)
(LOW : D2 NTSC, HIGH : D2 PAL)
RST ; RESET PULSE (LOW : RESET)
SMPL ; SAMPLING PULSE FOR O OUTPUT (f)

OUTPUT
O0 - O9 ; 10 BIT DIGITAL VIDEO OUT

INPUT/OUTPUT
DIO ; SERIAL DATA

