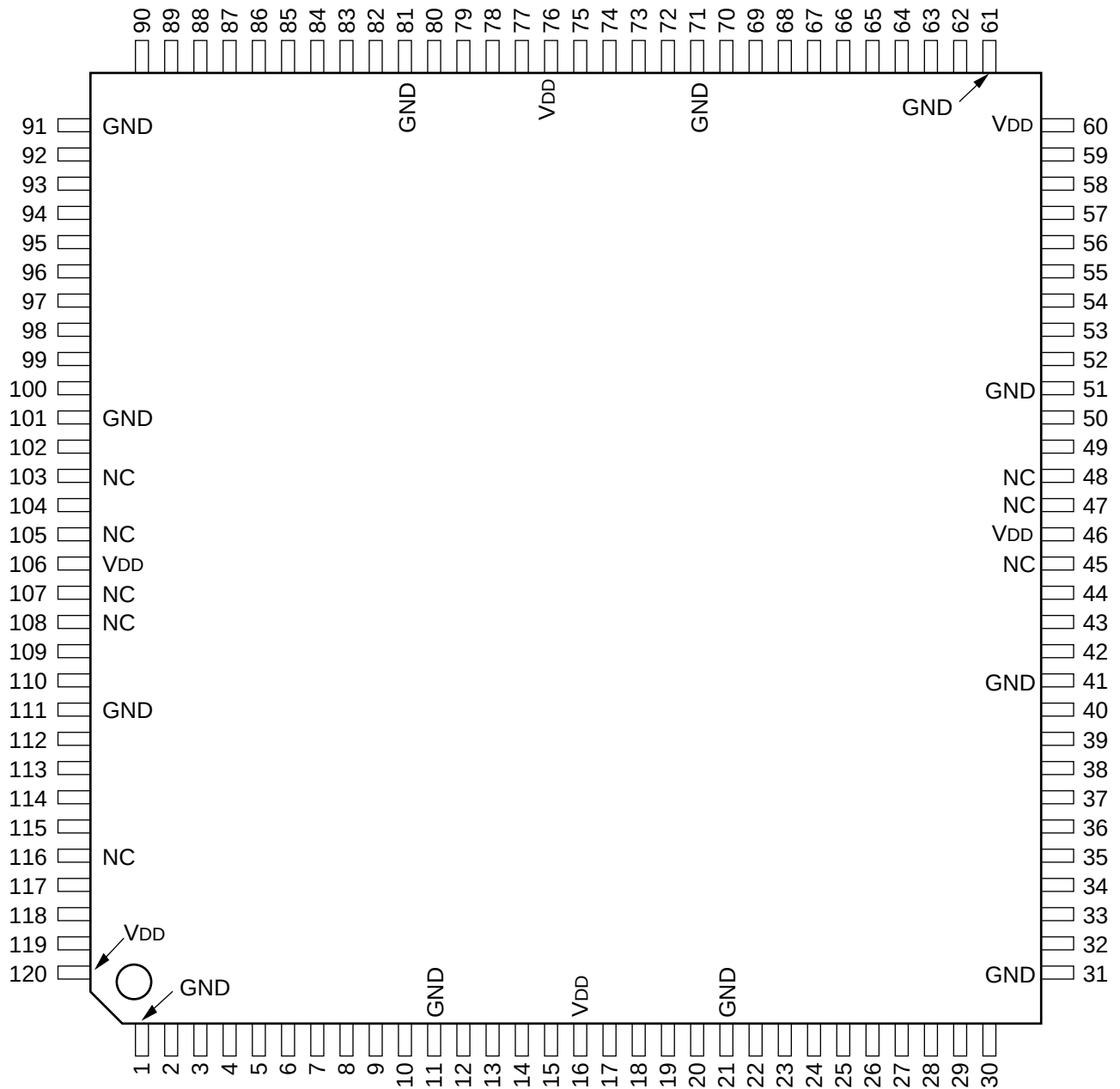


C-MOS DIGITAL AUDIO TIME BASE EXPANSION FOR SIF RECEIVER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	DESCRIPTION	PIN NO.	I/O	SIGNAL	DESCRIPTION
1	—	GND		61	—	GND	
2	I	XMM	TEST	62	I	OECRD	OUTPUT ENABLE FOR PIN 52 - 59
3	I	CLR	POWER ON CLEAR	63	I	CRA0	BUS READ ADDRESS 0
4	O	HIGH	TEST (ALWAYS H)	64	I	CRA1	BUS READ ADDRESS 1
5	O	RERR	TEST (R-ERROR)	65	I	CRA2	BUS READ ADDRESS 2
6	O	MON4	TEST (DELAY CH-4)	66	I	CRA3	BUS READ ADDRESS 3
7	O	MON3	TEST (DELAY CH-3)	67	I	CRA4	BUS READ ADDRESS 4
8	O	MON2	TEST (DELAY CH-2)	68	I	CRA5	BUS READ ADDRESS 5
9	O	MON1	TEST (DELAY CH-1)	69	I	DRA6	BUS READ ADDRESS 6
10	I	OEMON	OUTPUT ENABLE FOR PIN 4 - 9	70	I	CREN	BUS ADDRESS LATCH ENABLE
11	—	GND		71	—	GND	
12	I	ITA	TEST	72	O	ERROR1	CH-1 ERROR FLAG
13	I	ITB	TEST	73	O	ERROR2	CH-2 ERROR FLAG
14	I	ITC	TEST	74	O	ERROR3	CH-3 ERROR FLAG
15	I	ITD	TEST	75	O	ERROR4	CH-4 ERROR FLAG
16	—	VDD		76	—	VDD	
17	I	INTP4	CH-4 AUDIO INTERPOLATE	77	O	EXIST1	CH-1 EXIST
18	I	INTP3	CH-3 AUDIO INTERPOLATE	78	O	EXIST2	CH-2 EXIST
19	I	INTP2	CH-2 AUDIO INTERPOLATE	79	O	EXIST3	CH-3 EXIST
20	I	INTP1	CH-1 AUDIO INTERPOLATE	80	O	EXIST4	CH-4 EXIST
21	—	GND		81	—	GND	
22	I	CHEX1	CHANNEL EXCHANGE 1	82	O	DIDPER	DID PARITY ERROR
23	I	CHEX0	CHANNEL EXCHANGE 0	83	O	DIDERR	DID UNMATCH ERROR
24	I	AFS	AUDIO FORMAT SELECT	84	O	DBNPER	DBN PARITY ERROR
25	I	LRS	AUDIO LEFT/RIGHT BIT SELECT	85	O	DBNERR	DBN UNCONTINUITY ERROR
26	I	DIR	AUDIO LSB/MSB FIRST SELECT	86	O	DCPER	DC PARITY ERROR
27	I	20/16	AUDIO 20-BIT/16-BIT SELECT	87	O	DCERR	DC UNMATCH ERROR
28	O	WERR	TEST (W-ERROR)	88	O	UDPER	UD PARITY ERROR
29	O	Z3/4	CH-3/4 BLOCK SYNC (Z-FLAG)	89	O	CSERR	CHECKSUM ERROR
30	O	Z1/2	CH-1/2 BLOCK SYNC (Z-FLAG)	90	I	OEERR	OUTPUT ENABLE FOR PIN 82 - 89
31	—	GND		91	—	GND	
32	O	C3/4	CH-3/4 CHANNEL STATUS BIT	92	I	VIDEO0	VIDEO DATA 0
33	O	C1/2	CH-1/2 CHANNEL STATUS BIT	93	I	VIDEO1	VIDEO DATA 1
34	O	U3/4	CH-3/4 USER BIT	94	I	VIDEO2	VIDEO DATA 2
35	O	U1/2	CH-1/2 USER BIT	95	I	VIDEO3	VIDEO DATA 3
36	O	V3/4	CH-3/4 VALIDITY FLAG	96	I	VIDEO4	VIDEO DATA 4
37	O	V1/2	CH-1/2 VALIDITY FLAG	97	I	VIDEO5	VIDEO DATA 5
38	O	AD3/4	CH-3/4 AUDIO DATA	98	I	VIDEO6	VIDEO DATA 6
39	O	AD1/2	CH-1/2 AUDIO DATA	99	I	VIDEO7	VIDEO DATA 7
40	I	OEAD	OUTPUT ENABLE FOR PIN 29 - 39	100	I	VIDEO8	VIDEO DATA 8
41	—	GND		101	—	GND	
42	I	BCK	AUDIO BIT CLOCK	102	I	VIDEO9	VIDEO DATA 9
43	I	LRCK	AUDIO L/R CLOCK	103	—	NC	
44	O	CMYW	TEST	104	I	525/625	525/625 SELECT
45	—	NC		105	—	NC	
46	—	VDD		106	—	VDD	
47	—	NC		107	—	NC	
48	—	NC		108	—	NC	
49	I	TEST	TEST	109	—	NC	
50	I	CRCK	BUS ADDRESS LATCH CLOCK	110	I	VCK	VIDEO CLOCK
51	—	GND		111	—	GND	
52	O	CRD7	BUS READ DATA 7	112	I	D2/D1	D2/D1 SELECT
53	O	CRD6	BUS READ DATA 6	113	I	DID1	DID BIT1
54	O	CRD5	BUS READ DATA 5	114	I	DID2	DID BIT2
55	O	CRD4	BUS READ DATA 4	115	I	ERDB	ERROR OVERRIDE FOR DEBUG
56	O	CRD3	BUS READ DATA 3	116	—	NC	
57	O	CRD2	BUS READ DATA 2	117	O	EDHF	EDH FULL FIELD ERROR
58	O	CRD1	BUS READ DATA 1	118	O	EDHA	EDH ACTIVE PICTURE ERROR
59	O	CRD0	BUS READ DATA 0	119	O	OVFS	TEST
60	—	VDD		120	—	VDD	