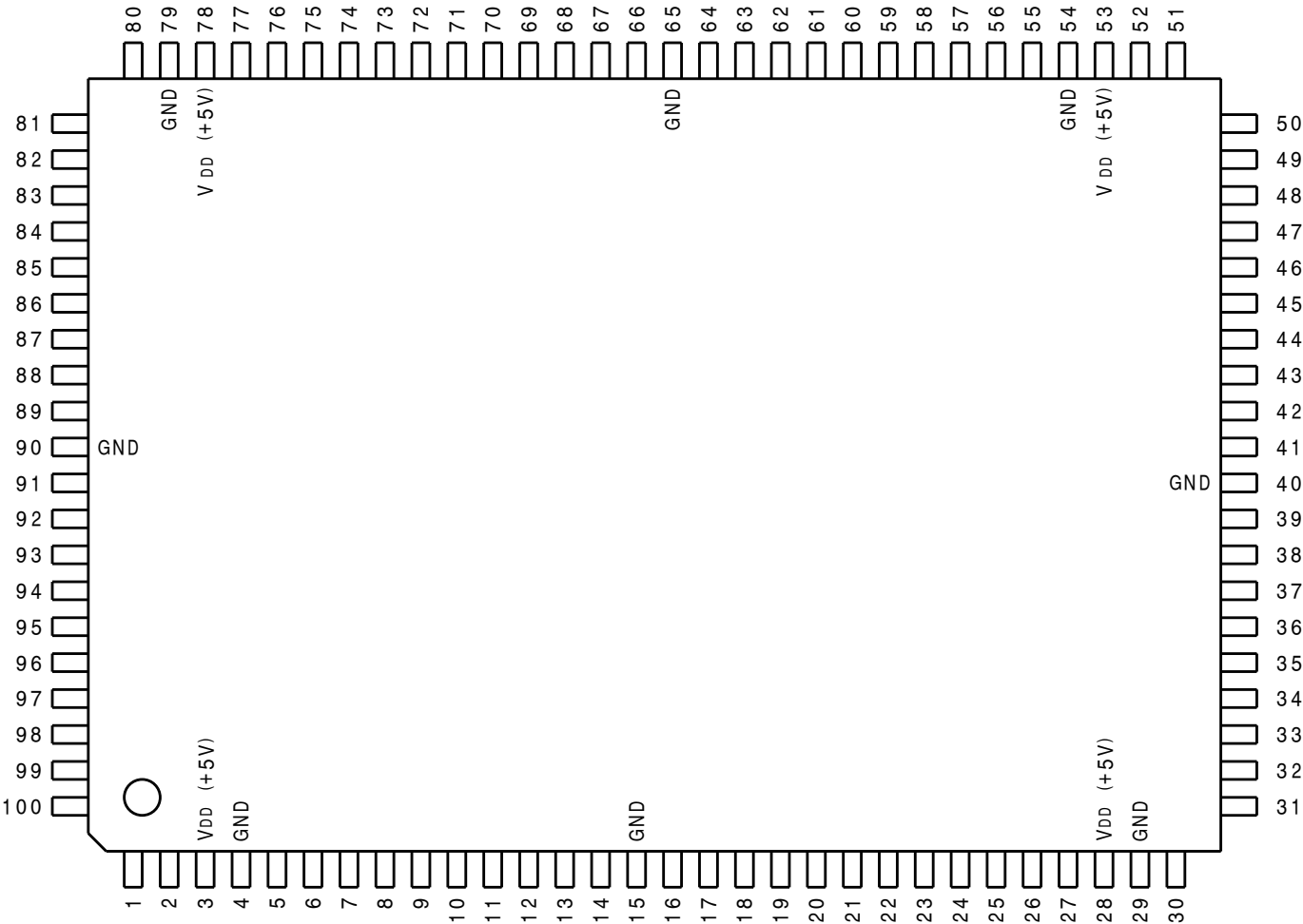
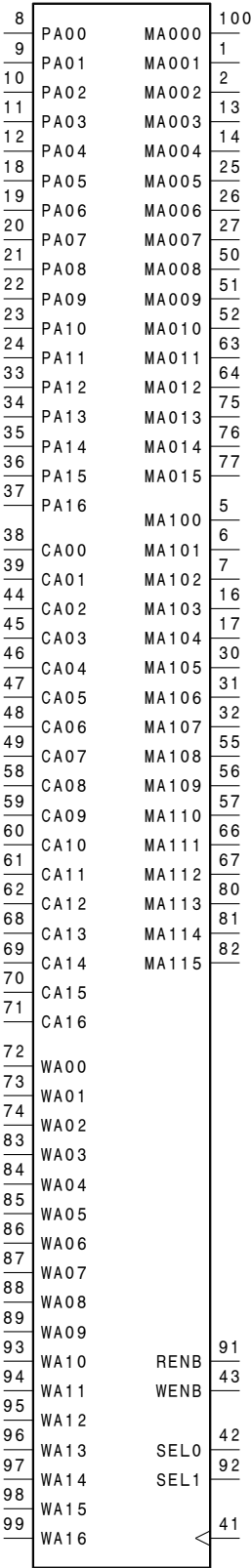

C-MOS MEMORY ADDRESS BUS CONTROL
-TOP VIEW-



(V_{DD} = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	MA001	26	O	MA006	51	O	MA009	76	O	MA014
2	O	MA002	27	O	MA007	52	O	MA010	77	O	MA015
3	—	V _{DD}	28	—	V _{DD}	53	—	V _{DD}	78	—	V _{DD}
4	—	GND	29	—	GND	54	—	GND	79	—	GND
5	O	MA100	30	O	MA105	55	O	MA108	80	O	MA113
6	O	MA101	31	O	MA106	56	O	MA109	81	O	MA114
7	O	MA102	32	O	MA107	57	O	MA110	82	O	MA115
8	I	PA00	33	I	PA12	58	I	CA08	83	I	WA03
9	I	PA01	34	I	PA13	59	I	CA09	84	I	WA04
10	I	PA02	35	I	PA14	60	I	CA10	85	I	WA05
11	I	PA03	36	I	PA15	61	I	CA11	86	I	WA06
12	I	PA04	37	I	PA16	62	I	CA12	87	I	WA07
13	O	MA003	38	I	CA00	63	O	MA011	88	I	WA08
14	O	MA004	39	I	CA01	64	O	MA012	89	I	WA09
15	—	GND	40	—	GND	65	—	GND	90	—	GND
16	O	MA103	41	I	CK	66	O	MA111	91	I	RENB
17	O	MA104	42	I	SEL0	67	O	MA112	92	I	SEL1
18	I	PA05	43	I	WENB	68	I	CA13	93	I	WA10
19	I	PA06	44	I	CA02	69	I	CA14	94	I	WA11
20	I	PA07	45	I	CA03	70	I	CA15	95	I	WA12
21	I	PA08	46	I	CA04	71	I	CA16	96	I	WA13
22	I	PA09	47	I	CA05	72	I	WA00	97	I	WA14
23	I	PA10	48	I	CA06	73	I	WA01	98	I	WA15
24	I	PA11	49	I	CA07	74	I	WA02	99	I	WA16
25	O	MA005	50	O	MA008	75	O	MA013	100	O	MA000



INPUT

CA00-CA16 ; READ ADDRESS FROM MEMORY
CK ; SYSTEM CLOCK
PA00-PA16 ; READ ADDRESS FROM MEMORY
RENB ; LATCH ENABLE FOR READ SYSTEM
SEL0 ; READ/WRITE CHANGE

	MA0	MA1
0	READ	WRITE
1	WRITE	READ

SEL1 ; READ ADDRESS SELECT
(0:PA MODE, 1:CA MODE)
WA00-WA16 ; WRITE ADDRESS TO MEMORY
WENB ; LATCH ENABLE FOR WRITE SYSTEM

OUTPUT

MA000-MA015 ; READ/WRITE ADDRESS
MA100-MA115 ; READ/WRITE ADDRESS

CONTROL		OUTPUT	
SEL0	SEL1	MA0	MA1
0	0	PA OUT	WA OUT
0	1	CA OUT	WA OUT
1	0	WA OUT	PA OUT
1	1	WA OUT	CA OUT