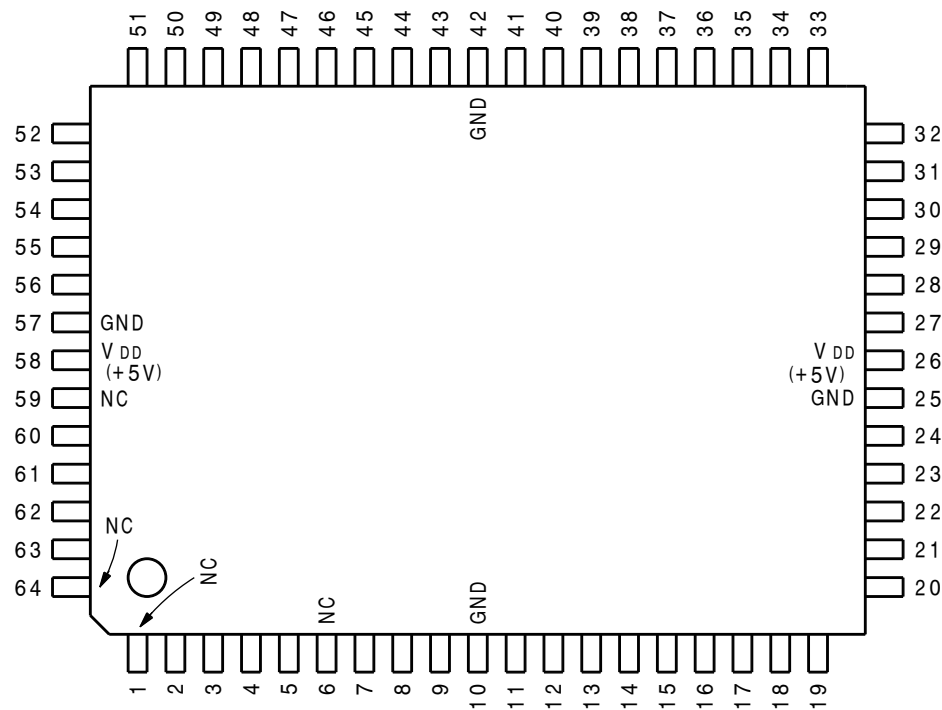
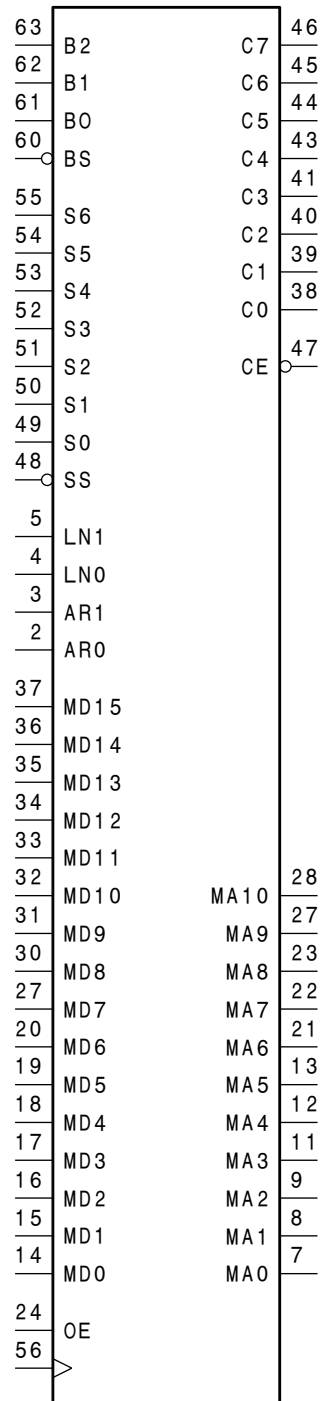

C-MOS CONTROLLED TO ADDRESS ARITHMETIC
-TOP VIEW-



(V_{DD}=+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	-	NC	17	I	MD3	33	I	MD11	49	I	S0
2	I	AR0	18	I	MD4	34	I	MD12	50	I	S1
3	I	AR1	19	I	MD5	35	I	MD13	51	I	S2
4	I	LN0	20	I	MD6	36	I	MD14	52	I	S3
5	I	LN1	21	O	MA6	37	I	MD15	53	I	S4
6	-	NC	22	O	MA7	38	O	C0	54	I	S5
7	O	MA0	23	O	MA8	39	O	C1	55	I	S6
8	O	MA1	24	I	OE	40	O	C2	56	I	CK
9	O	MA2	25	-	GND	41	O	C3	57	-	GND
10	-	GND	26	-	V _{DD}	42	-	GND	58	-	V _{DD}
11	O	MA3	27	O	MA9	43	O	C4	59	-	NC
12	O	MA4	28	O	MA10	44	O	C5	60	I	BS
13	O	MA5	29	I	MD7	45	O	C6	61	I	B0
14	I	MD0	30	I	MD8	46	O	C7	62	I	B1
15	I	MD1	31	I	MD9	47	O	CE	63	I	B2
16	I	MD2	32	I	MD10	48	I	SS	64	-	NC



INPUT

AR0, AR1, LN0, LN1;

ARITHMETIC AREA SIGNAL PORT

B0-B2 ; ADDRESS BANK REGISTER DATA PORT

BS ; ADDRESS BANK STROBE

CK ; CLOCK

MD0-MD15 ; MEMORY DATA PORT

OE ; OUTPUT ENABLE FOR MEMORY ADDRESS

S0-S6 ; START ADDRESS REGISTER

SS ; WRITE STROBE FOR START ADDRESS REGISTER

OUTPUT

C0-C7 ; CONTROL PORT FOR ADDRESS ARITHMETIC IC

CE ; CHIP ENABLE

MA0-MA10 ; MEMORY ADDRESS PORT