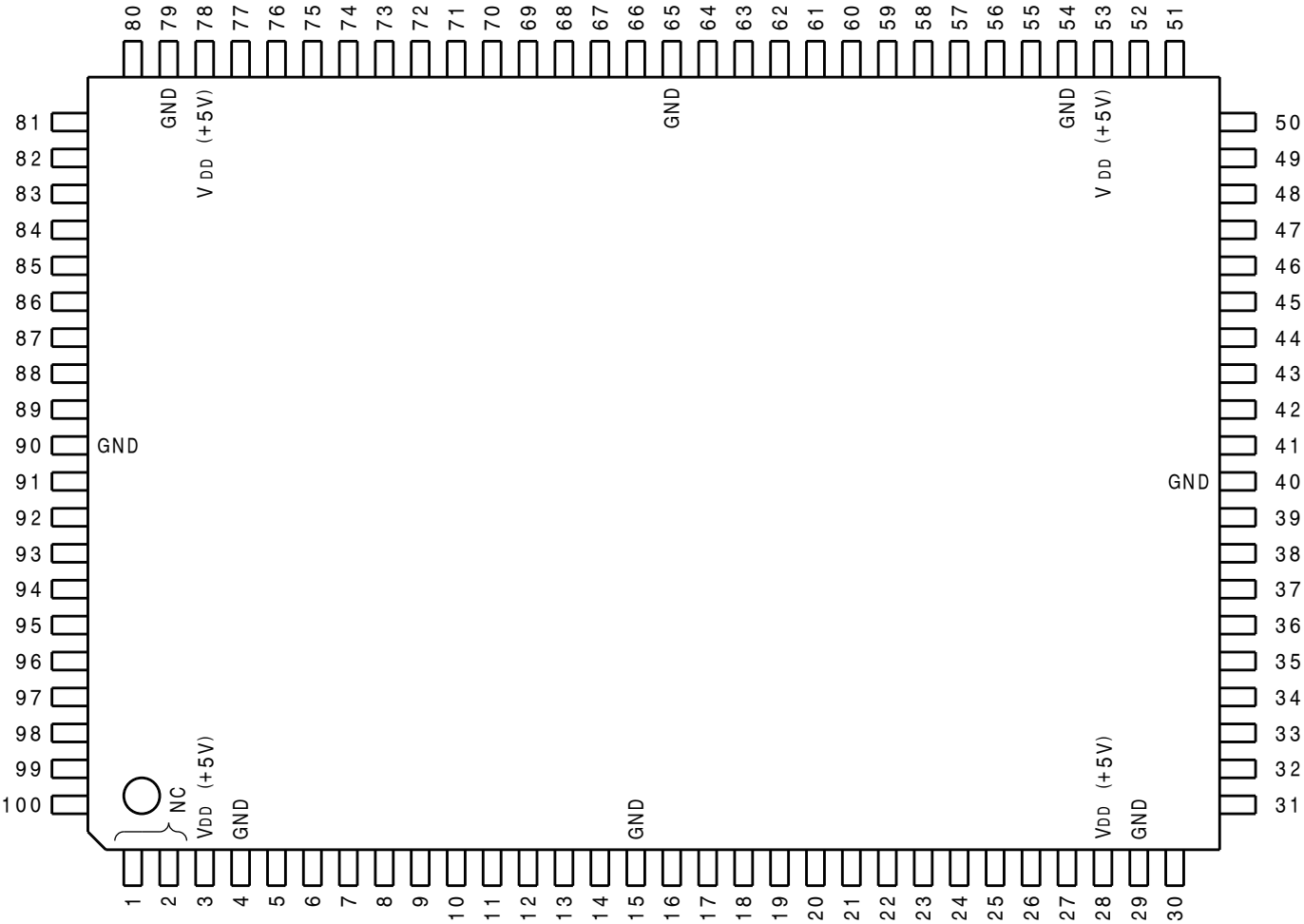

C-MOS VARIABLE LOW PASS FILTER
-TOP VIEW-



(V_{DD}=+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	–	NC	26	I	IE0	51	I	IC4	76	O	OB2
2	–	NC	27	I	IE1	52	I	IC5	77	O	OB3
3	–	V _{DD}	28	–	V _{DD}	53	–	V _{DD}	78	–	V _{DD}
4	–	GND	29	–	GND	54	–	GND	79	–	GND
5	I	IH4	30	I	IE2	55	I	IC6	80	O	OB4
6	I	IH5	31	I	IE3	56	I	IC7	81	O	OB5
7	I	IH6	32	I	IE4	57	I	IB0	82	O	OB6
8	I	IH7	33	I	IE5	58	I	IB1	83	O	OB7
9	I	IG0	34	I	IE6	59	I	IB2	84	I	SOB0
10	I	IG1	35	I	IE7	60	I	IB3	85	I	SOB1
11	I	IG2	36	I	ID0	61	I	IB4	86	O	OA0
12	I	IG3	37	I	ID1	62	I	IB5	87	O	OA1
13	I	IG4	38	I	ID2	63	I	IB6	88	O	OA2
14	I	IG5	39	I	ID3	64	I	IB7	89	O	OA3
15	–	GND	40	–	GND	65	–	GND	90	–	GND
16	I	IG6	41	I	CK	66	I	IA0	91	O	OA4
17	I	IG7	42	I	MODE	67	I	IA1	92	O	OA5
18	I	IF0	43	I	ID4	68	I	IA2	93	O	OA6
19	I	IF1	44	I	ID5	69	I	IA3	94	O	OA7
20	I	IF2	45	I	ID6	70	I	IA4	95	I	SOA0
21	I	IF3	46	I	ID7	71	I	IA5	96	I	SOA1
22	I	IF4	47	I	IC0	72	I	IA6	97	I	IH0
23	I	IF5	48	I	IC1	73	I	IA7	98	I	IH1
24	I	IF6	49	I	IC2	74	O	OB0	99	I	IH2
25	I	IF7	50	I	IC3	75	O	OB1	100	I	IH3

73	IA7	OA7	94	INPUT
72	IA6	OA6	93	
71	IA5	OA5	92	
70	IA4	OA4	91	
69	IA3	OA3	89	
68	IA2	OA2	88	
67	IA1	OA1	87	
66	IA0	OA0	86	
64	IB7	OB7	83	
63	IB6	OB6	82	
62	IB5	OB5	81	MODE ; MODE SELECT (0: COMPLEMENT 2 MODE, 1: INTEGER MODE) SOA0, SOA1 ; OA0-OA7 OUTPUT DATA SELECT SOB0, SOB1 ; OB0-OB7 OUTPUT DATA SELECT
61	IB4	OB4	80	
60	IB3	OB3	77	
59	IB2	OB2	76	
58	IB1	OB1	75	
57	IB0	OB0	74	
56	IC7			
55	IC6			
52	IC5			
51	IC4			
50	IC3			OUTPUT
49	IC2			
48	IC1			
47	IC0			
46	ID7	SOA1	96	
45	ID6	SOA0	95	
44	ID5		85	
43	ID4	SOB1	84	
39	ID3	SOB0		
38	ID2	MODE	42	
37	ID1		41	
36	ID0			
35	IE7	IG7	17	
34	IE6	IG6	16	
33	IE5	IG5	14	
32	IE4	IG4	13	
31	IE3	IG3	12	
30	IE2	IG2	11	
27	IE1	IG1	10	
26	IE0	IG0	9	
25	IF7	IH7	8	
24	IF6	IH6	7	
23	IF5	IH5	6	
22	IF4	IH4	5	
21	IF3	IH3	100	
20	IF2	IH2	99	
19	IF1	IH1	98	
18	IF0	IH0	97	