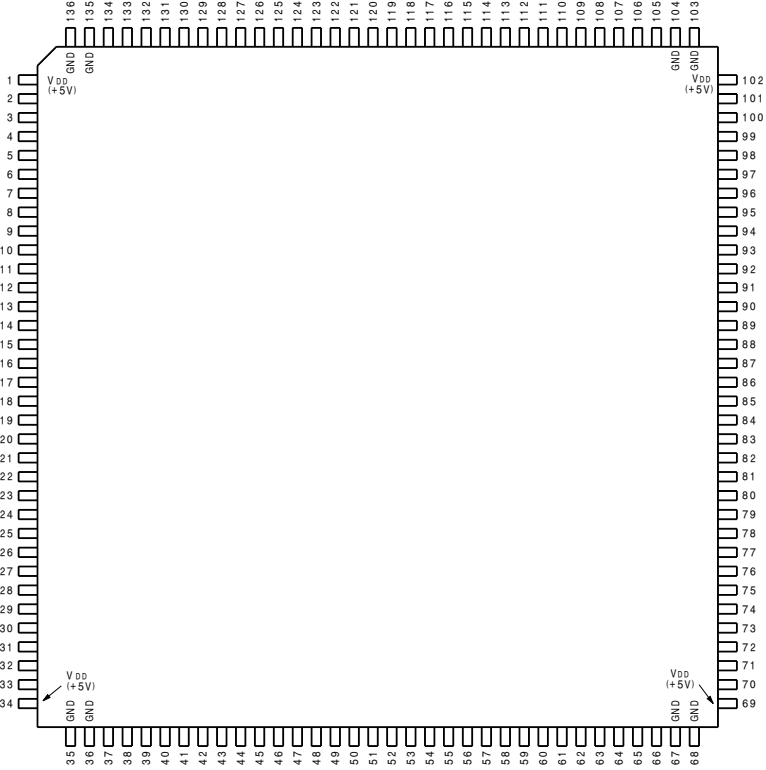
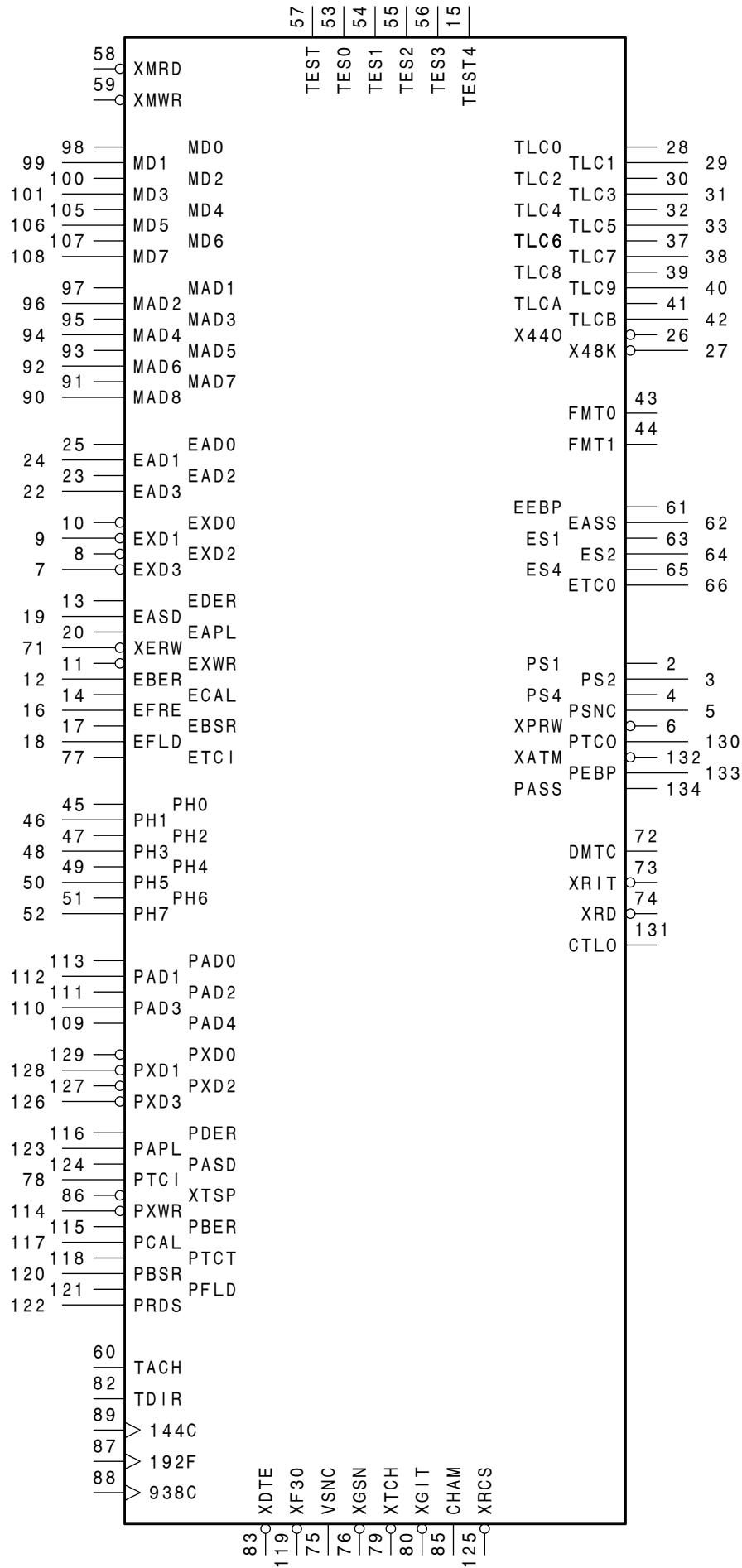

C-MOS TC(TIME CODE) READER
-TOP VIEW-



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	-	VDD	35	-	GND	69	-	VDD	103	-	GND
2	0	PS1	36	-	GND	70	0	ESNC	104	-	GND
3	0	PS2	37	0	TLC6	71	0	XERW	105	I/O	MD4
4	0	PS4	38	0	TLC7	72	0	DMTC	106	I/O	MD5
5	0	PSNC	39	0	TLC8	73	0	XRI T	107	I/O	MD6
6	0	XPRW	40	0	TLC9	74	0	XRD	108	I/O	MD7
7	I	EXD3	41	0	TLC A	75	I	VSNC	109	I	PAD4
8	I	EXD2	42	0	TLCB	76	I	XGSN	110	I	PAD3
9	I	EXD1	43	0	FMT0	77	I	ETC I	111	I	PAD2
10	I	EXD0	44	0	FMT1	78	I	PTC I	112	I	PAD1
11	I	EXWR	45	I	PH0	79	I	XTCH	113	I	PAD0
12	I	EBER	46	I	PH1	80	I	XG I T	114	I	PXWR
13	I	EDER	47	I	PH2	81	I	XRSQ	115	I	PBER
14	I	ECAL	48	I	PH3	82	I	TD I R	116	I	PDER
15	I	TES4	49	I	PH4	83	I	XOTE	117	I	PCAL
16	I	EFRE	50	I	PH5	84	I	XPRG	118	I	PTCT
17	I	EBSR	51	I	PH6	85	I	CHAM	119	I	XF30
18	I	EFLD	52	I	PH7	86	I	XTSP	120	I	PBSR
19	I	EASD	53	I	TES0	87	I	192F	121	I	PFLD
20	I	EAPL	54	I	TES1	88	I	938C	122	I	PRDS
21	I	EAD4	55	I	TES2	89	I	144C	123	I	PAPL
22	I	EAD3	56	I	TES3	90	I	MAD8	124	I	PASD
23	I	EAD2	57	I	TEST	91	I	MAD7	125	I	XRCS
24	I	EAD1	58	I	XMRD	92	I	MAD6	126	I	PXD3
25	I	EAD0	59	I	XMWR	93	I	MAD5	127	I	PXD2
26	0	X440	60	I	TACH	94	I	MAD4	128	I	PXD1
27	0	X48K	61	0	EEBP	95	I	MAD3	129	I	PXD0
28	0	TLC0	62	0	EASS	96	I	MAD2	130	0	PTCO
29	0	TLC1	63	0	ES1	97	I	MAD1	131	0	CTLO
30	0	TLC2	64	0	ES2	98	I/O	MD0	132	0	XATM
31	0	TLC3	65	0	ES4	99	I/O	MD1	133	0	PEBP
32	0	TLC4	66	0	ETC0	100	I/O	MD2	134	0	PASS
33	0	TLC5	67	-	GND	101	I/O	MD3	135	-	GND
34	-	VDD	68	-	GND	102	-	VDD	136	-	GND



INPUT

144C : 1.44MHz CLOCK IN
 192F : 192Fs CLOCK IN
 938C : 9.38MHz CLOCK IN
 CHAM : CHASE MODE AUTO/ADRS
 EAD0-EAD4 : EXT TC ADDRESS DATA IN (0-4)
 EAPL : EXT TC ASSIGN DATA LATCH
 EASD : EXT TC ASSIGN FLAG IN
 EBER : EXT TC BI-PHASE MARK ERROR IN
 EBSR : EXT TC BSRL IN
 ECAL : EXT TC CAL FLAG IN
 EDER : EXT TC DATA ERROR IN
 EFLD : EXT TC FIELD SIGNAL IN
 EFRE : EXT TC DIRECTION IN
 ETCI : EXT TC IN
 EXD0 - EXD3 : EXT TC DATA IN (0-3)
 EXWR : EXT TC WRITE SIGNAL IN
 MAD1-MAD8 : CPU ADDRESS IN (1-8)
 PAD0-PAD4 : PB TC ADDRESS DATA IN (0-4)
 PAPL : PB TC ASSIGN DATA LATCH
 PASD : PB TC ASSIGN DATA IN
 PBER : PB TC BI-PHASH MARK ERROR IN
 PBSR : PB TC BSRL IN
 PCAL : PB TC CAL FLAG IN
 PDER : PB TC DATA ERROR IN
 PFLD : PB TC FIELD SIGNAL IN
 PH0 - PH7 : ROM DATA (0-7)
 PRDS : PB TC SYNC WORD IN
 PTCI : PB TC IN
 PTCT : PB TC TCT SIGNAL IN
 PXD0 - PXD3 : PB TC DATA IN (0-3)
 PXWR : PB TC WRITE SIGNAL IN
 TACH : TACH IN
 TDIR : TAPE DIRECTION IN
 TES0-TES4 : TEST (0-4)
 TEST : TEST
 VSNC : VIDEO SYNC IN
 XDTE : DUMMY TC OUT EN
 XEWR : EXT TC DATA WRITE PULSE
 XF30 : DUMMY TC 30
 XGIT : TC GEN INTERRUPT IN
 XGSN : GENERATOR SYNC IN
 XMRD : I/O READ
 XMWR : I/O WRITE
 XPRG : PB REGENE ON
 XRCS : TCR G.A. CHIP SELECT
 XRS0 : RESET
 XTCH : HEAD TOCH
 XTSP : PB SYNC PLAY ON

OUTPUT

CTLO : CTL CLOCK OUT
 DMTC : DUMMY TC OUT
 EASS : EXT TC ASSIGN FLAG OUT
 EEBP : EXT TC ERROR BYPASS SEL
 ES1-ES2 : EXT TC FORMAT SEL (1-2)
 ES4 : EXT TC FORMAT SEL4
 ESNC : EXT TC SYNC OUT
 ETCO : EXT TC OUT
 DMTC : DUMMY TC OUT
 FMT0 : FORMAT DATA (ROM ADDRESS C)
 FMT1 : FORMAT DATA (ROM ADDRESS D)
 PASS : PB TC ASSIGN FLAG OUT
 PEBP : PB TC ERROR BYPASS SEL
 PS1-PS2 : PB TC FORMAT SEL (1-2)
 PS4 : PB TC FORMAT SEL4
 PSNC : PB TC SYNC OUT
 PTCO : PB TC OUT
 TLC0-TLC9 : ROM ADDRESS DATA (0-9)
 TLCA : ROM ADDRESS DATA A
 TLCB : ROM ADDRESS DATA B
 X440 : ROM ADDRESS DATA E
 X48K : ROM ADDRESS DATA F
 XATM : PB TC READER MODE SEL
 XEWR : EXT TC DATA WRITE PULSE
 XPRW : PB TC WRITE PULSE
 XRD : READY OUT
 XRIT : TC READER INTERRUPT OUT

INPUT/OUTPUT

MD0-MD7 : CPU DATA I/O (0-7)