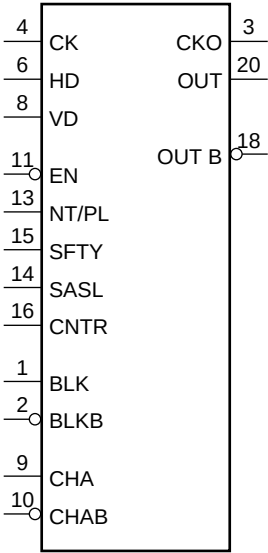
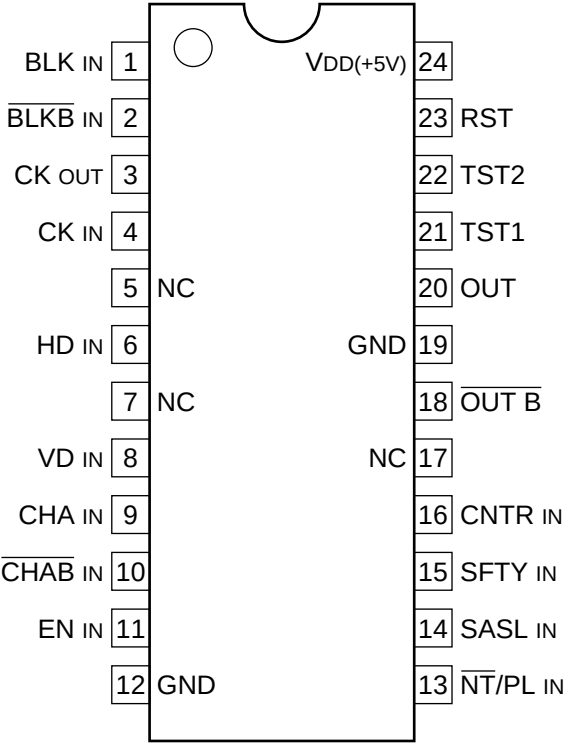

C-MOS GATE ARRAY
-TOP VIEW-



INPUT

BLK ; BLKG IN
BLKB ; BLKG IN
CHA ; CHR IN
CHAB ; CHR IN
CK ; CLOCK IN
CNTR ; CENTER ON/OFF
EN ; ENABLE IN
HD ; HORIZONTAL DRIVE IN
NT/PL ; NTSC/PAL
RST ; RESET
SASL ; SAFETY 80/90
SFTY ; SAFETY ON/OFF
VD ; VERTICAL DRIVE IN
TST1 ; TEST 1
TST2 ; TEST 2

OUTPUT

OUT ; OUTPUT
OUT B ; OUTPUT
CKO ; CLOCK OUT

