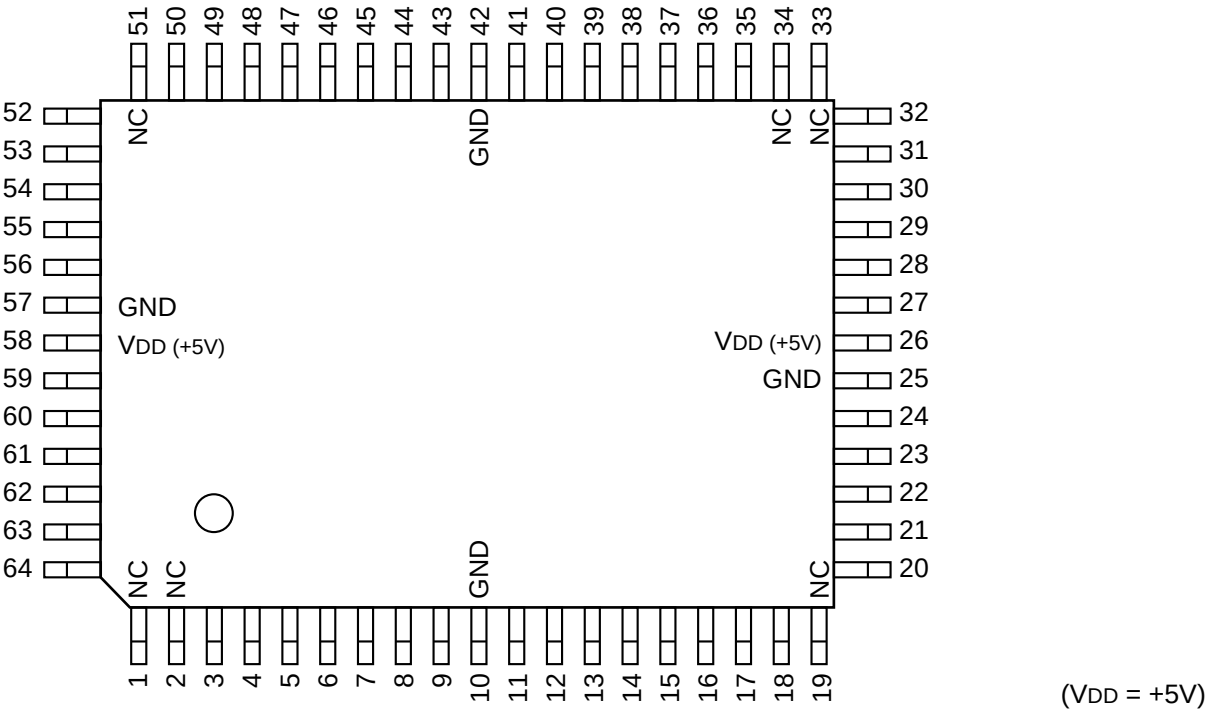
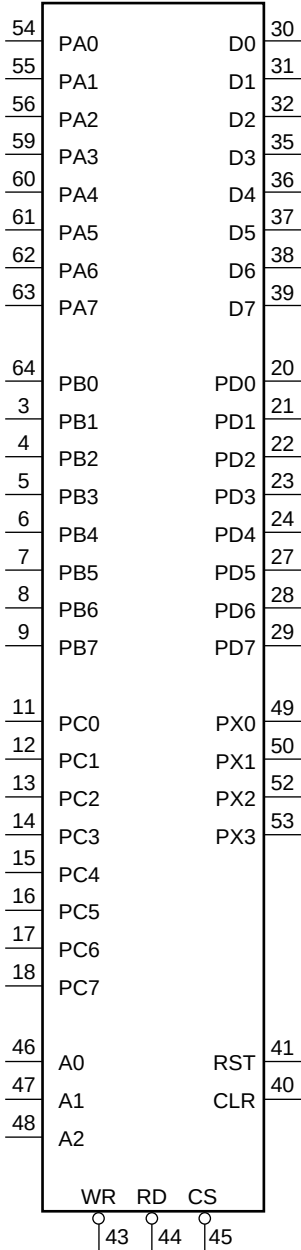


C-MOS I/O PORT EXPANDER
—TOP VIEW—



(VDD = +5V)											
PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	17	I/O	PC6	33	—	NC	49	I/O	PX0
2	—	NC	18	I/O	PC7	34	—	NC	50	I/O	PX1
3	I/O	PB1	19	—	NC	35	I/O	D3	51	—	NC
4	I/O	PB2	20	I/O	PD0	36	I/O	D4	52	I/O	PX2
5	I/O	PB3	21	I/O	PD1	37	I/O	D5	53	I/O	PX3
6	I/O	PB4	22	I/O	PD2	38	I/O	D6	54	I/O	PA0
7	I/O	PB5	23	I/O	PD3	39	I/O	D7	55	I/O	PA1
8	I/O	PB6	24	I/O	PD4	40	I	CLR	56	I/O	PA2
9	I/O	PB7	25	—	GND	41	I	RST	57	—	GND
10	—	GND	26	—	VDD	42	—	GND	58	—	VDD
11	I/O	PC0	27	I/O	PD5	43	I	WR	59	I/O	PA3
12	I/O	PC1	28	I/O	PD6	44	I	RD	60	I/O	PA4
13	I/O	PC2	29	I/O	PD7	45	I	CS	61	I/O	PA5
14	I/O	PC3	30	I/O	D0	46	I/O	A0	62	I/O	PA6
15	I/O	PC4	31	I/O	D1	47	I/O	A1	63	I/O	PA7
16	I/O	PC5	32	I/O	D2	48	I/O	A2	64	I/O	PB0



INPUT

A0-A2 ; ADDRESS
CLR ; CLEAR
CS ; CHIP SELECT
RD ; READ STROBE
RST ; RESET
WR ; WRITE ENABLE

INPUT/OUTPUT

D0-D7 ; DATA
PA0-PA7 ; PORT A
PB0-PB7 ; PORT B
PC0-PC7 ; PORT C
PD0-PD7 ; PORT D
PX0-PX3 ; PORT X