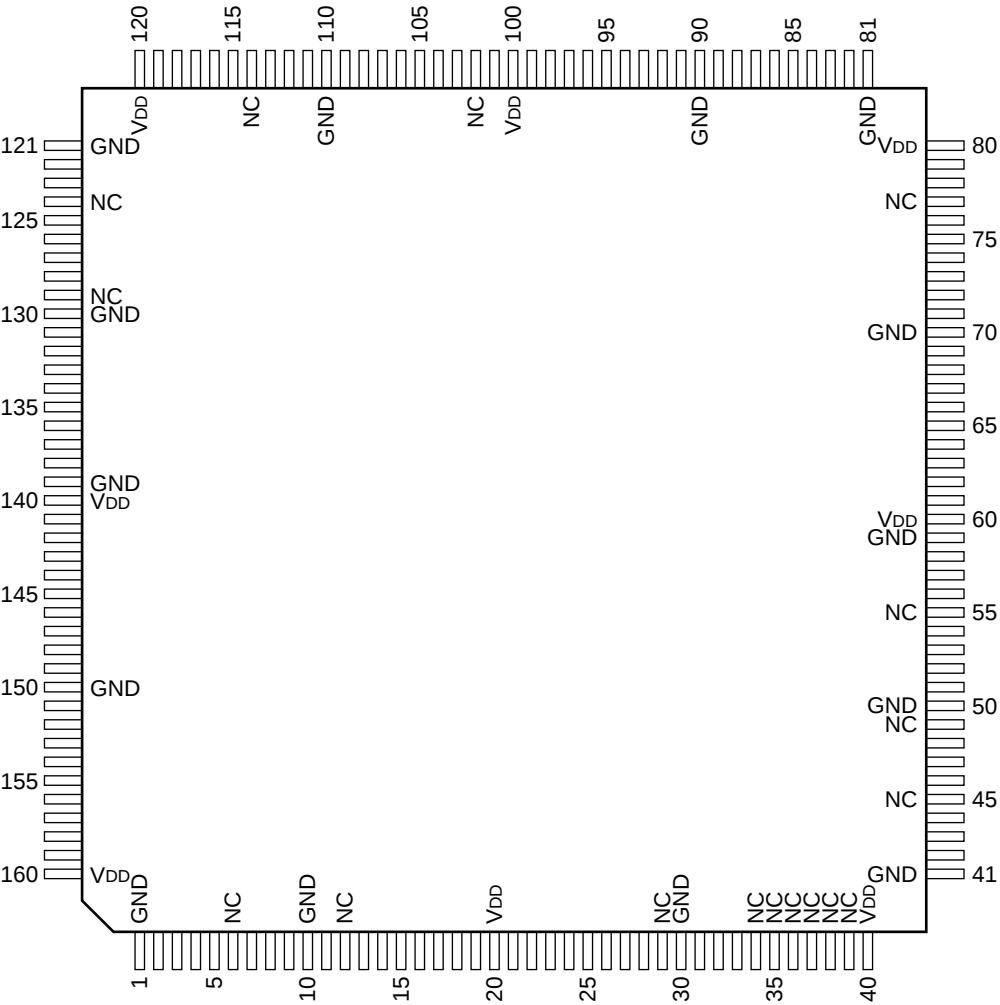


C-MOS NETWORK INTERFACE

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	41	—	GND	81	—	GND	121	—	GND
2	I	SYSCK	42	O	SCK	82	I/O	ILDT0	122	O	AD1EN
3	I/O	SYSDT	43	O	XFSO	83	I/O	ILDT1	123	O	AD2EN
4	I	SYSXRS	44	O	XHSO	84	I/O	ILDT2	124	—	NC
5	I	SYSXWE	45	—	NC	85	I/O	ILDT3	125	I	PL1CK
6	—	NC	46	O	A0	86	I/O	ILDT4	126	O	PL1DT
7	O	CARDY0	47	O	A1	87	I/O	ILDT5	127	O	PL1EN
8	O	CARDY1	48	O	A2	88	I/O	ILDT6	128	O	PL1A
9	O	CARDY2	49	—	NC	89	I/O	ILDT7	129	—	NC
10	—	GND	50	—	GND	90	—	GND	130	—	GND
11	O	CARDY3	51	O	SFRM	91	I	DCK	131	I	PL2CK
12	—	NC	52	O	DTCT	92	I	EXCKEN	132	O	PL2DT
13	I	CAAD0	53	O	MUTE	93	I	DIN	133	O	PL2EN
14	I	CAAD1	54	O	SCR	94	I	XRS	134	O	PL2A
15	I	CAAD2	55	—	NC	95	O	AX1CK	135	O	TPXST
16	I	CAAD3	56	O	RSERR0	96	O	AX1DT	136	O	AD1XRS
17	I	CAAD4	57	O	RSERR1	97	O	AX1SY	137	I	AD1RCK
18	I	CAAD5	58	O	RSERR2	98	O	AX2CK	138	O	AD1XRE
19	I	CAAD6	59	—	GND	99	O	AX2DT	139	—	GND
20	—	VDD	60	—	VDD	100	—	VDD	140	—	VDD
21	I/O	CAOUT0	61	O	ILAD0	101	O	AX2SY	141	O	AD1XWE
22	I/O	CAOUT1	62	O	ILAD1	102	—	NC	142	I	AD1XFF
23	I/O	CAOUT2	63	O	ILAD2	103	O	PKEN	143	I	AD1XHF
24	I/O	CAOUT3	64	O	ILAD3	104	O	PERR	144	I	AD1XEF
25	I/O	CAOUT4	65	O	ILAD4	105	O	POUT0	145	O	AD1XST
26	I/O	CAOUT5	66	O	ILAD5	106	O	POUT1	146	I	AD1XOK
27	I/O	CAOUT6	67	O	ILAD6	107	O	POUT2	147	O	AD1XER
28	I/O	CAOUT7	68	O	ILAD7	108	O	POUT3	148	O	TPDT
29	—	NC	69	O	ILAD8	109	O	POUT4	149	O	AD2XRS
30	—	GND	70	—	GND	110	—	GND	150	—	GND
31	I	CAXCE	71	O	ILAD9	111	O	POUT5	151	I	AD2RCK
32	I	CAXRE	72	O	ILAD10	112	O	POUT6	152	O	AD2XRE
33	I	CAXWE	73	O	ILAD11	113	O	POUT7	153	O	AD2XWE
34	—	NC	74	O	ILAD12	114	—	NC	154	I	AD2XFF
35	—	NC	75	O	ILAD13	115	O	VEN	155	I	AD2XHF
36	—	NC	76	O	ILAD14	116	O	VERR	156	I	AD2XEF
37	—	NC	77	—	NC	117	O	VCK	157	O	AD2XST
38	—	NC	78	O	ILXRE	118	O	DEN	158	I	AD2XOK
39	—	NC	79	O	ILXWE	119	O	HDEN	159	O	AD2XER
40	—	VDD	80	—	VDD	120	—	VDD	160	—	VDD

INPUT

AD1RCK, AD2RCK : AUDIO FIFO CLOCK
 AD1XEF, AD2XEF : AUDIO FIFO EMPTY FLAG
 AD1XFF, AD2XFF : AUDIO FIFO FULL FLAG
 AD1XHF, AD2XHF : AUDIO FIFO HALF FLAG
 AD1XOK, AD2XOK : AUDIO FIFO DATA OUTPUT ENABLE
 CAAD0 - CAAD6 : ADDRESS
 CAXCE : DATA ENABLE FLAG
 CAXRE : READ ENABLE FLAG
 CAXWE : WRITE ENABLE FLAG
 DCK : DATA TRANSFER CLOCK
 DIN : TRANSFER DATA
 EXCKEN : INTERNAL CLOCK FREQUENCY SELECT
 PL1CK : VIDEO DPLL CLOCK
 PL2CK : AUDIO DPLL CLOCK
 SYSCK : SYSTEM CLOCK
 SYSXRS : READ START FLAG
 SYSXWE : WRITE ENABLE FLAG
 XRS : RESET

OUTPUT

A0 - A2 : MASTER COUNTER
 AD1EN, AD2EN : AUDIO ENABLE
 AD1XER, AD2XER : AUDIO ERROR FLAG
 AD1XRE, AD2XRE : AUDIO FIFO READ ENABLE
 AD1XRS, AD2XRS : AUDIO FIFO RESET
 AD1XST, AD2XST : AUDIO FIFO OUTPUT DATA STROBE FLAG
 AD1XWE, AD2XWE : AUDIO FIFO WRITE ENABLE
 AX1CK : AUX1 DATA CLOCK
 AX1DT : AUX1 DATA
 AX1SY : AUX1 SYNC FLAG
 AX2CK : AUX2 DATA CLOCK
 AX2DT : AUX2 DATA
 AX2SY : AUX2 SYNC FLAG
 CARDY0 - CARDY3 : DECODED C.A. PACKET IDENTIFICATION FLAG
 DEN : EXTRA DATA ENABLE FLAG
 DTCT : NO-SYNC DETECT FLAG
 HDEN : PACKET HEADER ENABLE FLAG
 ILAD0 - ILAD14 : RAM ADDRESS FOR INTERLEAVING
 ILXRE : READ ENABLE FLAG FOR INTERLEAVING
 ILXWE : WRITE ENABLE FLAG FOR INTERLEAVING
 MUTE : AUDIO SYSTEM MUTING FLAG
 PERR : PACKET ERROR FLAG
 PKEN : PACKET ENABLE FLAG
 PL1A : VIDEO DPLL CHARGE PUMP
 PL1DT : VIDEO DPLL SERIAL DATA
 PL1EN : VIDEO DPLL DATA ENABLE
 PL2A : AUDIO DPLL CHARGE PUMP
 PL2DT : AUDIO DPLL SERIAL DATA
 PL2EN : AUDIO DPLL DATA ENABLE
 POUT1 - POUT7 : PACKET DATA
 RSERR0 - RSERR2 : REED SOLOMON ERROR FLAG
 SCK : SYMBOL CLOCK
 SCR : SCRAMBLE FLAG
 SFRM : SUPER FRAME
 TPD : TEST DATA
 TPXST : TEST START FLAG
 VCK : DIVIDED VIDEO CLOCK
 VEN : VIDEO ENABLE
 VERR : VIDEO ERROR FLAG
 XFSO : FRAME SYNC SIGNAL
 XHSO : HORIZONTAL SYNC SIGNAL

INPUT/OUTPUT

CAOUT0 - CAOUT7 : DATA
 ILDT0 - ILDT7 : DATA FOR INTERLEAVING RAM
 SYSDT : SERIAL DATA

