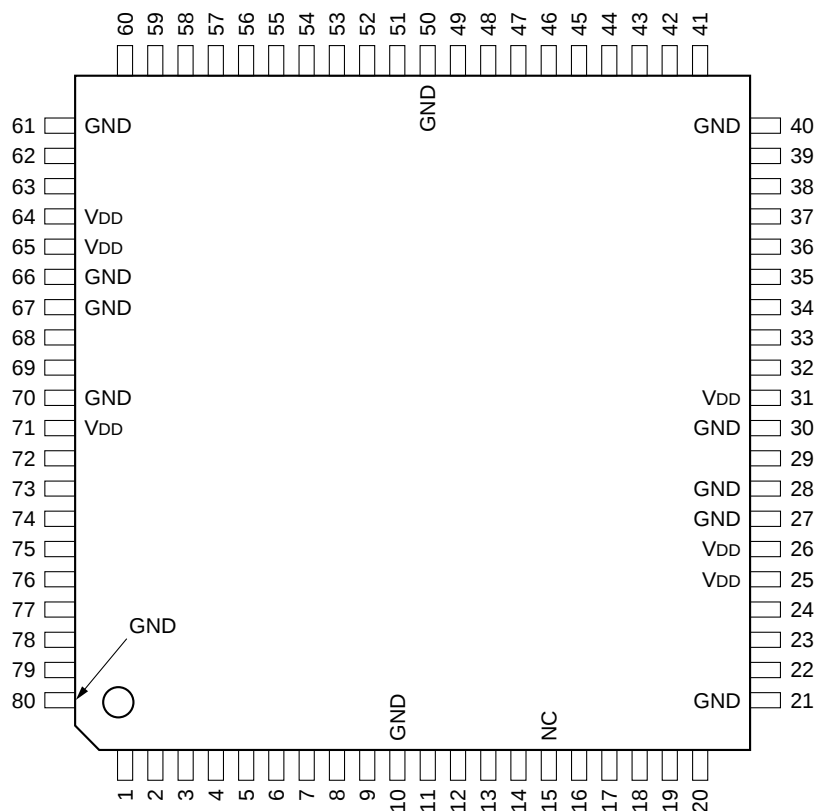


C-MOS JOG/SHUTTLE CONTROLLER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	RESET	21	—	GND	41	I/O	CPUBUS7	61	—	GND
2	I	OSEL4	22	I	CAPTJS	42	I/O	CPUBUS6	62	I	CAPTA
3	I	OSEL3	23	I	JSCLR	43	I/O	CPUBUS5	63	I	CAPTB
4	I	OSEL2	24	I	CAPTIO	44	I/O	CPUBUS4	64	—	VDD
5	I	OSEL1	25	—	VDD	45	I/O	CPUBUS3	65	—	VDD
6	I	OSEL0	26	—	VDD	46	I/O	CPUBUS2	66	—	GND
7	O	CDIRO	27	—	GND	47	I/O	CPUBUS1	67	—	GND
8	I	CDIRI	28	—	GND	48	I/O	CPUBUS0	68	I	MDTES
9	O	CFG4	29	I	CK	49	I	CS	69	O	CFP
10	—	GND	30	—	GND	50	—	GND	70	—	GND
11	I	CFG4	31	—	VDD	51	I/O	IO7	71	—	VDD
12	I	CFG3	32	I	SRFGA	52	I/O	IO6	72	I	PBCTL
13	I	CFG2	33	I	SRFGB	53	I/O	IO5	73	I	CPUCTL
14	I	CFG1	34	O	SRDIRO	54	I/O	IO4	74	I	CTLRST
15	—	NC	35	I	TEST FG4	55	I/O	IO3	75	O	DIVCTL
16	—	TCK	36	I	TEST DIR	56	I/O	IO2	76	O	EDITCTL
17	—	TDI	37	I	TRFGA	57	I/O	IO1	77	I	CTLSEL2
18	—	TENA1	38	I	TRFGB	58	I/O	IO0	78	I	CTLSEL1
19	—	TDO	39	O	TRDIRO	59	I	IOSEL	79	I	CTLSEL0
20	—	VST	40	—	GND	60	I	H PAL	80	—	GND