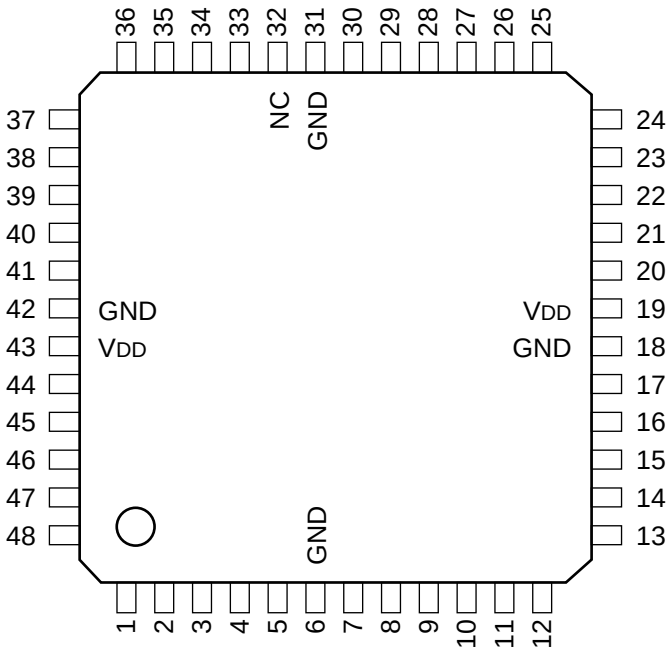
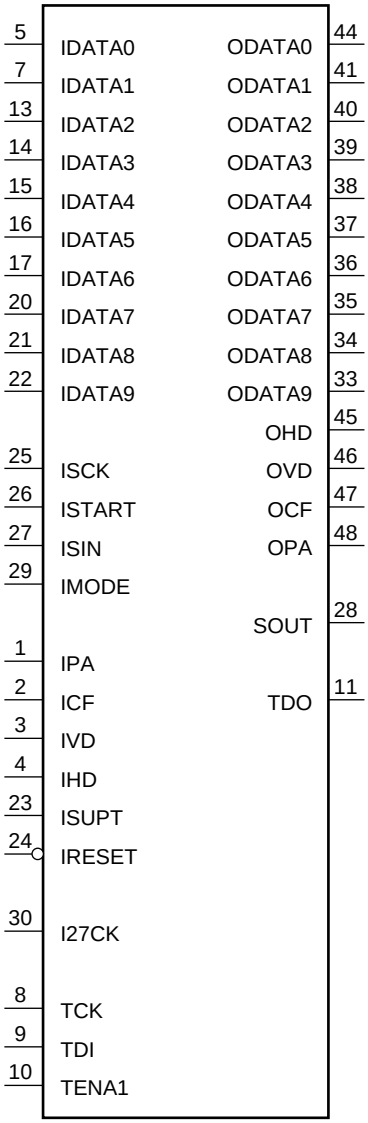
C-MOS SETUP ADD /REMOVE

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	IPA	13	I	IDATA2	25	I	ISCK	37	O	ODATA5
2	I	ICF	14	I	IDATA3	26	I	ISTART	38	O	ODATA4
3	I	IVD	15	I	IDATA4	27	I	ISIN	39	O	ODATA3
4	I	IHD	16	I	IDATA5	28	O	SOUT	40	O	ODATA2
5	I	IDATA0	17	I	IDATA6	29	I	IMODE	41	O	ODATA1
6	—	GND	18	—	GND	30	I	I27CK	42	—	GND
7	I	IDATA1	19	—	VDD	31	—	GND	43	—	VDD
8	I	TCK	20	I	IDATA7	32	—	NC	44	O	ODATA0
9	I	TDI	21	I	IDATA8	33	O	ODATA9	45	O	OHD
10	I	TENA1	22	I	IDATA9	34	O	ODATA8	46	O	OVD
11	O	TDO	23	I	ISUPT	35	O	ODATA7	47	O	OCF
12	—	VST	24	I	$\overline{\text{IRESET}}$	36	O	ODATA6	48	O	OPA

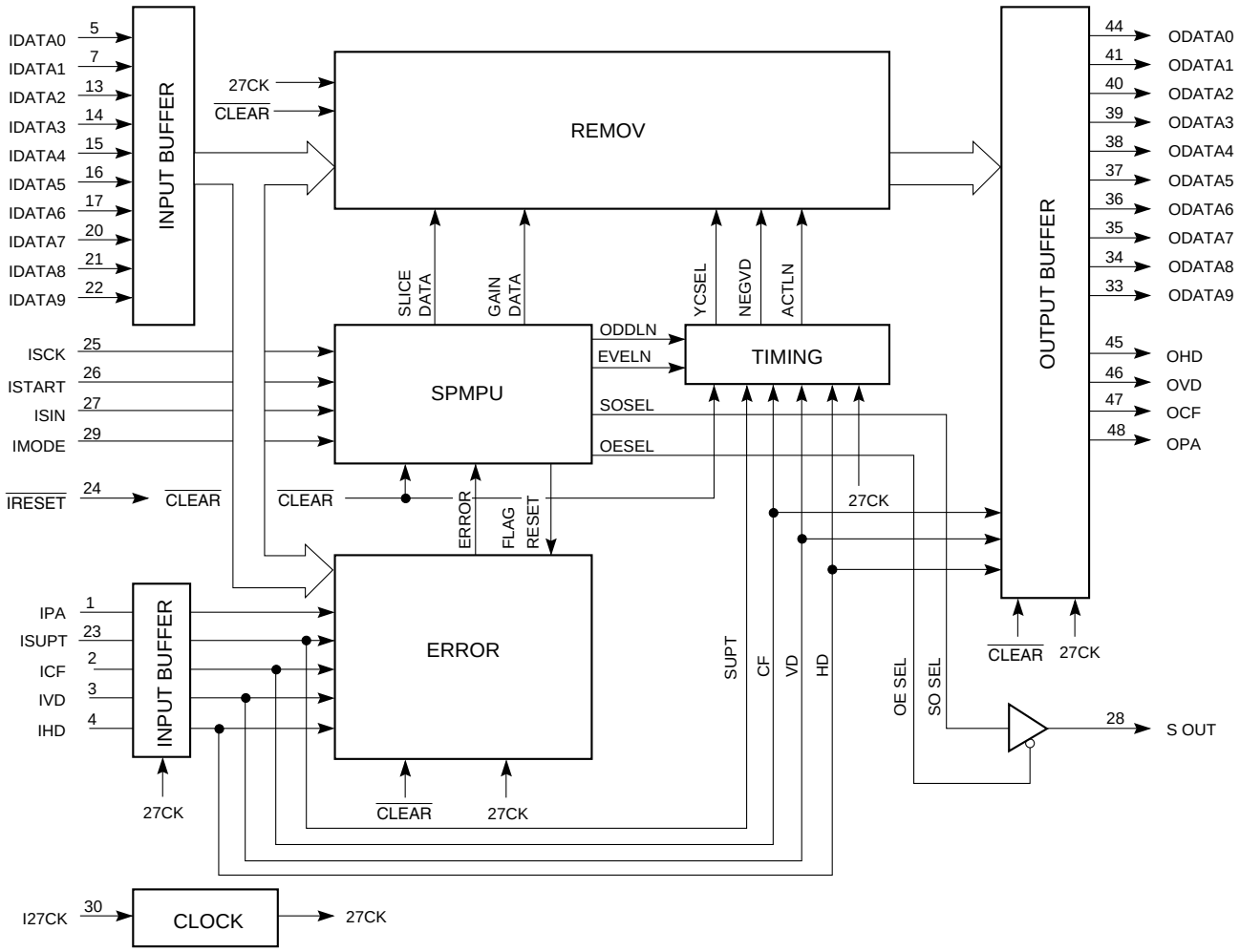


INPUT

I27CK : SYSTEM CLOCK (27 MHz)
ICF : COLOR FRAMING DATA
IDATA0 - IDATA9
: 4 : 2 : 2 PARALLEL DATA
IHD : HD
IMODE : SERIAL COMMUNICATION FORMAT SELECT
(1 : ANALOG TBC PLAYBACK COMMUNICATION FORMAT,
0 : DIGITAL β CAM RECORD COMMUNICATION FORMAT)
IPA : PARITIY DATA
IRESET : SYSTEM RESET
ISCK : SERIAL COMMUNICATION CLOCK
ISIN : SERIAL COMMUNICATION DATA
ISTART : SERIAL COMMUNICATION START PULSE
ISURT : HALF H BLANKING TIMING PULSE
IVD : VD
TCK : SMART CHECK TERMINAL
TDI : SMART CHECK TERMINAL
TENA1 : SMART CHECK TERMINAL

OUTPUT

OCF : COLOR FRAMING DATA
ODATA0 - ODATA9
: 4 : 2 : 2 PARALLEL DATA
OHD : HD
OPA : PARITY DATA
OVD : VD
SOUT : SERIAL COMMUNICATION DATA
TDO : SMART CHECK TERMINAL


IDATA0
IDATA1
IDATA2
IDATA3
IDATA4
IDATA5
IDATA6
IDATA7
IDATA8
IDATA9
INPUT BUFFER
27CK
CLEAR
REMOV
OUTPUT BUFFER
ODATA0
ODATA1
ODATA2
ODATA3
ODATA4
ODATA5
ODATA6
ODATA7
ODATA8
ODATA9
SLICE DATA
GAIN DATA
YCSEL
NEGVD
ACTLN
ISCK
ISTART
ISIN
IMODE
SPMPU
ODDLN
EVELN
SOSEL
OESEL
TIMING
OHD
OVD
OCF
OPA
IRESET
CLEAR
ERROR
FLAG
RESET
IPA
ISUPT
ICF
IVD
IHD
ERROR
SUPT
CF
VD
HD
OE SEL
SO SEL
S OUT
I27CK
CLOCK