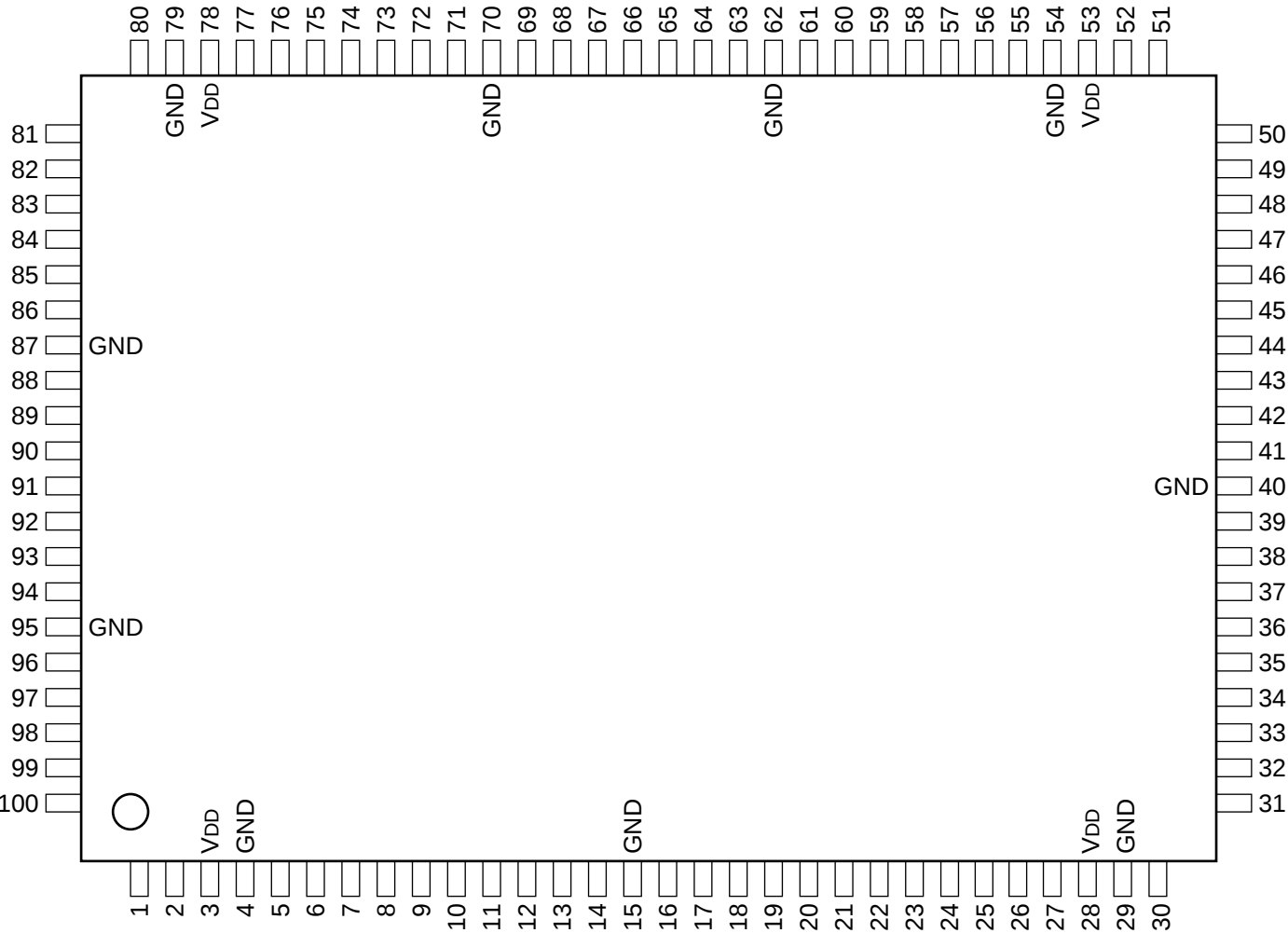


C-MOS DIGITAL AUDIO PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	EA9	26	I	SIB	51	I/O	ED17	76	I/O	ED12
2	O	EC0	27	I	SIC	52	I/O	ED2	77	I/O	ED28
3	—	VDD	28	—	VDD	53	—	VDD	78	—	VDD
4	—	GND	29	—	GND	54	—	GND	79	—	GND
5	O	EC1	30	O	SOA	55	I/O	ED18	80	I/O	ED13
6	O	EC2	31	O	SOB	56	I/O	ED3	81	I/O	ED29
7	O	EA13	32	O	SOC	57	I/O	ED19	82	I/O	ED14
8	O	EA14	33	I/O	ECJ0	58	I/O	ED4	83	I/O	ED30
9	O	EA15	34	I/O	ECJ1	59	I/O	ED20	84	I/O	ED15
10	I	TST	35	I/O	ECJ2	60	I/O	ED5	85	I/O	ED31
11	I	TDR	36	I/O	ECJ3	61	I/O	ED21	86	O	XOE
12	O	BFOT	37	O	REDY	62	—	GND	87	—	GND
13	O	CLKO	38	O	TRDT	63	I/O	ED6	88	O	CAS
14	I	CLKI	39	I	XLAT	64	I/O	ED22	89	O	XWE
15	—	GND	40	—	GND	65	I/O	ED7	90	O	RAS
16	I	TS0	41	I	SCK	66	I/O	ED23	91	O	EA0
17	I	TS1	42	I	RVDT	67	I/O	ED8	92	O	EA1
18	I	TSA	43	O	SCL0	68	I/O	ED24	93	O	EA2
19	I	LRK0	44	O	SCL1	69	I/O	ED9	94	O	EA3
20	I	LRK1	45	O	LR0F	70	—	GND	95	—	GND
21	I	BCK0	46	O	LR1F	71	I/O	ED25	96	O	EA4
22	I	BCK1	47	I	XRST	72	I/O	ED10	97	O	EA5
23	O	BCT	48	I/O	ED0	73	I/O	ED26	98	O	EA6
24	I/O	LCT	49	I/O	ED16	74	I/O	ED11	99	O	EA7
25	I	SIA	50	I/O	ED1	75	I/O	ED27	100	O	EA8

INPUT

BCK0, BCK1	; BIT CLOCK
CLKI	; CRYSTAL OSCILLATOR
LRK0, LRK1	; LR CLOCK
RVDT	; SERIAL DATA
SCK	; SERIAL DATA TIMING CLOCK
SIA, SIB, SIC	; SERIAL AUDIO DATA
TDR	; TEST
TS0, TS1	; TEST
TSA	; TEST
TST	; TEST
XLAT	; SERIAL DATA MODE IDENTIFICATION SIGNAL
XRST	; RESET

OUTPUT

BCT	; DIVIDE BIT CLOCK
BFOT	; MASTER CLOCK BUFFER
CAS	; CAS SIGNAL FOR EXTERNAL DRAM
CLKO	; CRYSTAL OSCILLATOR
EA0 - EA9	; EXTERNAL RAM ADDRESS
EA13- EA15	; EXTERNAL RAM ADDRESS
EC0, EC1, EC2	; EXTERNAL RAM ADDRESS
LR0F, LR1F	; TEST
RAS	; RAS SIGNAL FOR EXTERNAL DRAM/ CE SIGNAL FOR EXTERNAL SRAM
REDY	; READY
SCL0, SCL1	; TEST
SOA, SOB, SOC	; SERIAL AUDIO
TRDT	; SERIAL DATA OUTPUT
XOE	; OUTPUT ENABLE FOR EXTERNAL RAM
XWE	; WRITE ENABLE FOR EXTERNAL RAM

INPUT/OUTPUT

ECJ0 - ECJ3	; JUMP INPUT/TEST DATA OUTPUT
ED0 - ED31	; EXTERNAL RAM DATA
LCT	; COUNTER INPUT/DIVIDED LRCK OUTPUT

