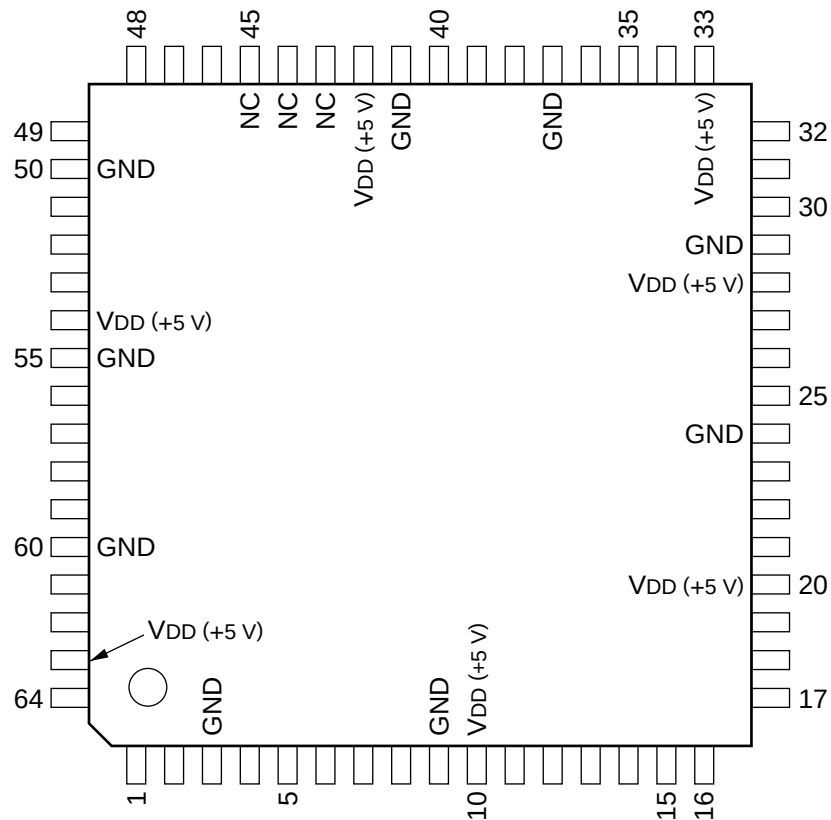

C-MOS TIMING GENERATOR FOR PROGRESSIVE SCAN READOUT SYSTEM CCD MAGE SENSOR

—TOP VIEW—



(VDD = +5 V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	OSCO	17	—	CH	33	—	VDD	49	—	TEST8
2	I	OSCI	18	O	XSG	34	O	XCPOB	50	—	GND
3	—	GND	19	O	XSUB	35	O	XCPDM	51	O	CL
4	I	INT	20	—	VDD	36	O	PBLK	52	O	CLD
5	I	TEST1	21	O	XV3	37	—	GND	53	O	CKO
6	I	STRB	22	O	XV2	38	—	TEST5	54	—	VDD
7	I	DCLK	23	O	XV1	39	—	TEST6	55	—	GND
8	I	DATA	24	—	GND	40	—	TEST7	56	I	XGRST
9	—	GND	25	O	XH1	41	—	GND	57	I	STDBY
10	—	VDD	26	O	XH2	42	—	VDD	58	I	TRIG
11	I	RM	27	O	RG	43	—	NC	59	I	ESG
12	I	TEST2	28	—	VDD	44	—	NC	60	—	GND
13	I	FSE	29	—	GND	45	—	NC	61	I	HD
14	I	SMDE	30	O	XSHP	46	O	ID	62	I	VD
15	I	TEST3	31	O	XSHD	47	O	WEN	63	—	VDD
16	I	TEST4	32	O	XRS	48	O	BUSY	64	I	CKI

INPUT

CKI	; CLOCK
DATA	; SHUTTER SPEED SETTING
DCLK	; SHUTTER SPEED SETTING
ESG	; EXTERNAL READ
FSE	; EXTERNAL TRIGGER SWEEP THROW AWAY SELECT (H: HIGH SPEED SWEEP THROW AWAY EFFECTIVE/ L: HIGH SPEED SWEEP THROW AWAY INVALID)
HD	; HORIZONTAL SYNC
OSCI	; OSCILLATOR INVERTER
INT	; INTERNAL OSCILLATOR SELECT (H: INTERNAL OSCILLATOR CELL/L: CKI INPUT EFFECTIVE)
RM	; FRAME RATE SELECT (H: NORMAL READ/L: TWICE SPEED READ)
SMDE	; READ TIMING SELECT (H: ESG SETTING INVALID/L: ESG INPUT EFFECTIVE)
STDBY	; STANDBY (H: NORMAL/L: INTERNAL CLOCK SUPPLY STOP)
STRB	; SHUTTER SPEED SETTING
TEST1 - TEST8	; TEST
TRIG	; EXTERNAL TRIGGER
VD	; VERTICAL SYNC
XGRST	; INTERNAL FF RESET (L: RESET)

OUTPUT

CKO	; 3232 fH CLOCK
CL	; 1616 fH CLOCK
CLD	; AD CONVERSION PULSE
BUSY	; TRIGGER MODE FLAG
ID	; LINE DETECT
OSCO	; OSCILLATOR INVERTER
PBLK	; BLANKING CLEANING PULSE
RG	; RESET GATE PULSE
WEN	; WRITE ENABLE
XCPDM	; CLAMP PULSE
XCPOB	; CLAMP PULSE
XH1, XH2	; CCD HORIZONTAL DRIVE CLOCK
XRS	; SAMPLE HOLD PULSE
XSG	; CCD SENSOR ELECTRIC READ OUT PULSE
XSHD	; SAMPLE HOLD PULSE
XSHP	; SAMPLE HOLD PULSE
XSUB	; CCD ELECTRIC SWEEP THROW AWAY PULSE
XV1 - XV3	; CCD VERTICAL DRIVE CLOCK

